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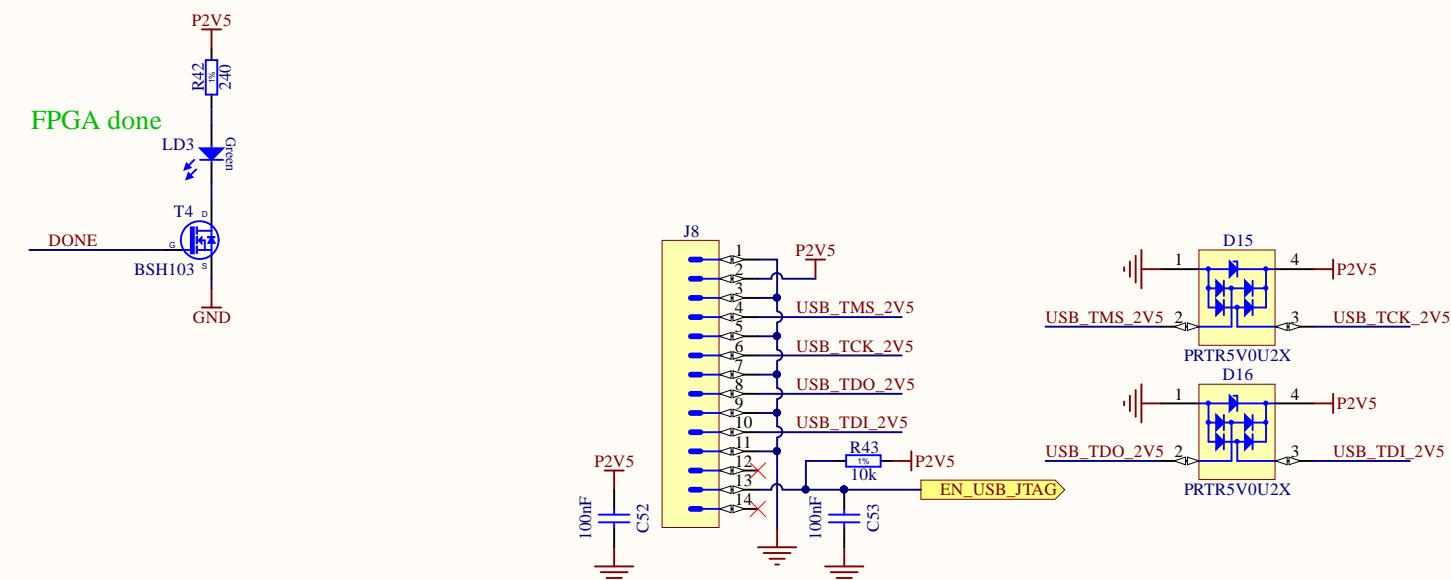
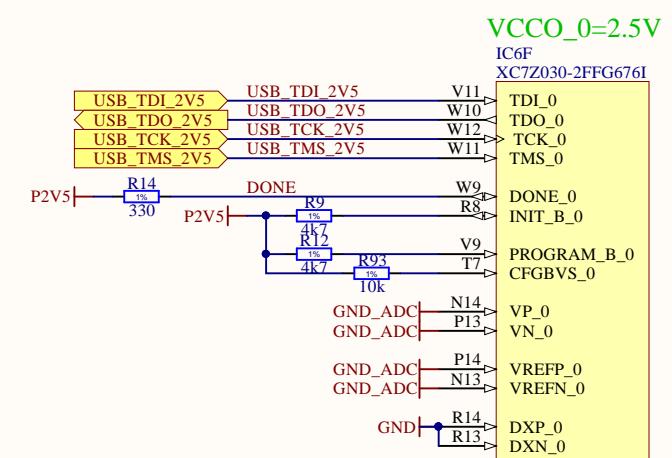
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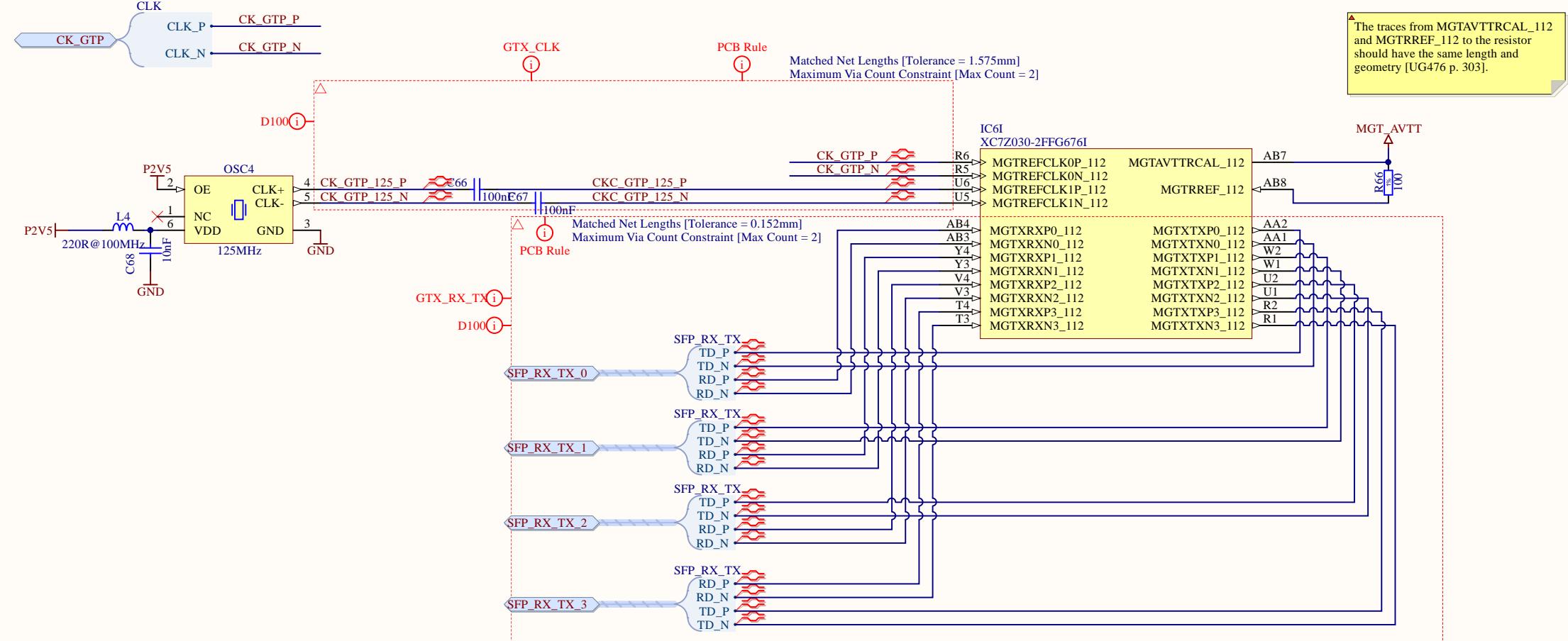
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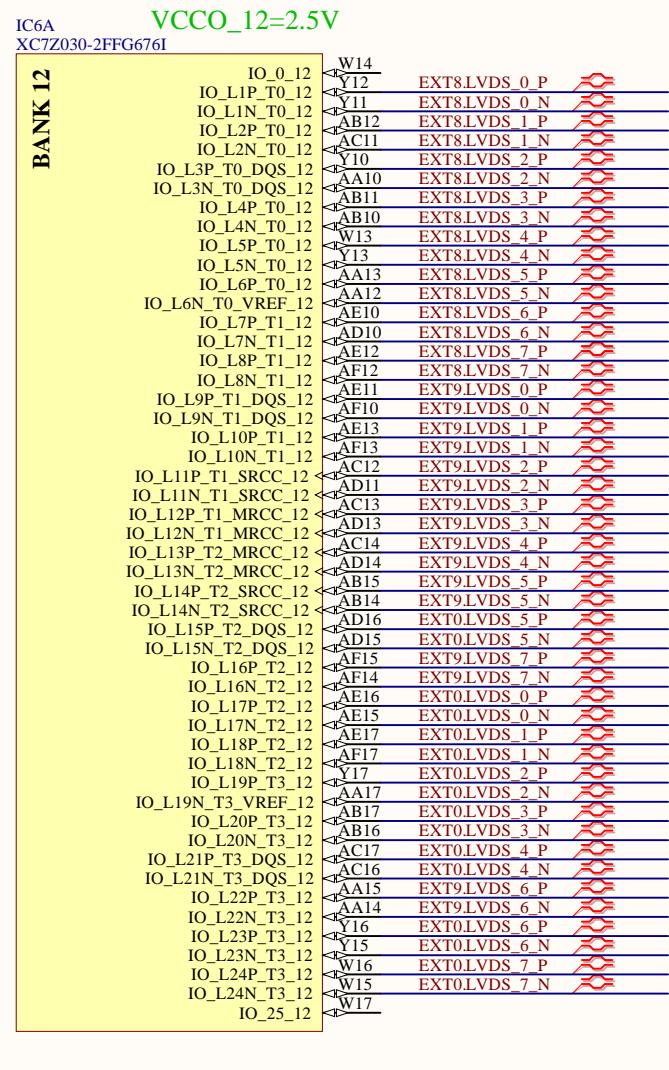
Bank 0 configuration



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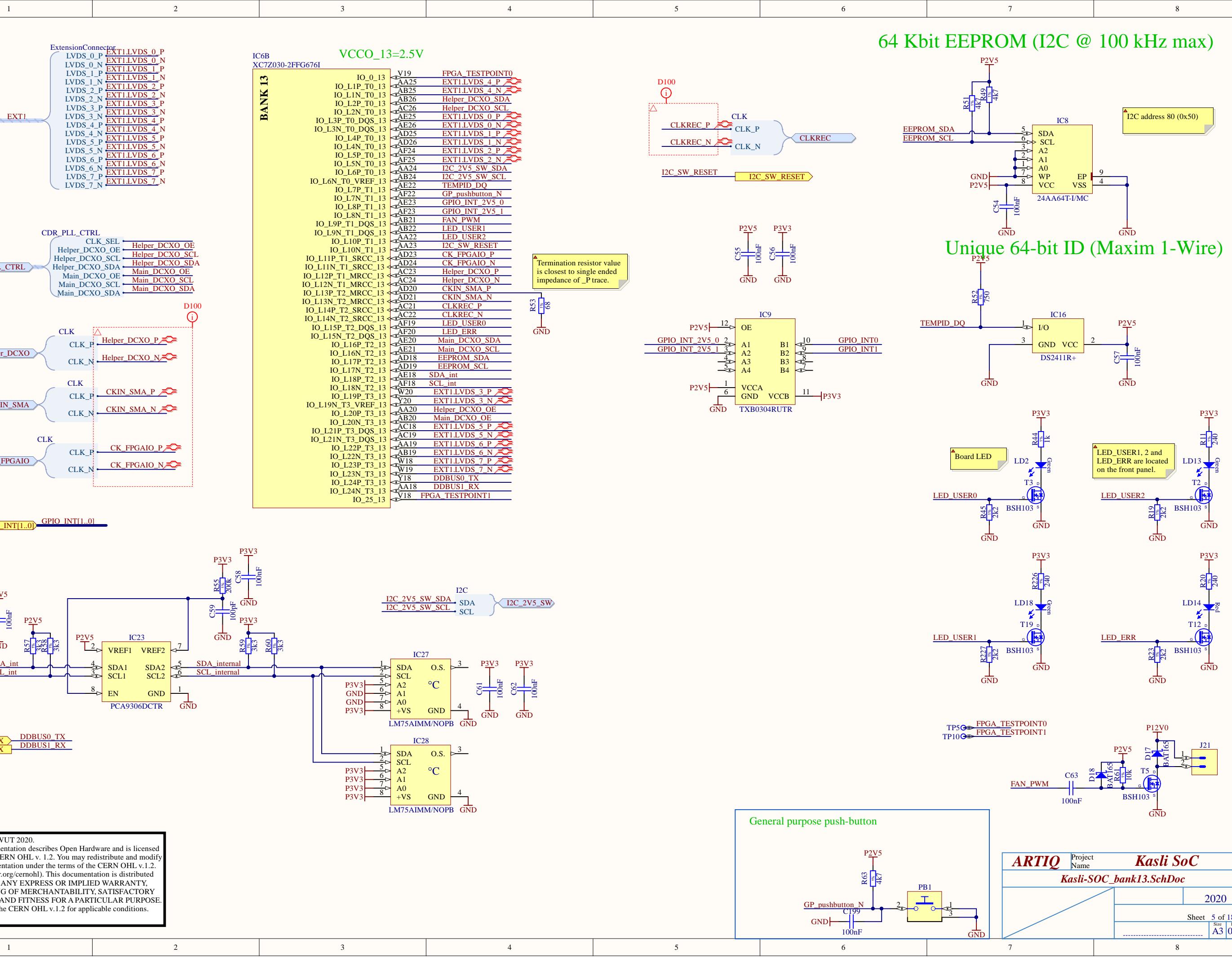
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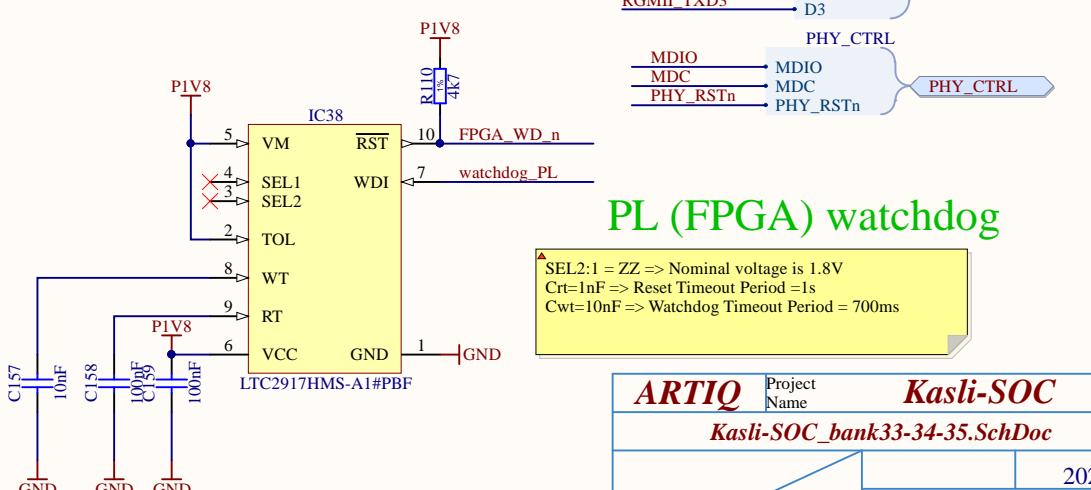
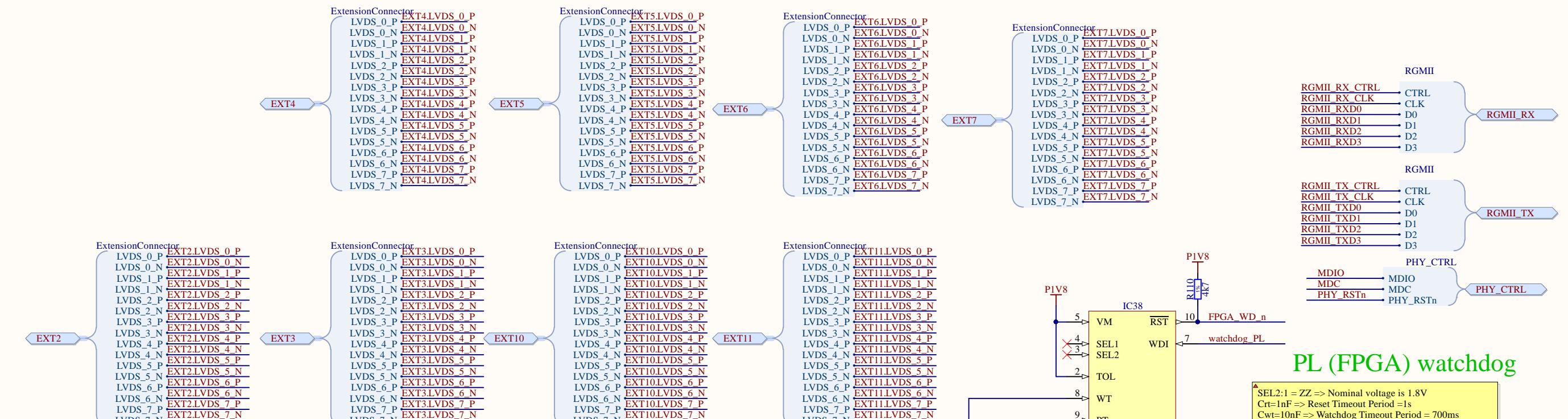
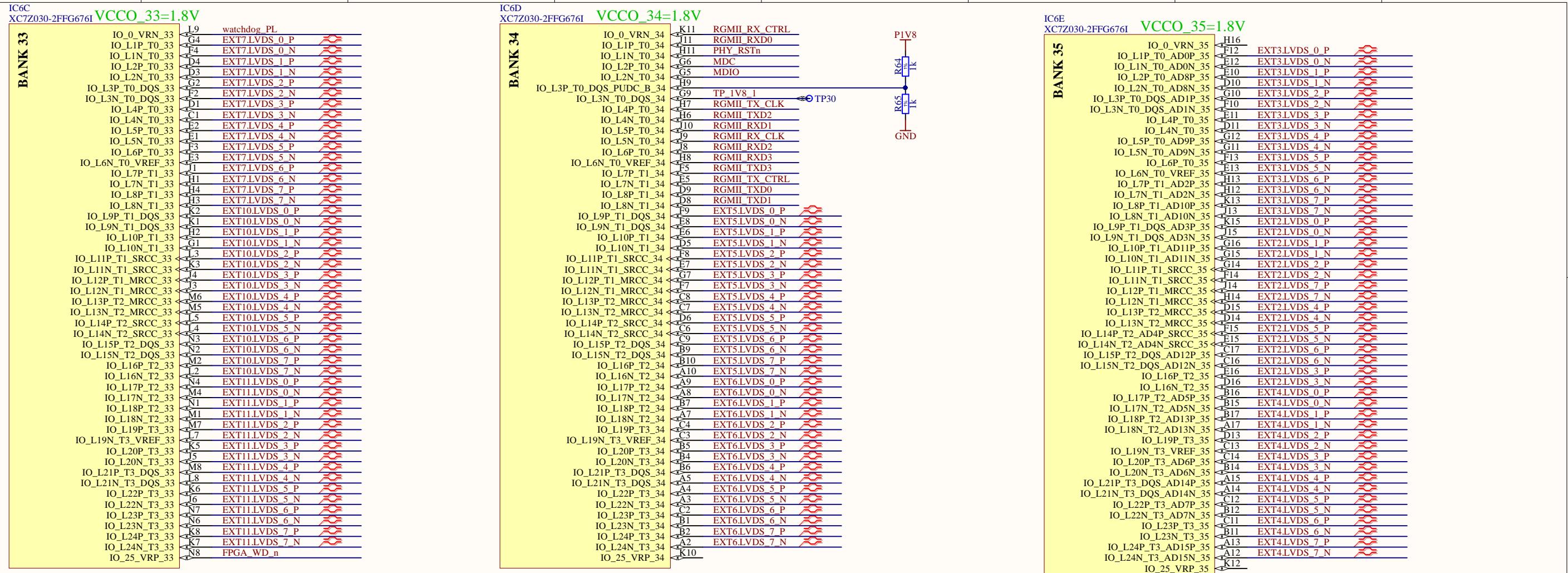
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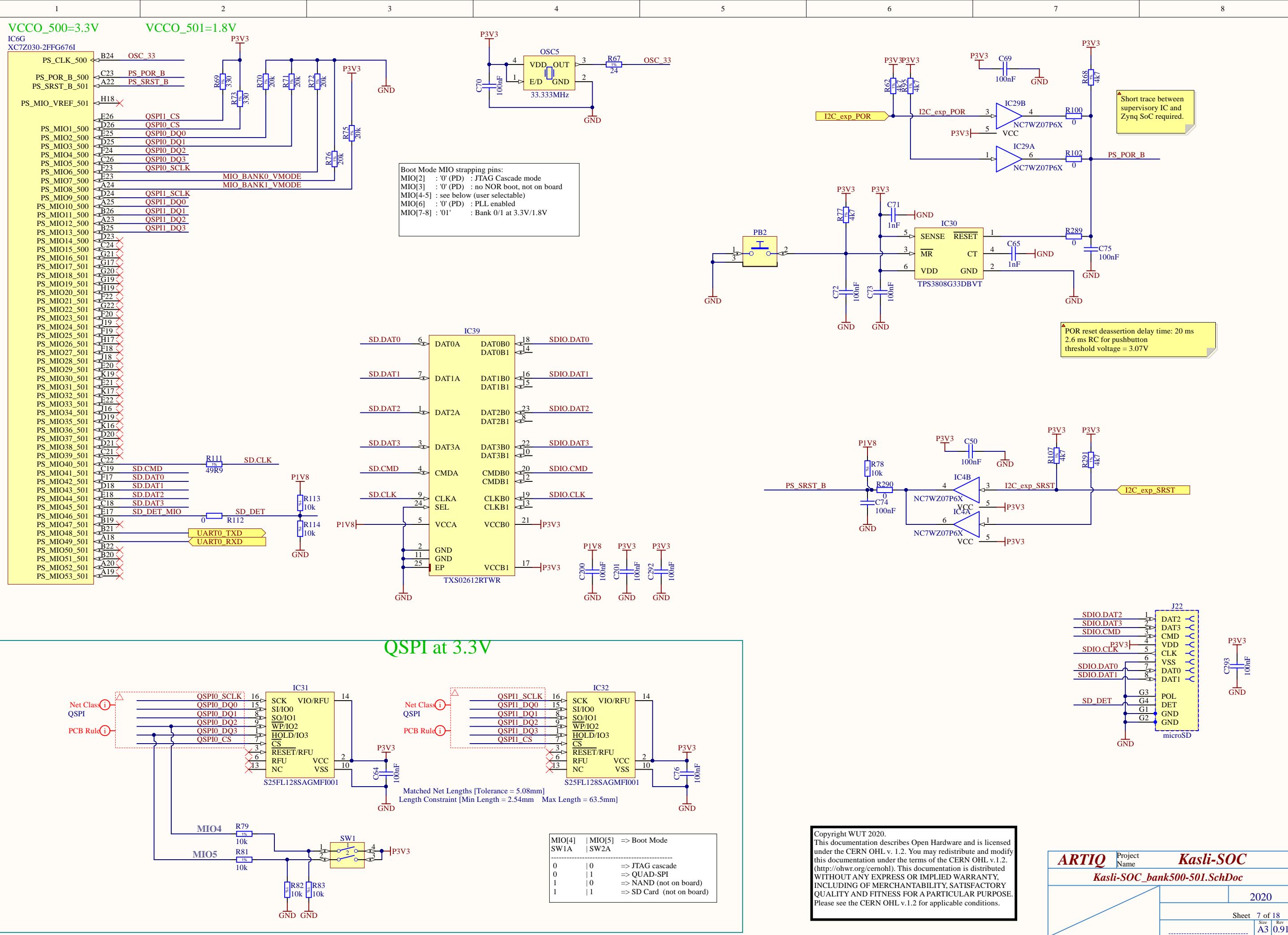
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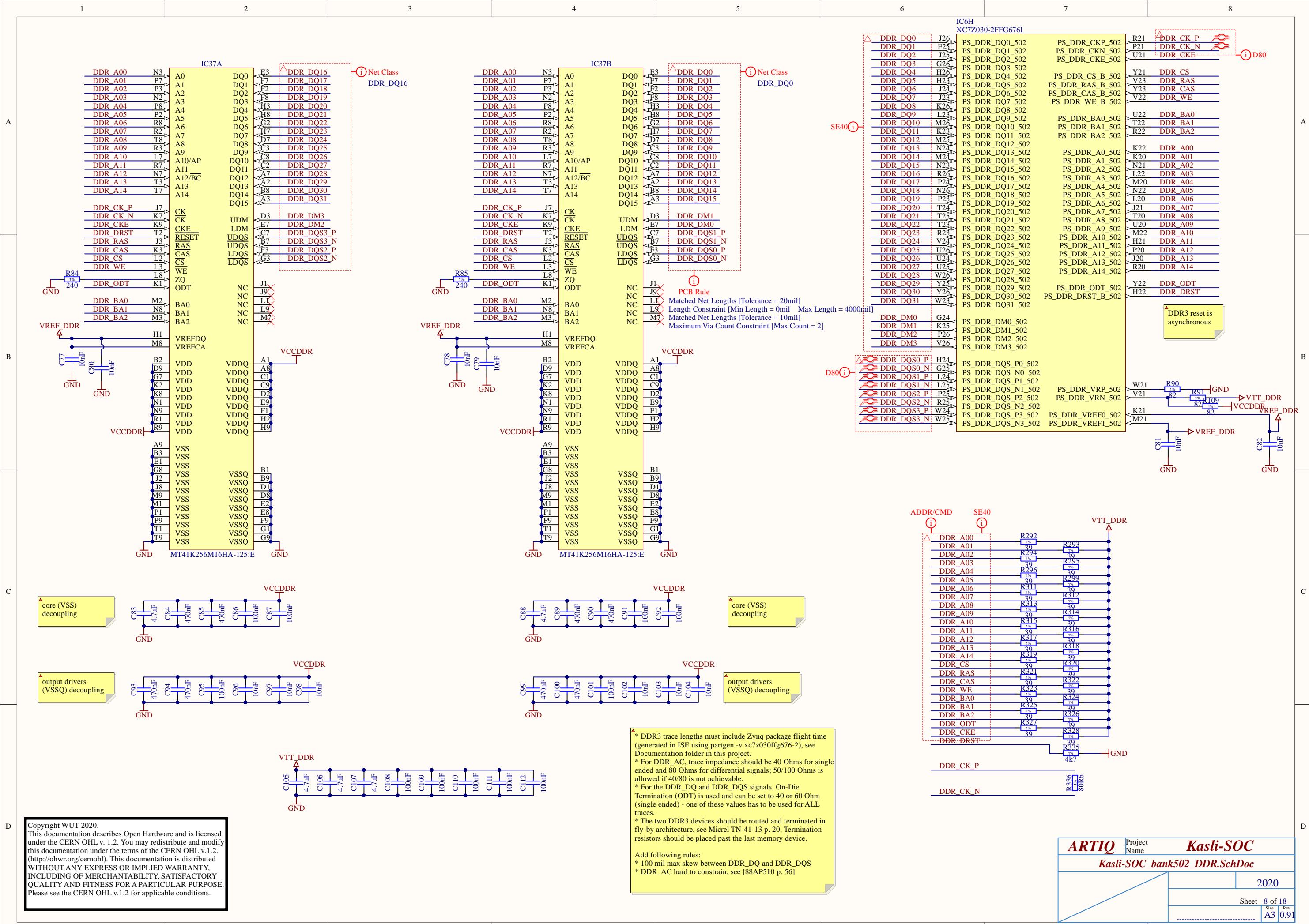
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Kasli-SOC_bank12.SchDoc		
		2020
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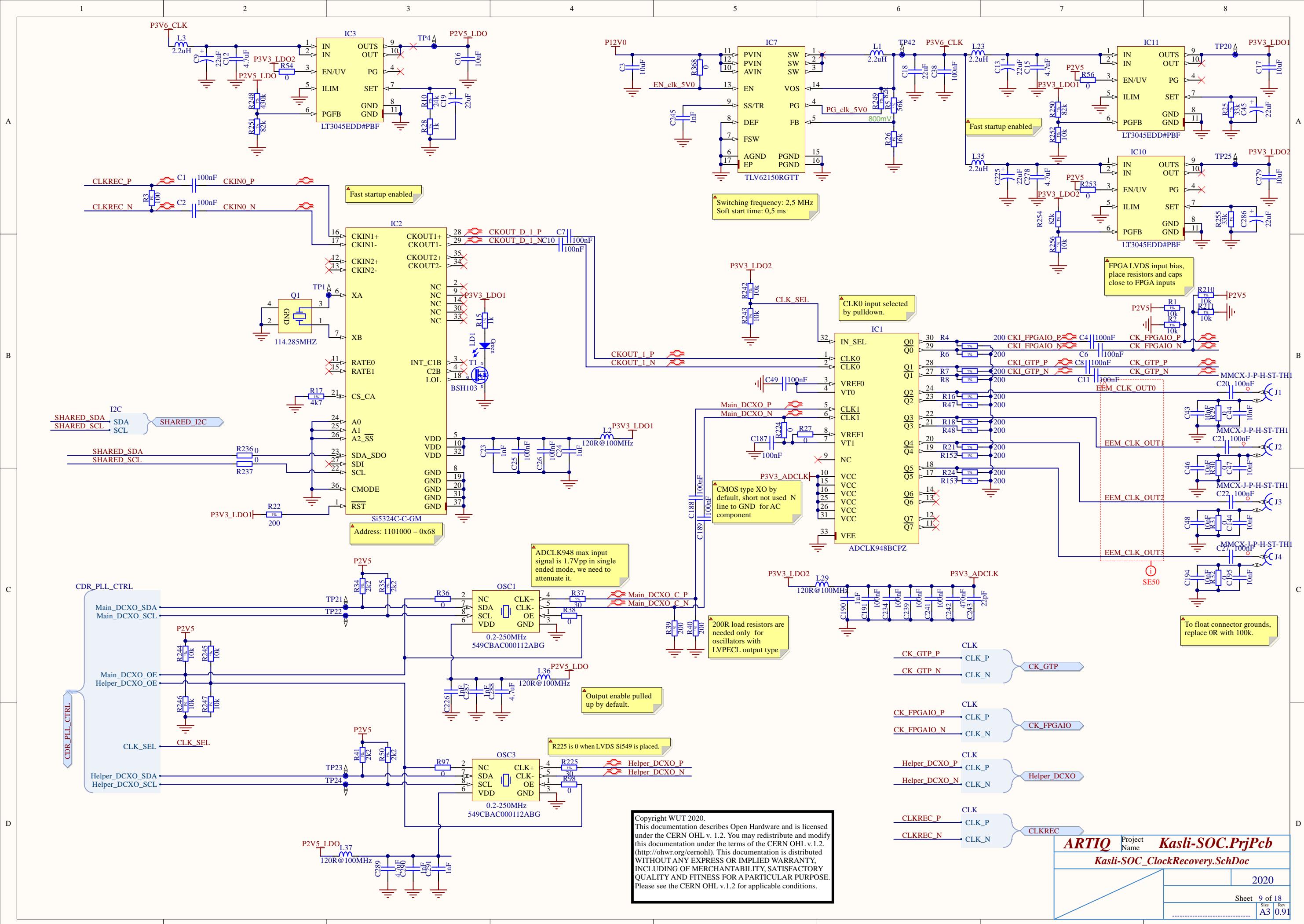




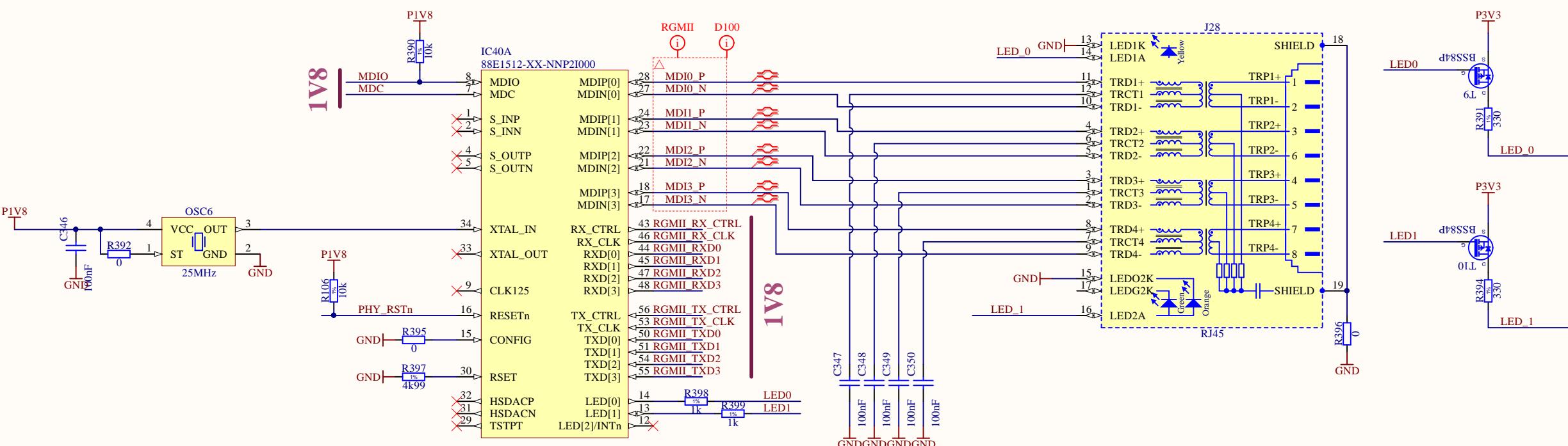
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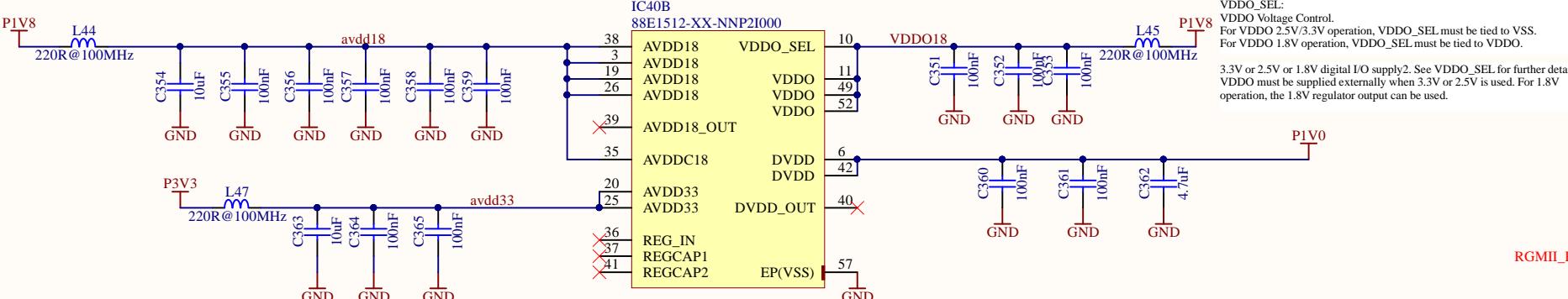




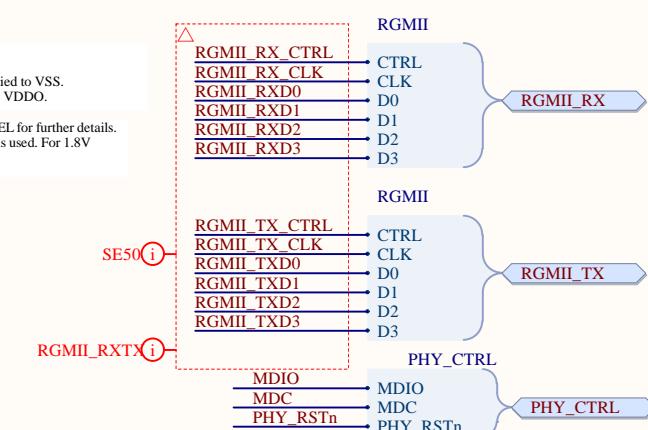
A



C



VDDO_SEL:
VDDO Voltage Control.
For VDDO 2.5V/3.3V operation, VDDO_SEL must be tied to VSS.
For VDDO 1.8V operation, VDDO_SEL must be tied to VDDO.
3.3V or 2.5V or 1.8V digital I/O supply2. See VDDO_SEL for further details.
VDDO must be supplied externally when 3.3V or 2.5V is used. For 1.8V operation, the 1.8V regulator output can be used.



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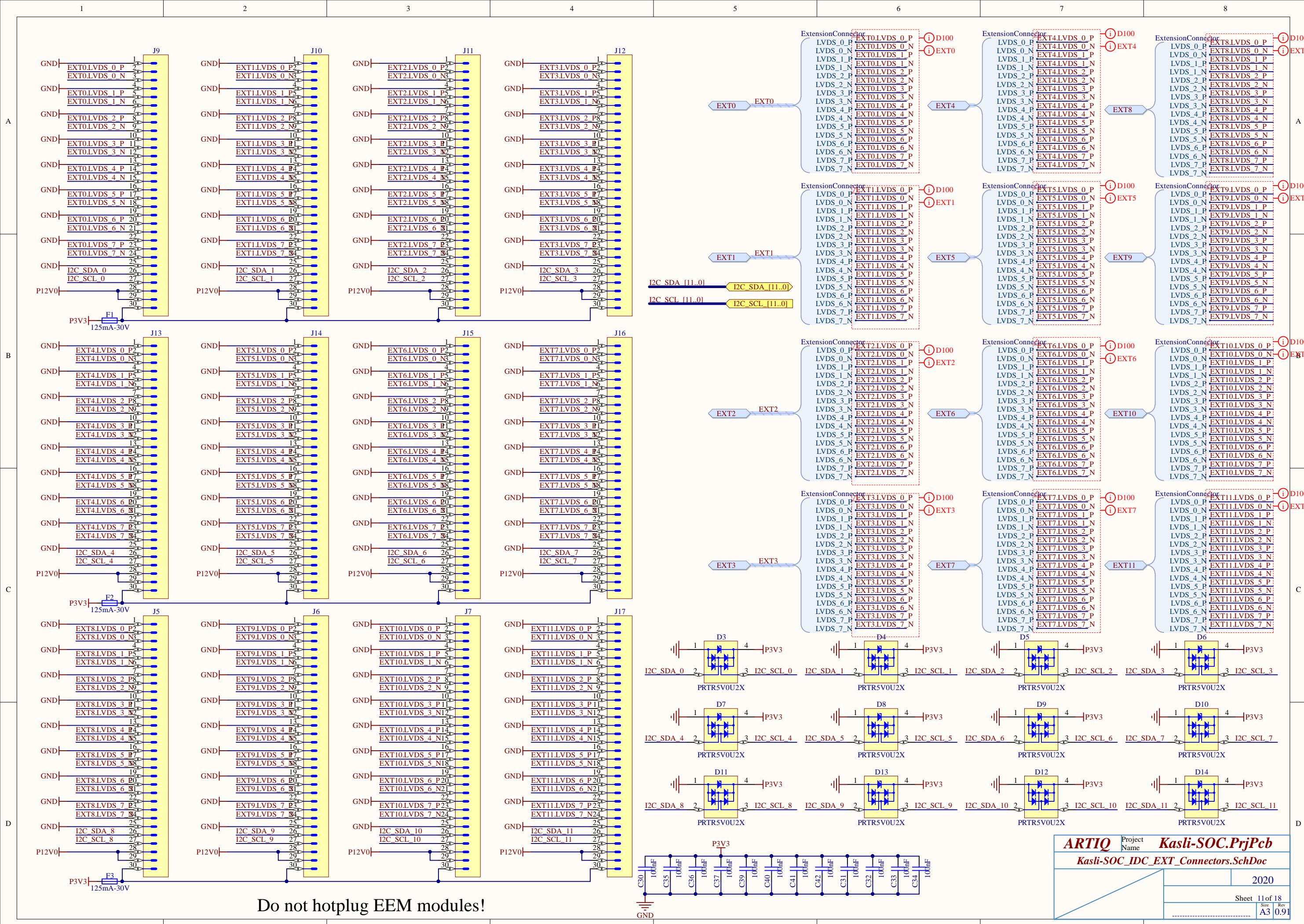
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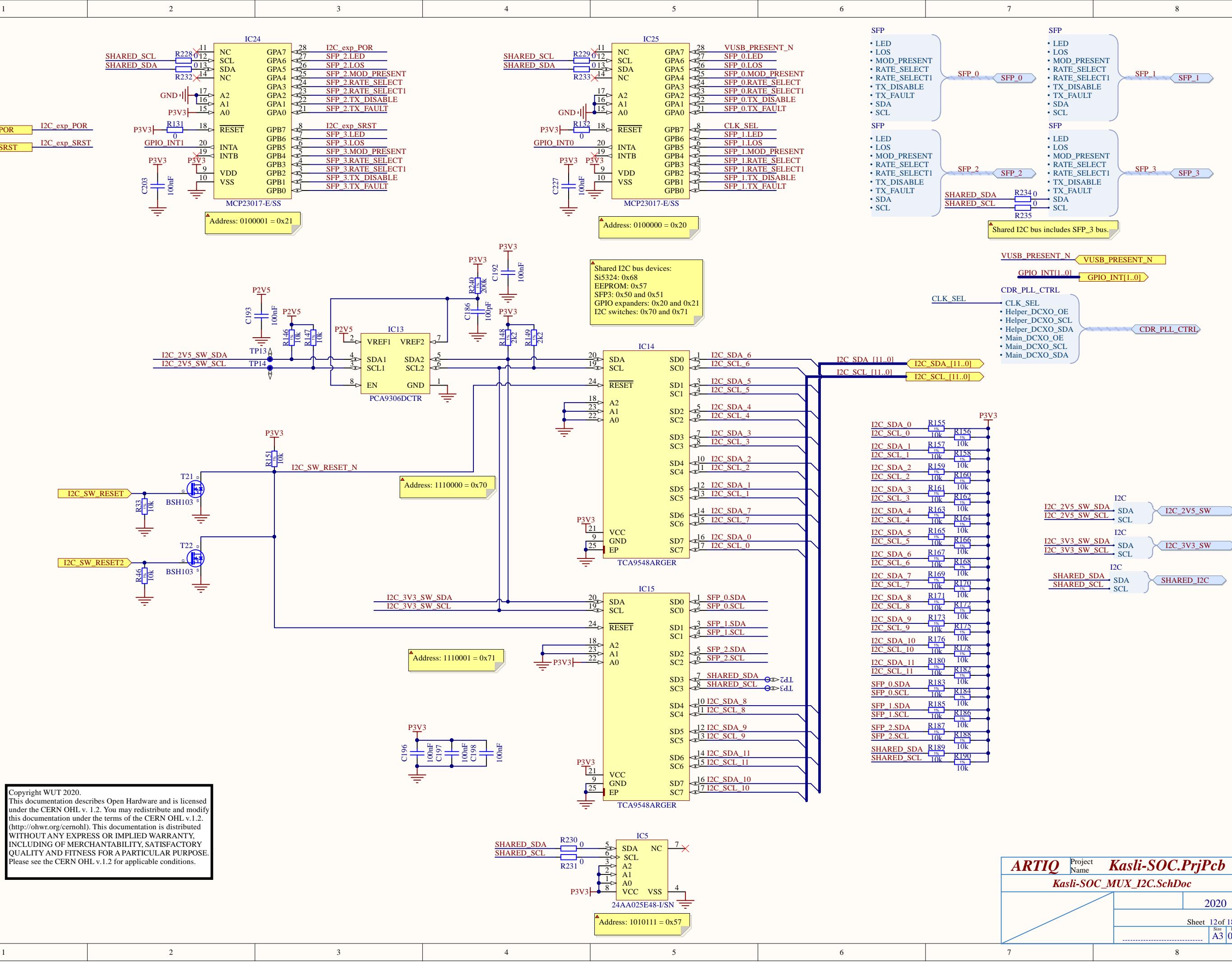
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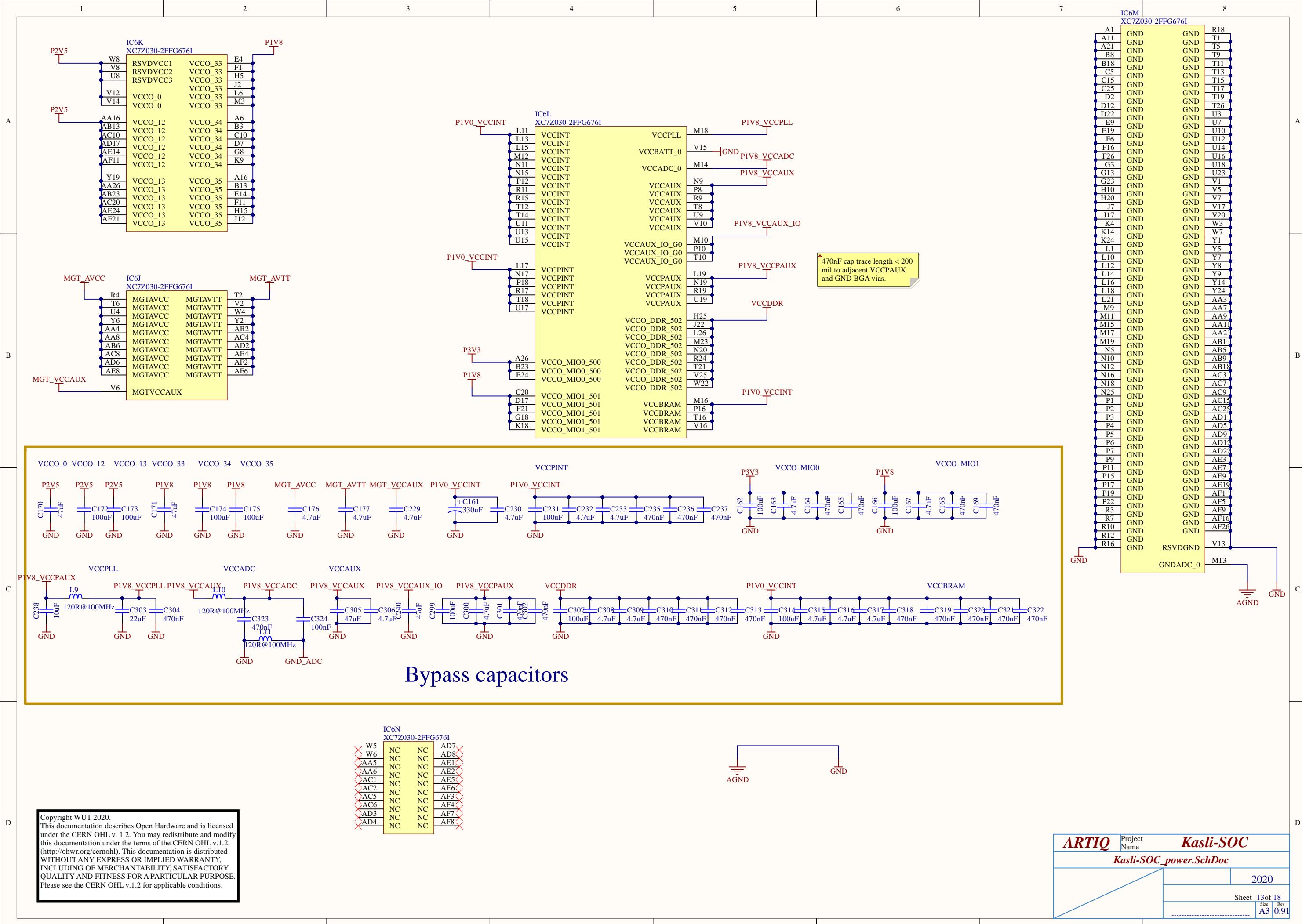
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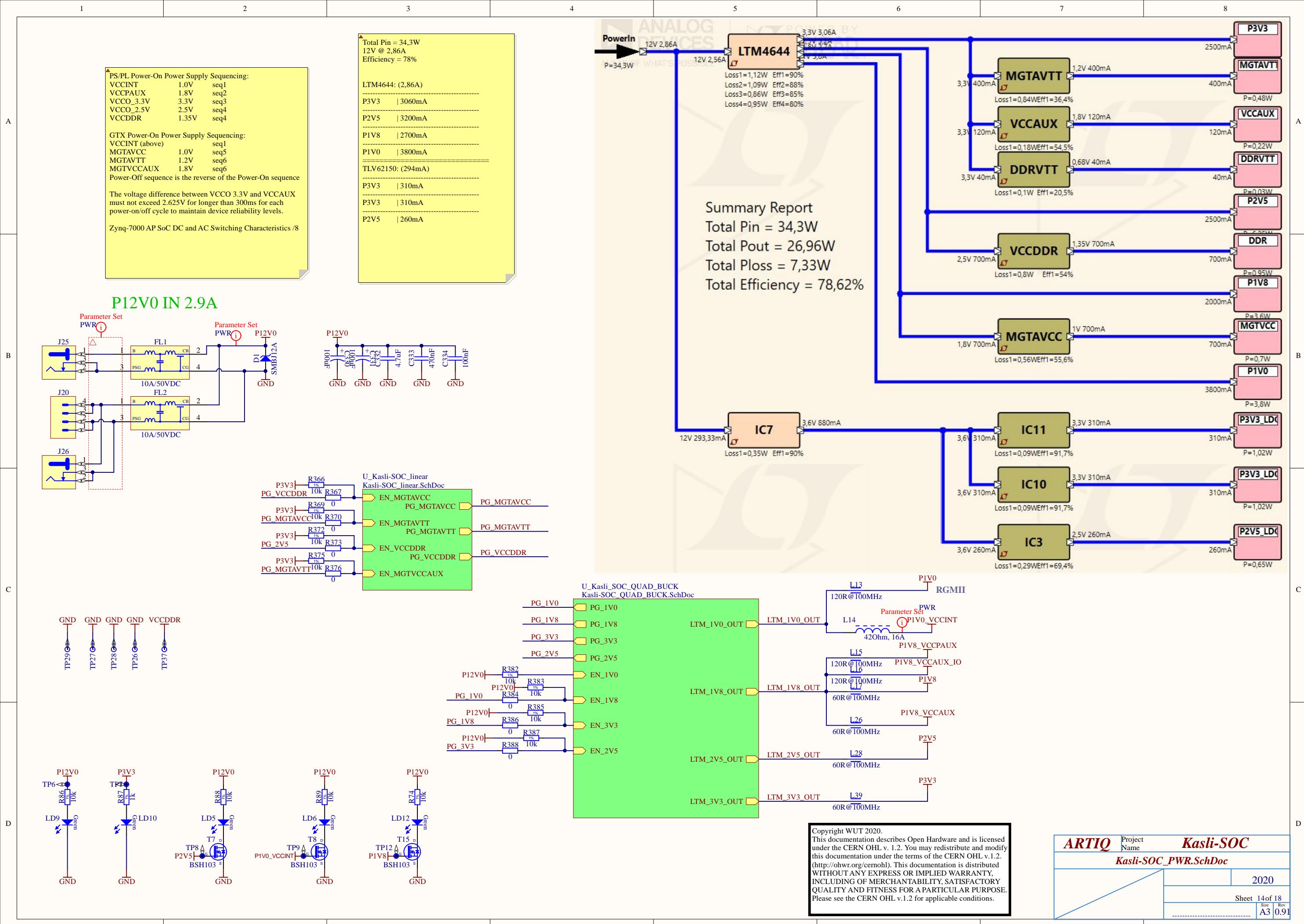
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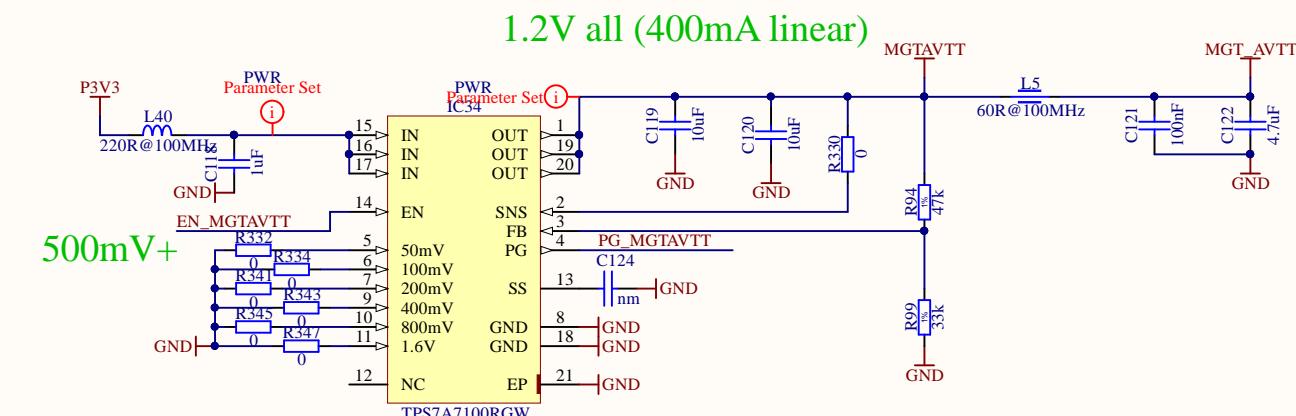
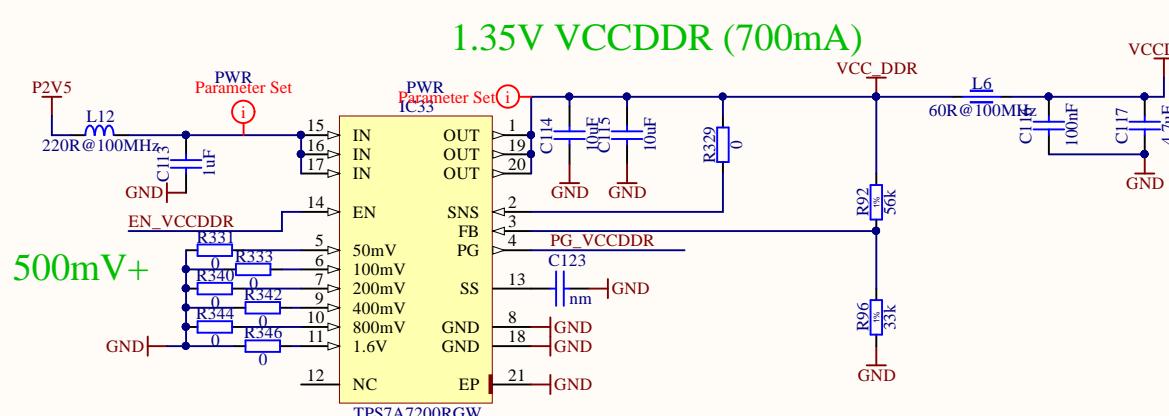
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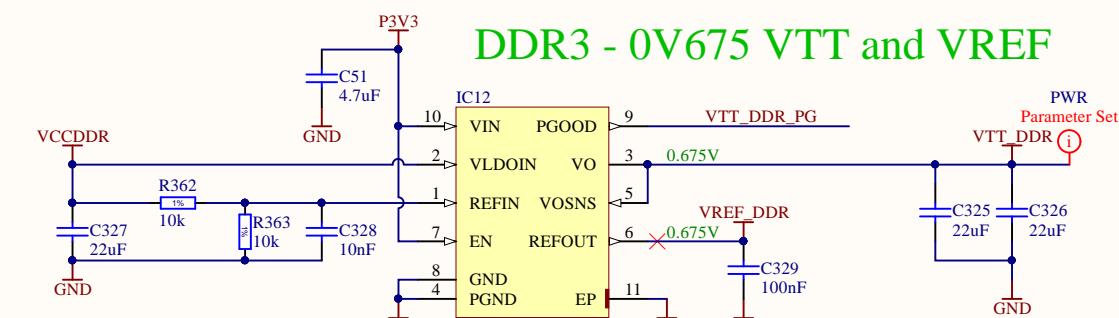
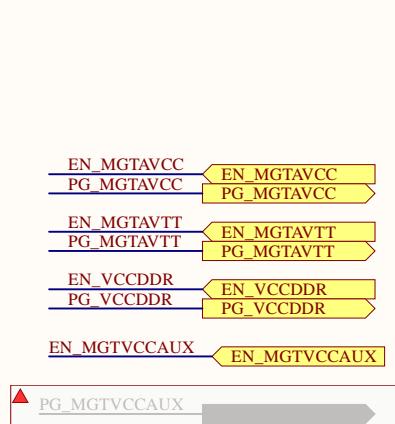
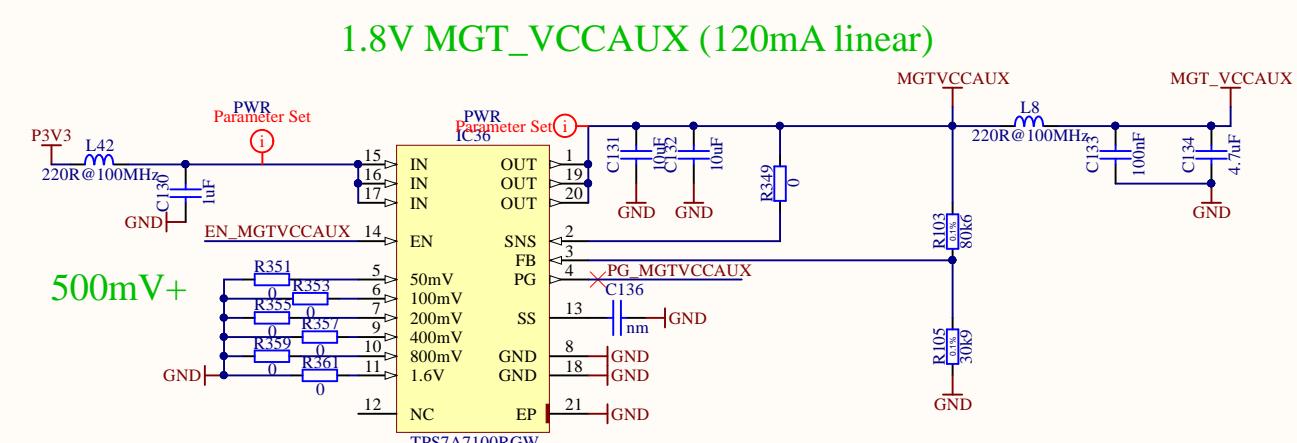
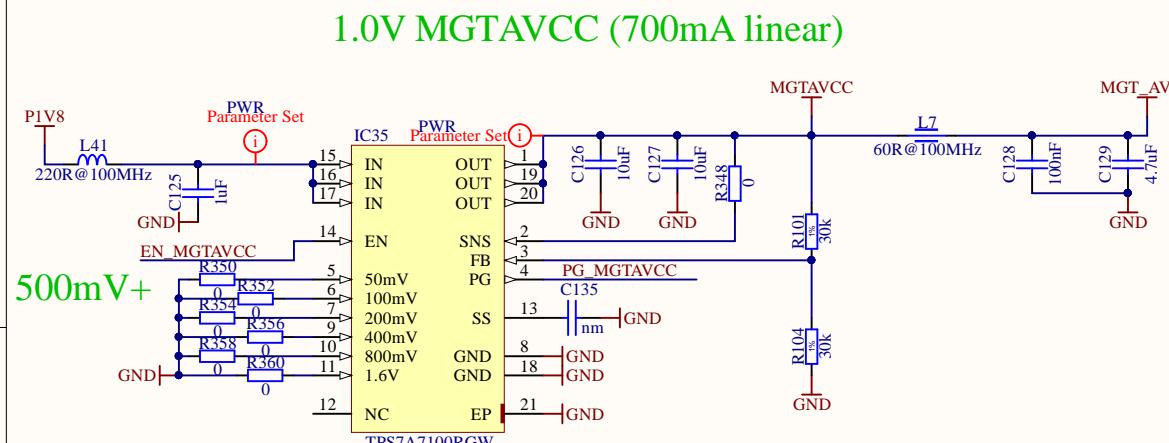




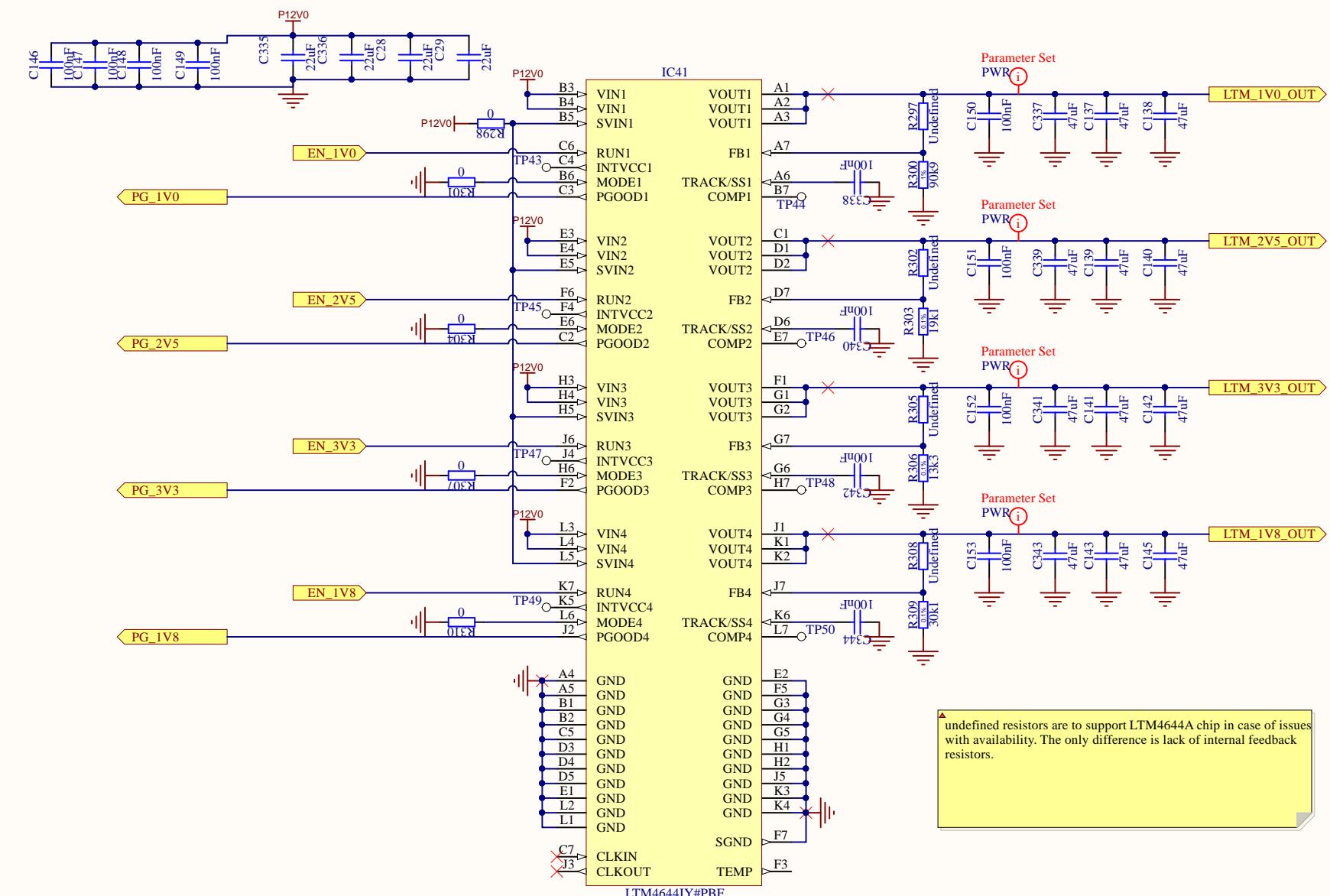




While using configuration pins, unmount FB resistors and mount SNS 0R resistor!



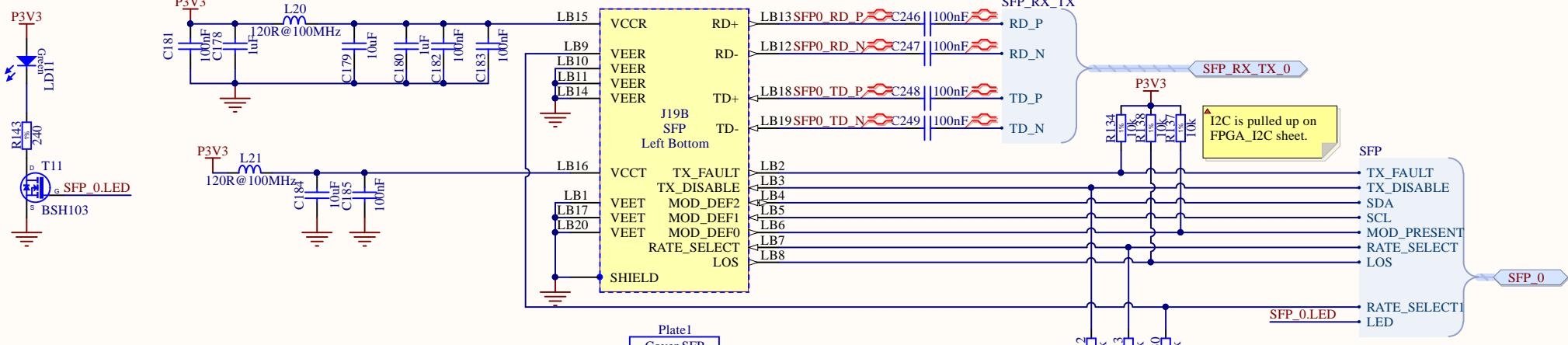
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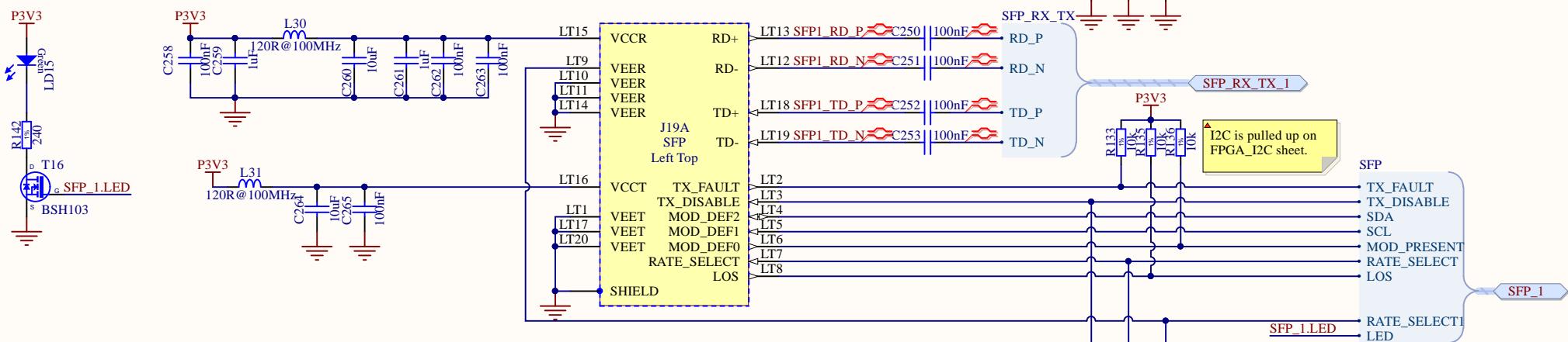
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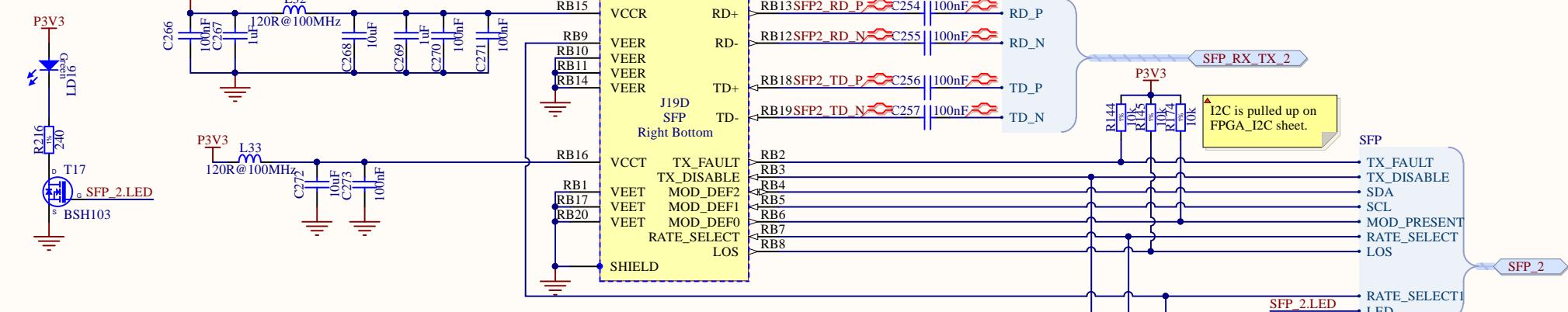
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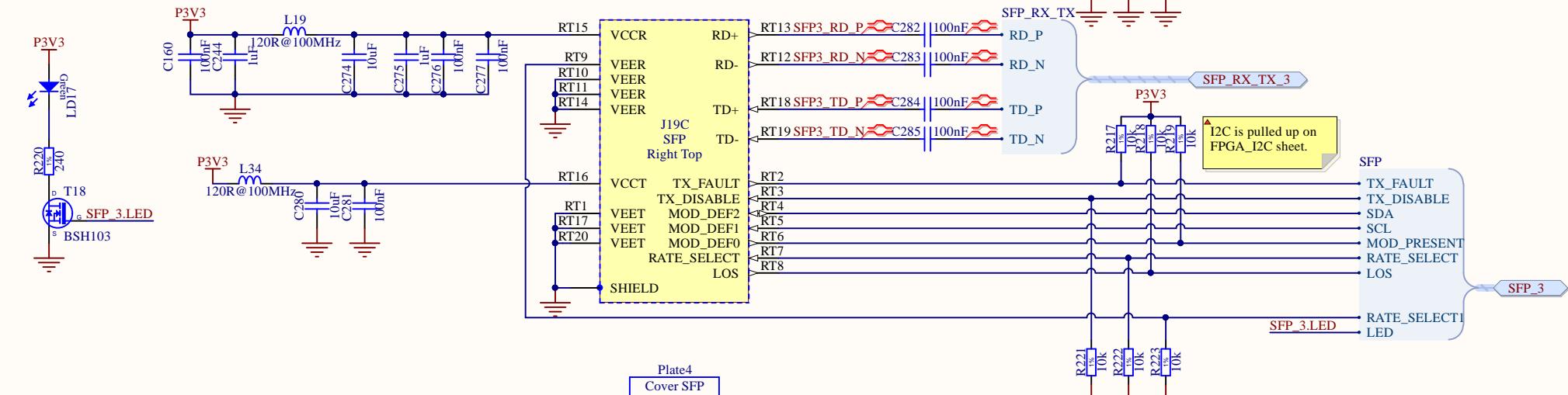
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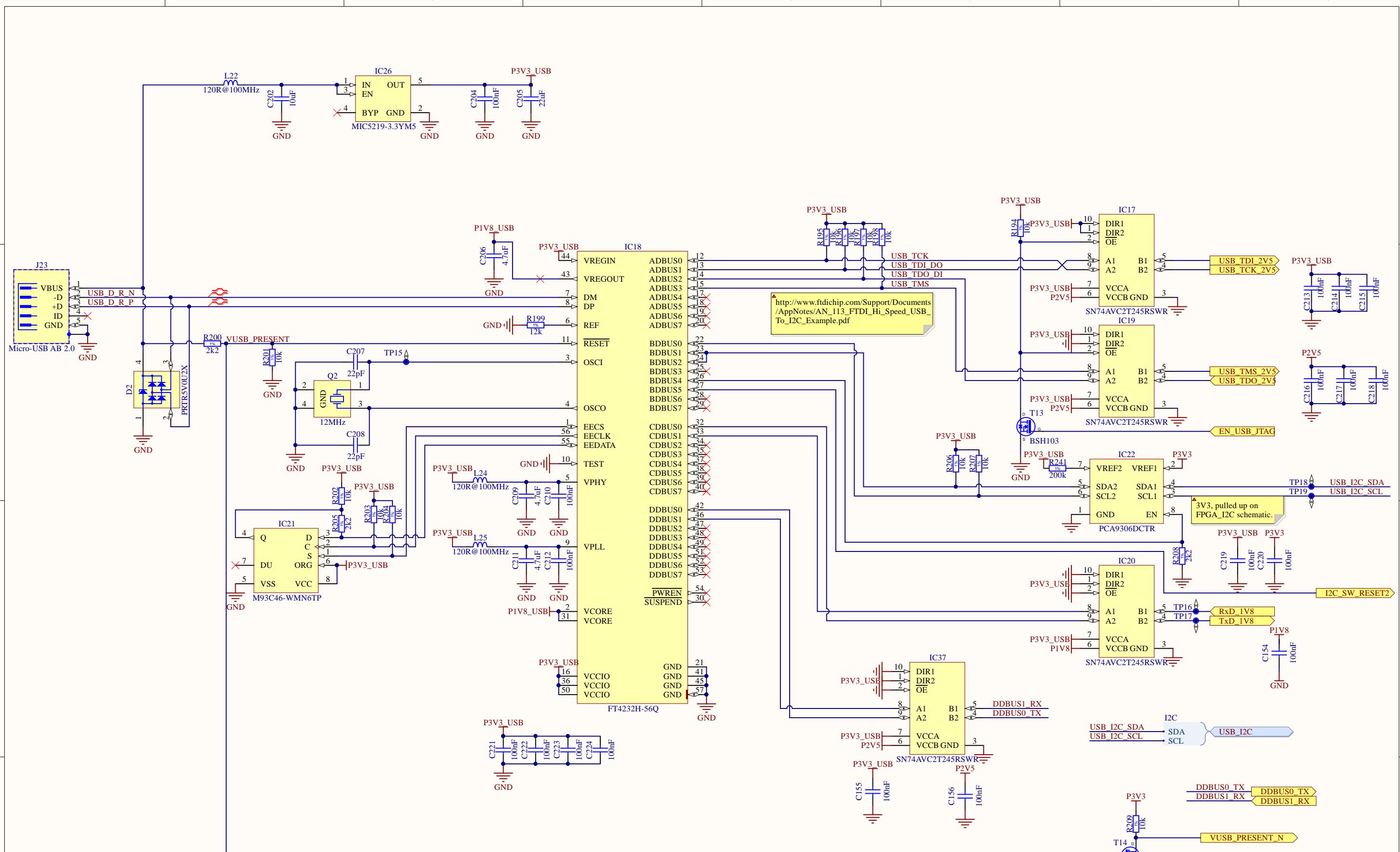
ARTIQ Project Name **Kasli-SOC.PrfPcb**

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