

A

A

B

B

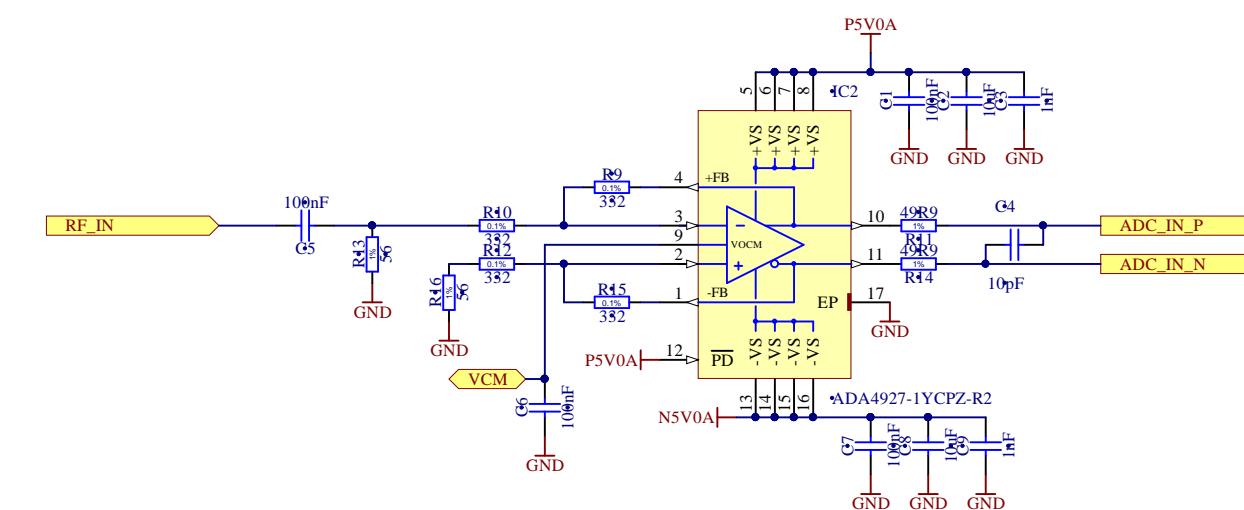
C

c

D

D

**E** Copyright WUT 2017  
This documentation describes Open Hardware and is licensed under the CERN OHL v.1.1. You may redistribute and modify this documentation under the terms of the CERN OHL v.1.1. (<http://ohwr.org/CERNOHL>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE. Please see the CERN OHL v.1.1 for applicable conditions.



**ANSWER** The answer is  $\frac{1}{2} \pi r^2 h$ .

## *ADC*

Project/Equipment		ADC/DAC MEZZANINE	
Cannot open file D:\Dropbox\DESIGNS\M TCA projects\SiN ARA\AFE_-	<b><i>ADC Input channel</i></b>	Designer G.K. Drawn by G.K. Check by - Last Mod. - File ADC_channel.SchDoc Print Date 03.02.2017 01:54:09	XX/XX/XXXX - 03.02.2017 <a href="#">ADC_channel.SchDoc</a> Sheet - of -
Warsaw University of Technology Nowowiejska 15/19	ISe	ARTIO	Size A3 - Rev -

A

A

B

B

C

C

D

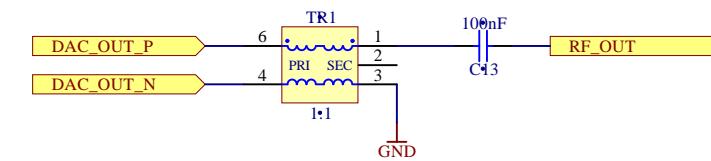
D

E

E

Copyright WUT 2017

This documentation describes Open Hardware and is licensed under the CERN OHL v.1.1. You may redistribute and modify this documentation under the terms of the CERN OHL v.1.1. (<http://ohwr.org/CERNOHL>). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE. Please see the CERN OHL v.1.1 for applicable conditions.



#### Project/Equipment    ADC/DAC MEZZANINE

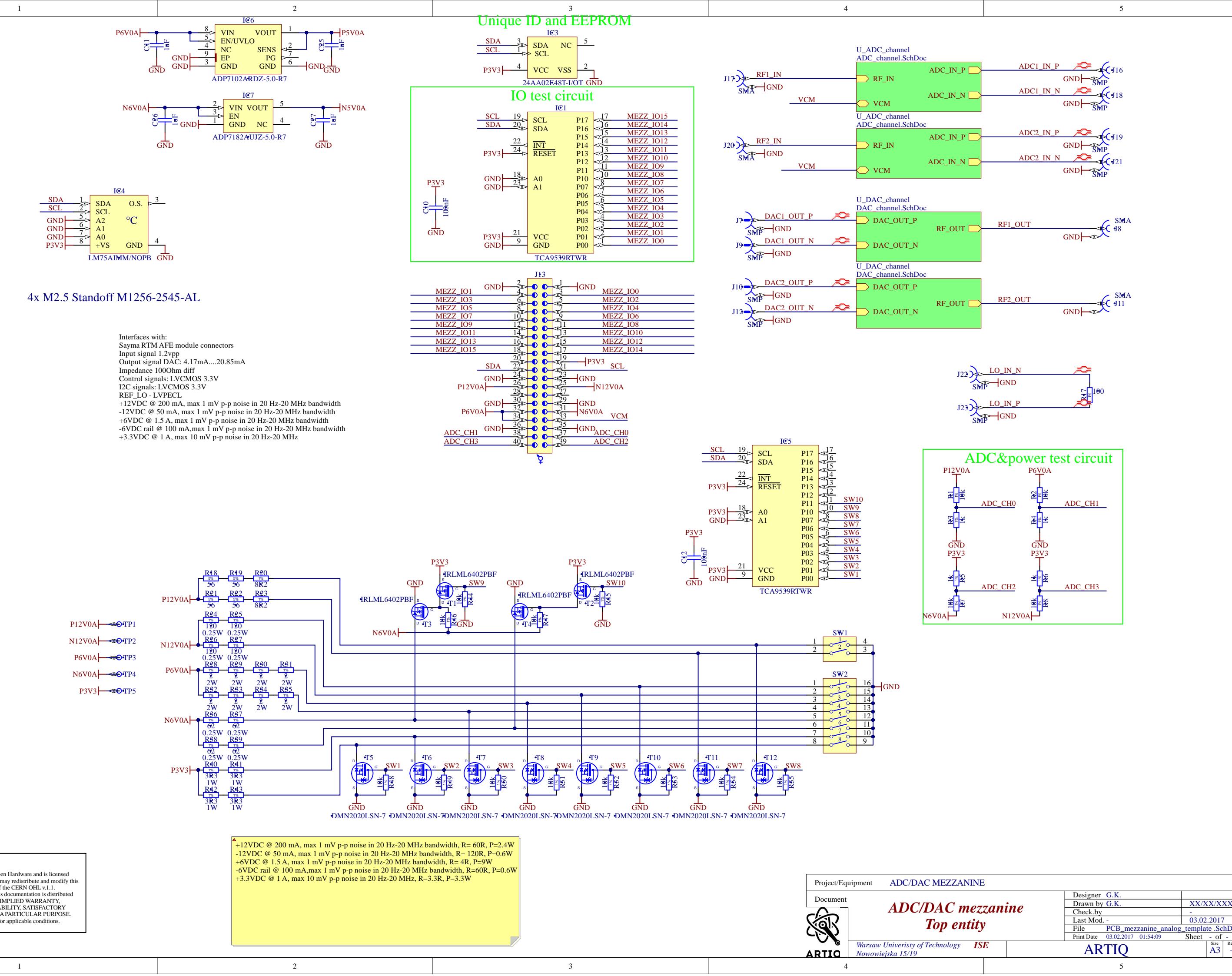
### DAC Output channel

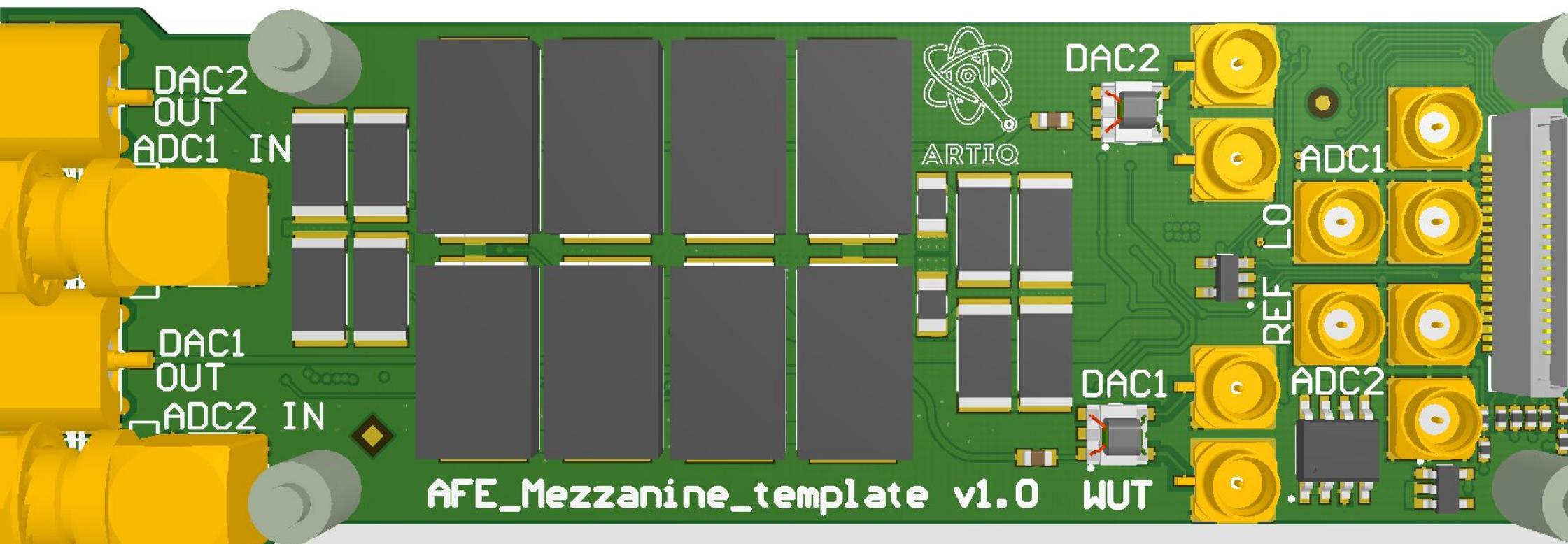
Warsaw University of Technology ISE  
Nowowiejska 15/19

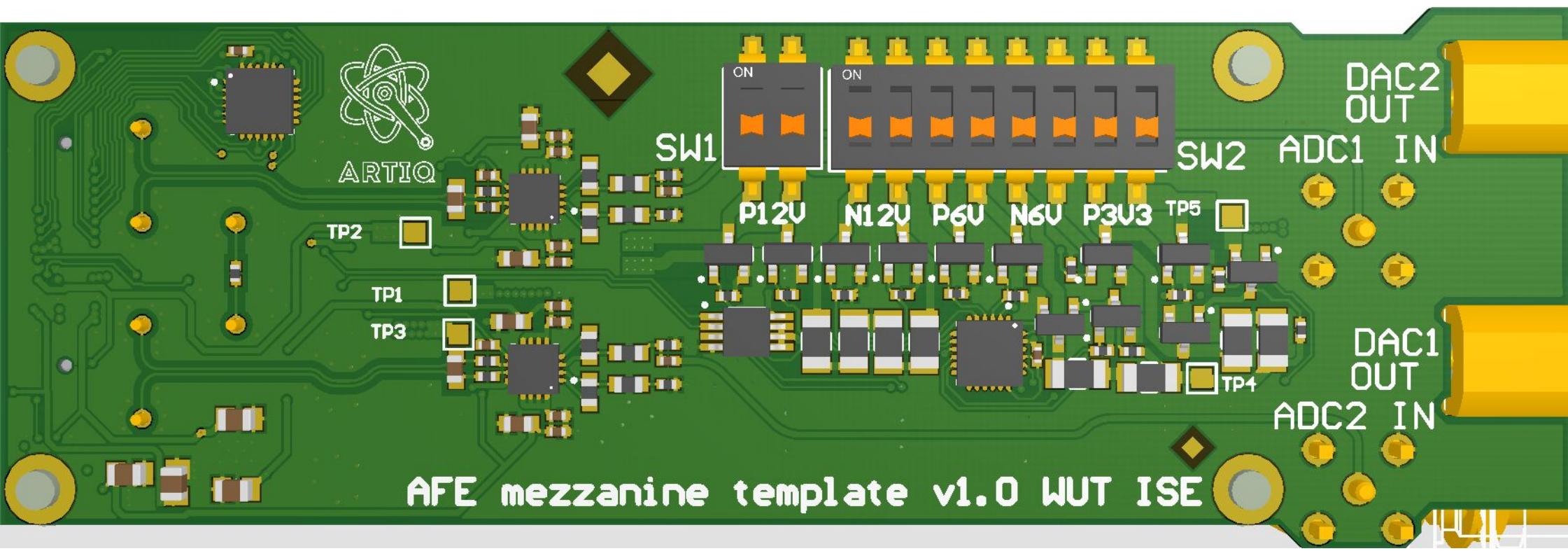
ARTIQ

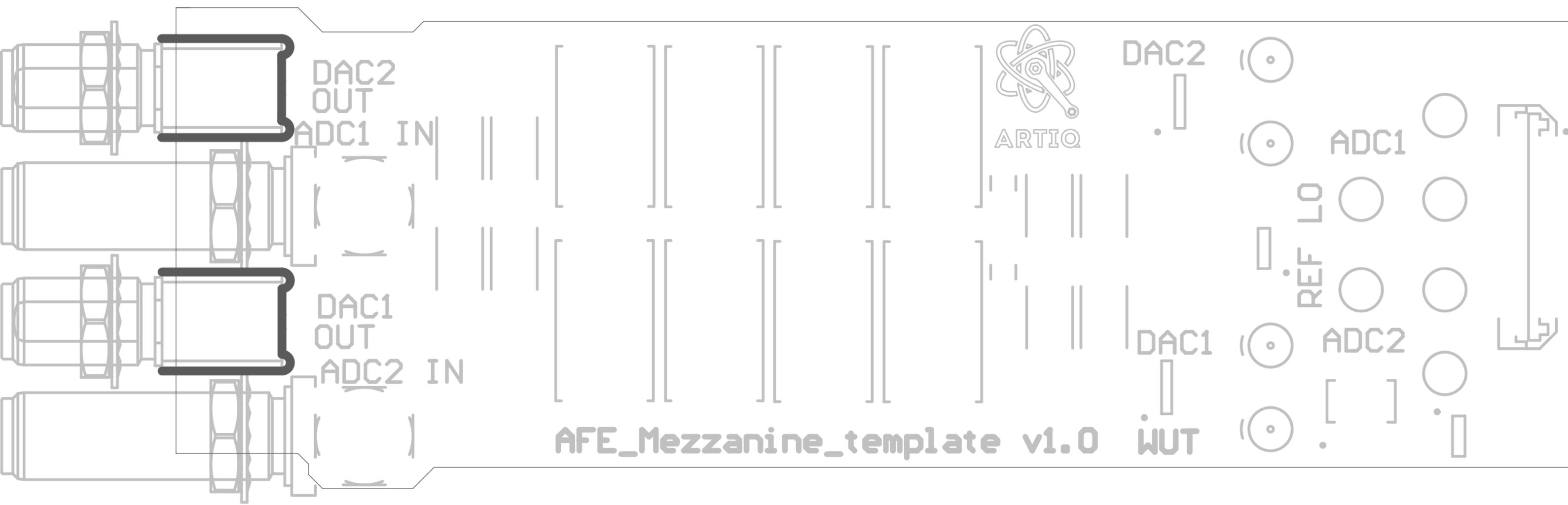
A3

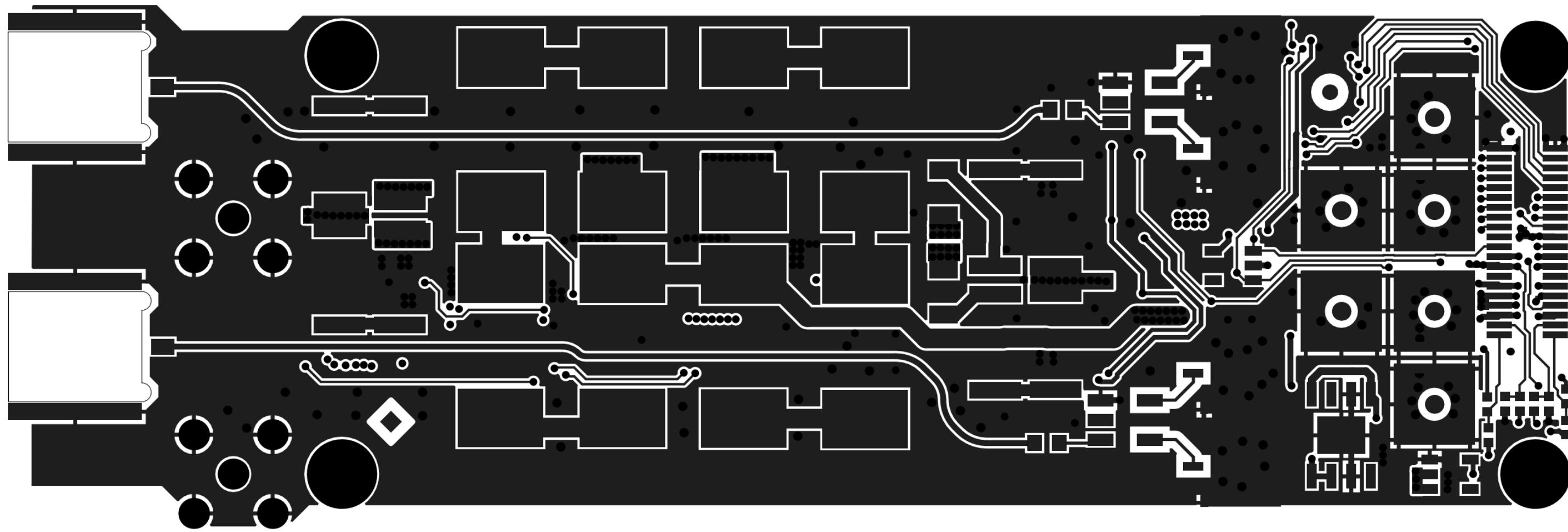
Designer G.K.	Drawn by G.K.	XX/XX/XXXX
Cannot open file	-	-
D:\Dropbox\DESIGNS\M	Last Mod. -	03.02.2017
TCA	File	DAC_channel.SchDoc
projects\SIN	Print Date	03.02.2017 01:54:09
ARA\AFE_	Sheet	of

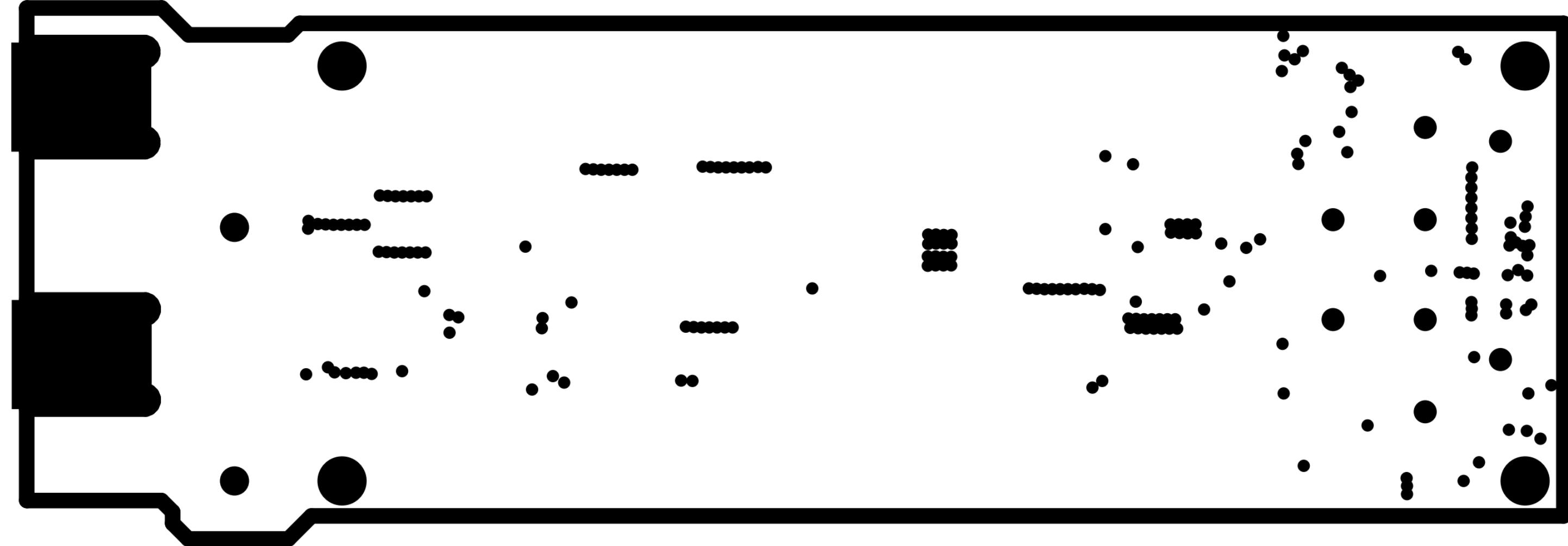


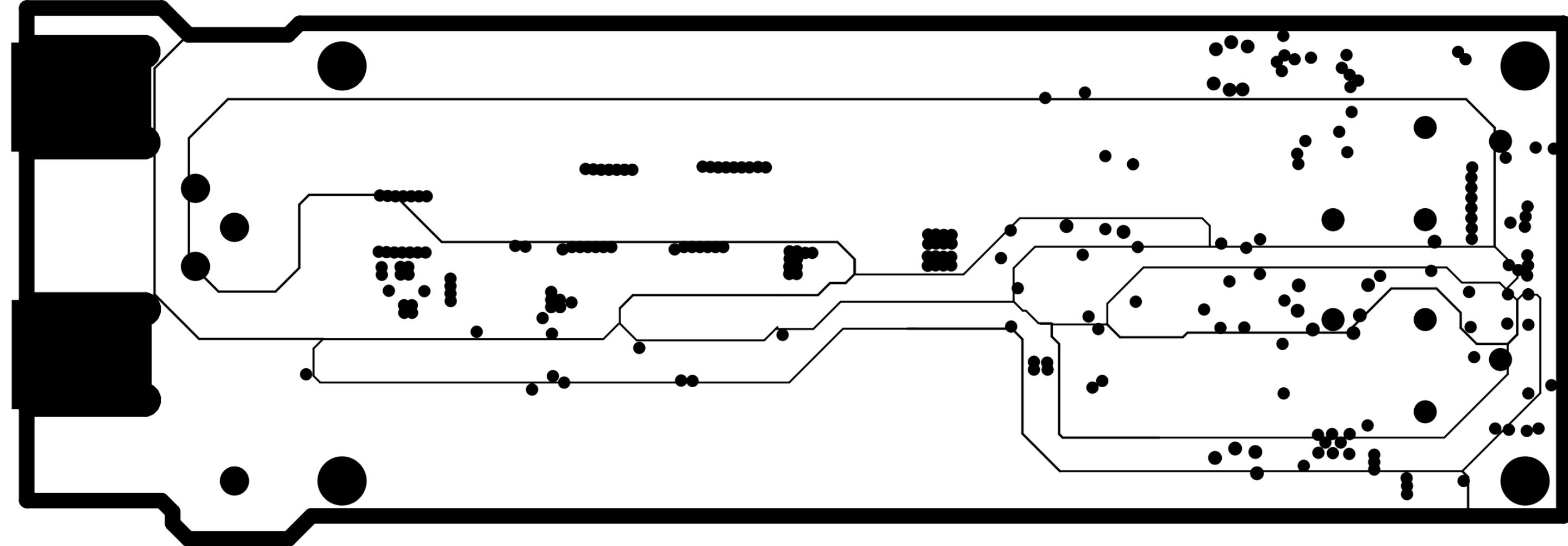


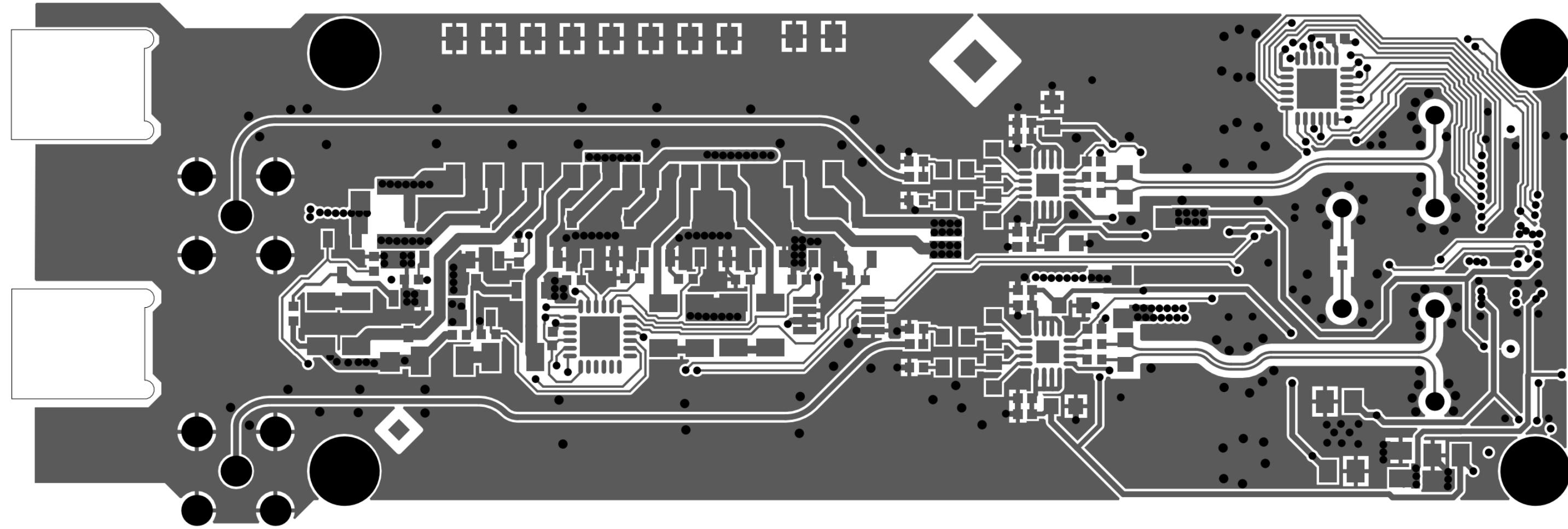


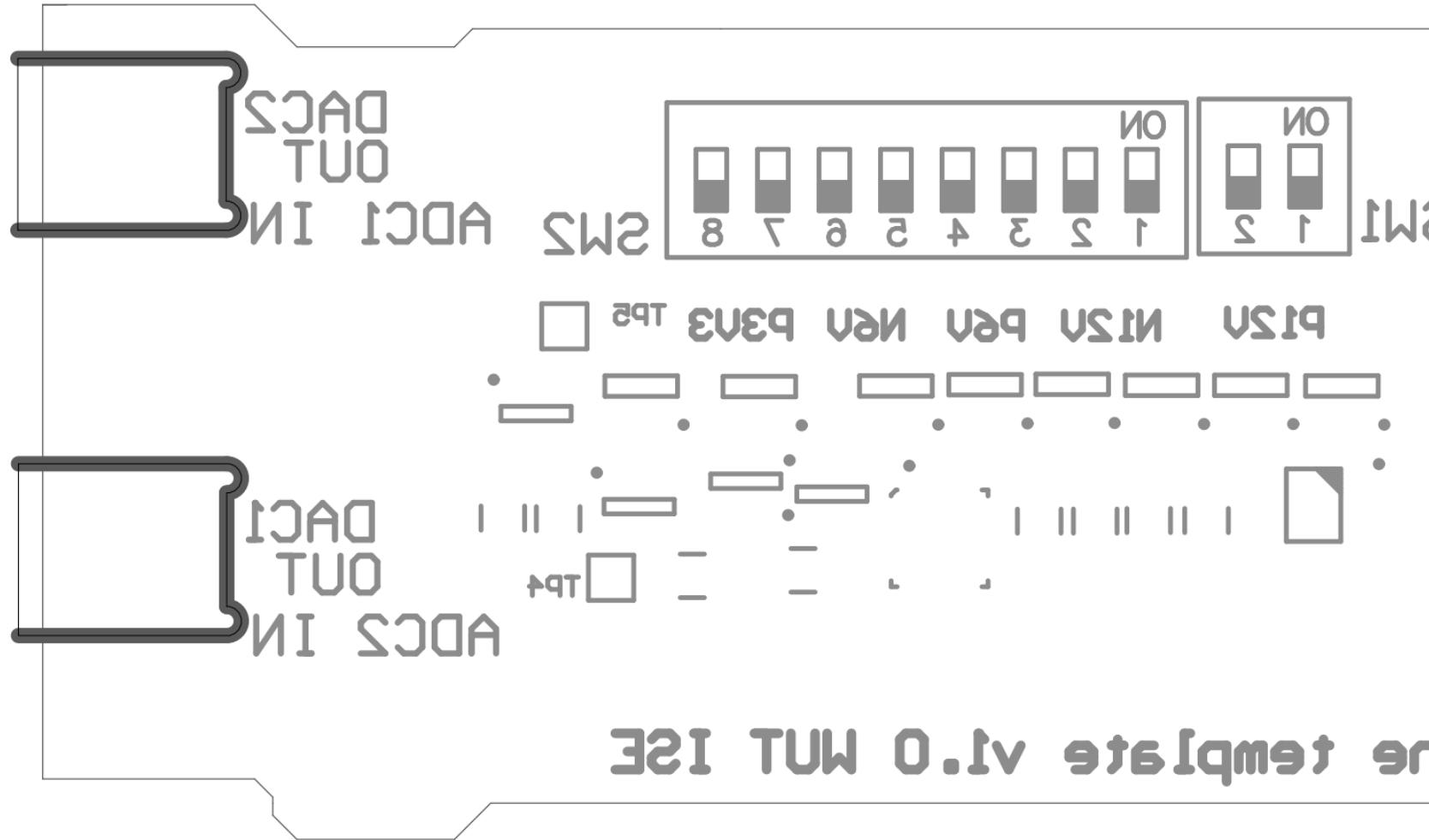












AFE measurement template v1.0 MUT ISE