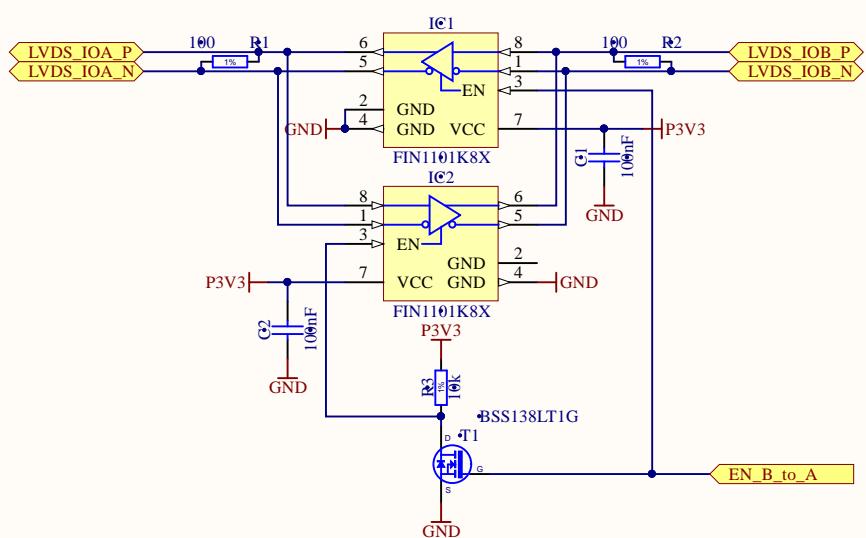


A

A



B

B

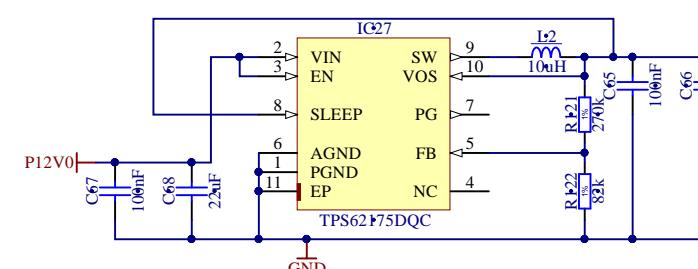
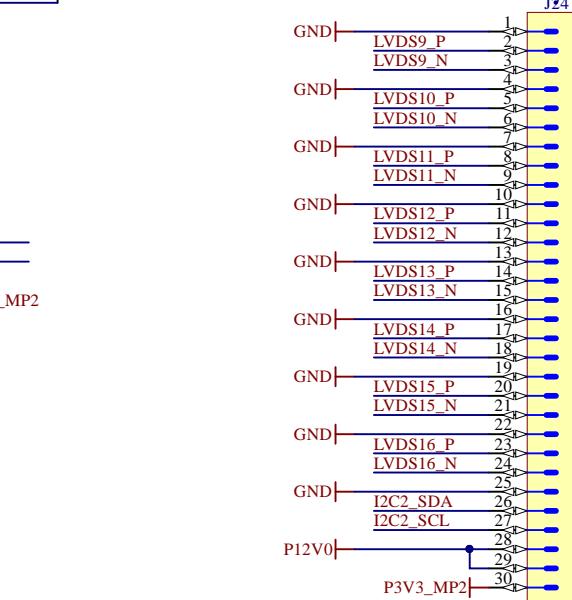
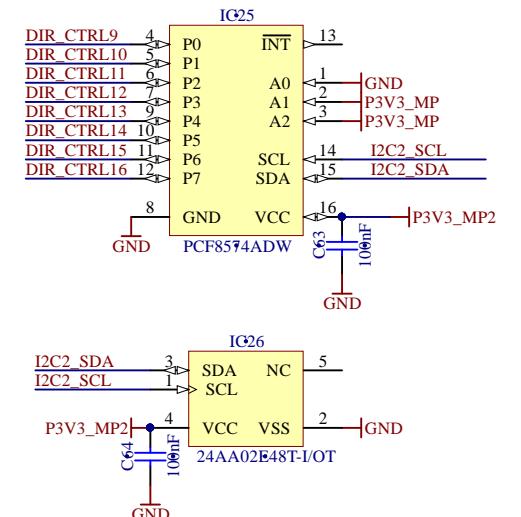
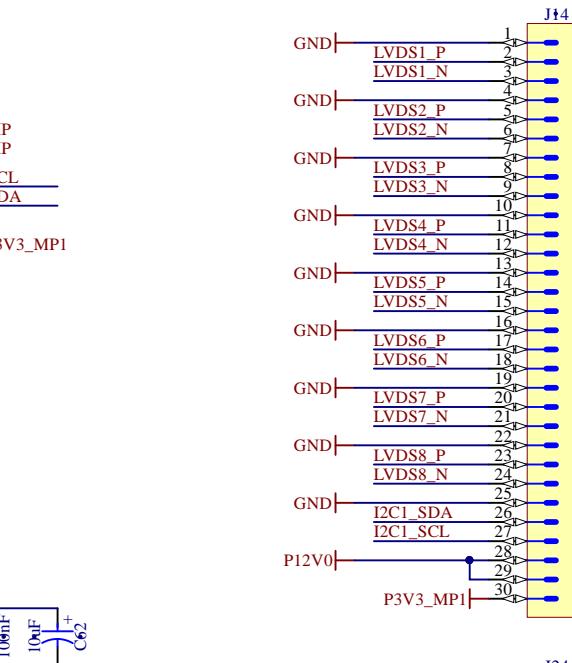
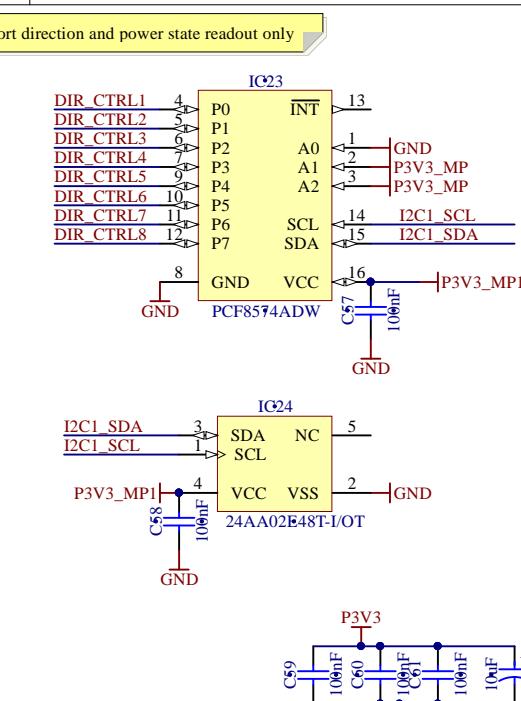
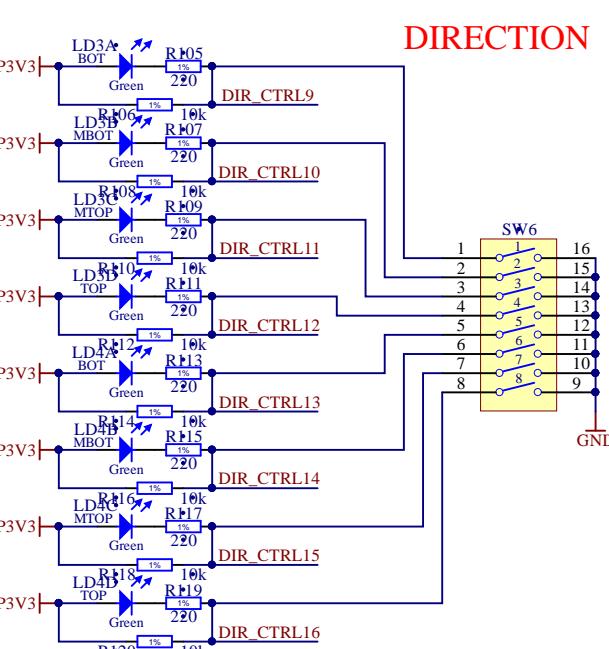
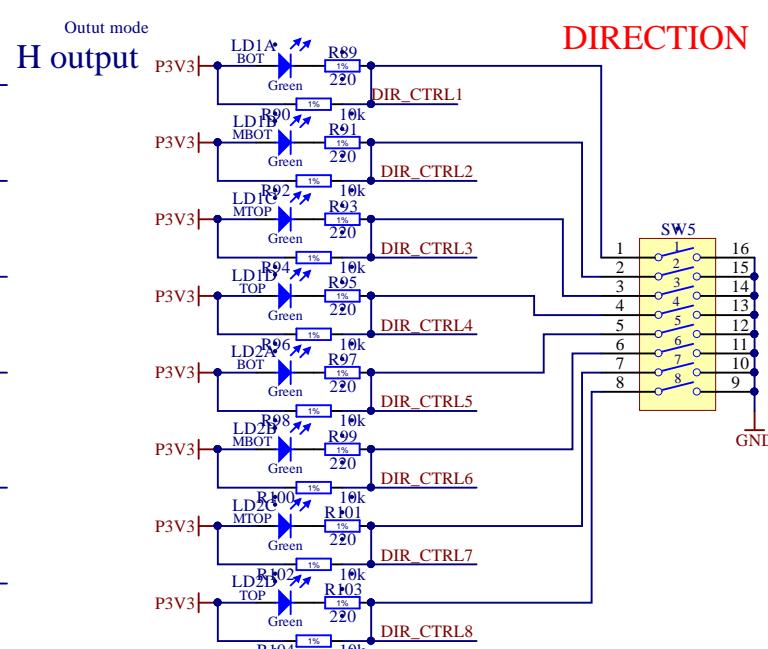
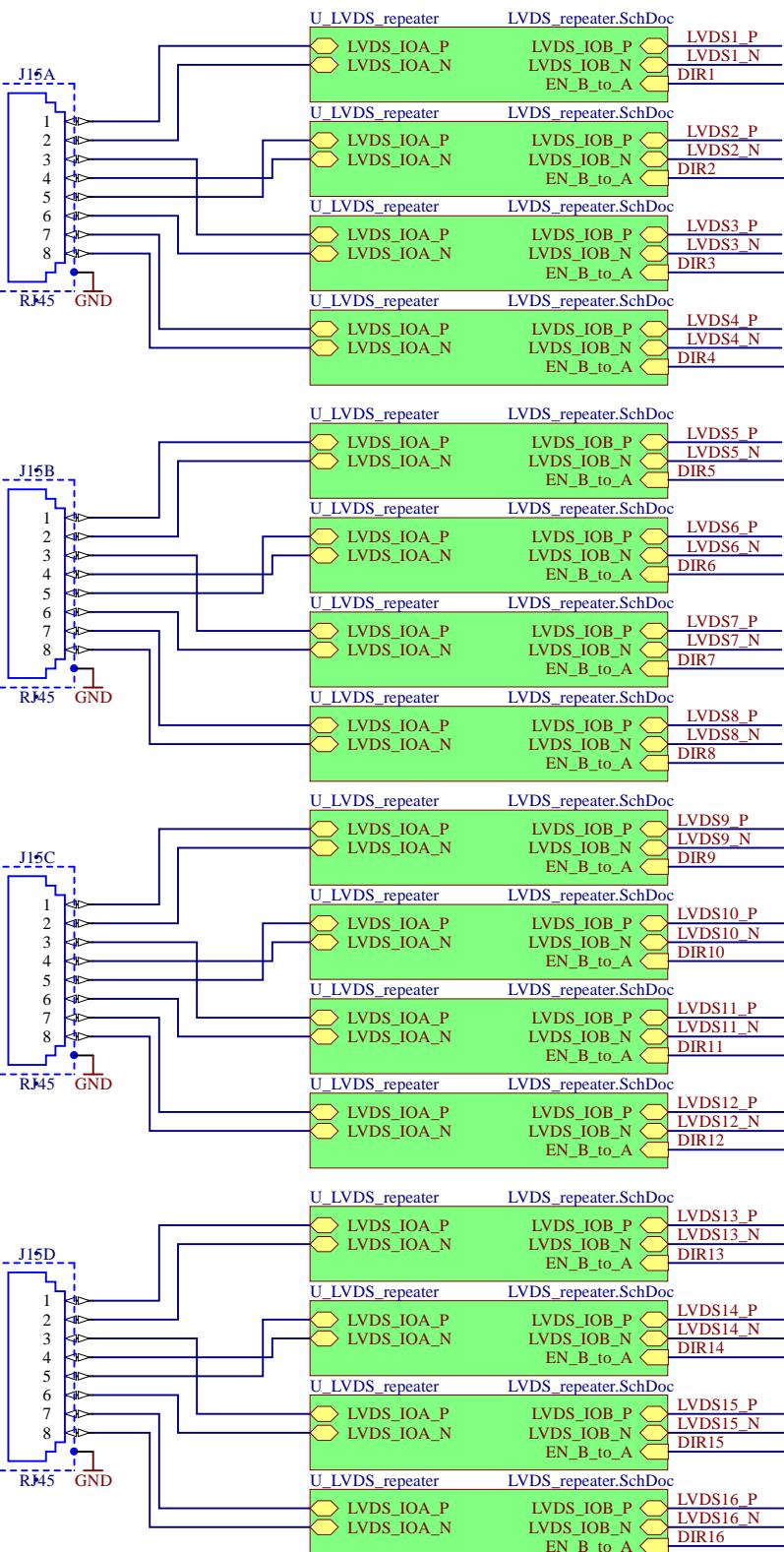
C

C

D

D

Title		
Size A4	Number	Revision
Date: 02.02.2017	Sheet of	
File: D:\Dropbox..\LVDS_repeater.SchDoc	Drawn By:	



Project/Equipment ADC/DAC MEZZANINE



Document

Title
Title2

Designer G.K.	Drawn by G.K.	XX/XX/XXXX
Check by -	-	-
Last Mod. -	-	14.01.2017
File PCB_3U_RJ45.schdoc	-	-
Print Date 02.02.2017 01:38:22	Sheet of	-

Warsaw University of Technology ISE

ARTIQ

Size A3 Rev -

