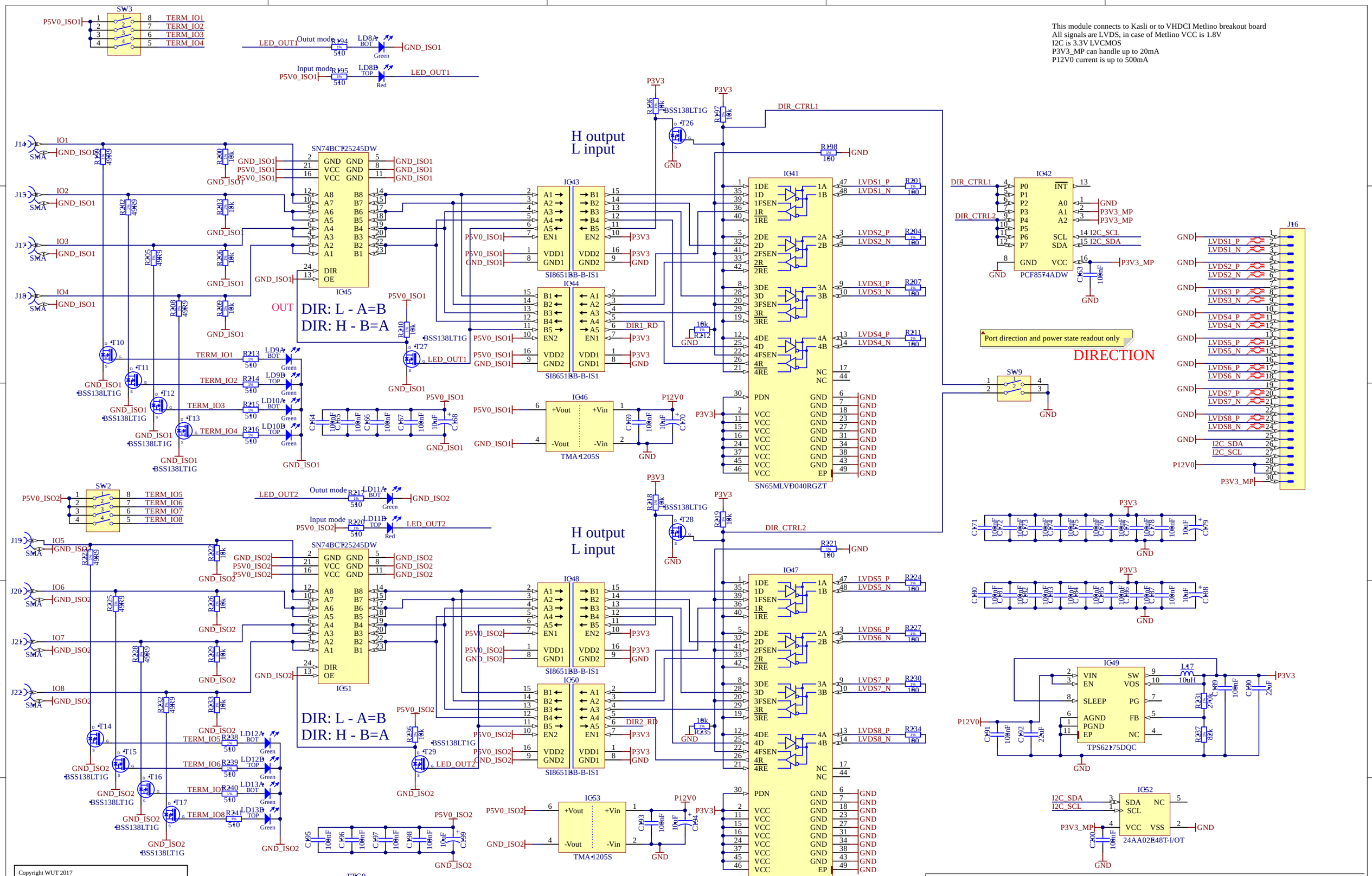
This module connects to Kasli or to VHDCI Metlino breakout board
All signals are LVDS, in case of Metlino VCC is 1.8V
I2C is 3.3V LVCMOS
P3V3_MP can handle up to 20mA
P12V0 current is up to 500mA



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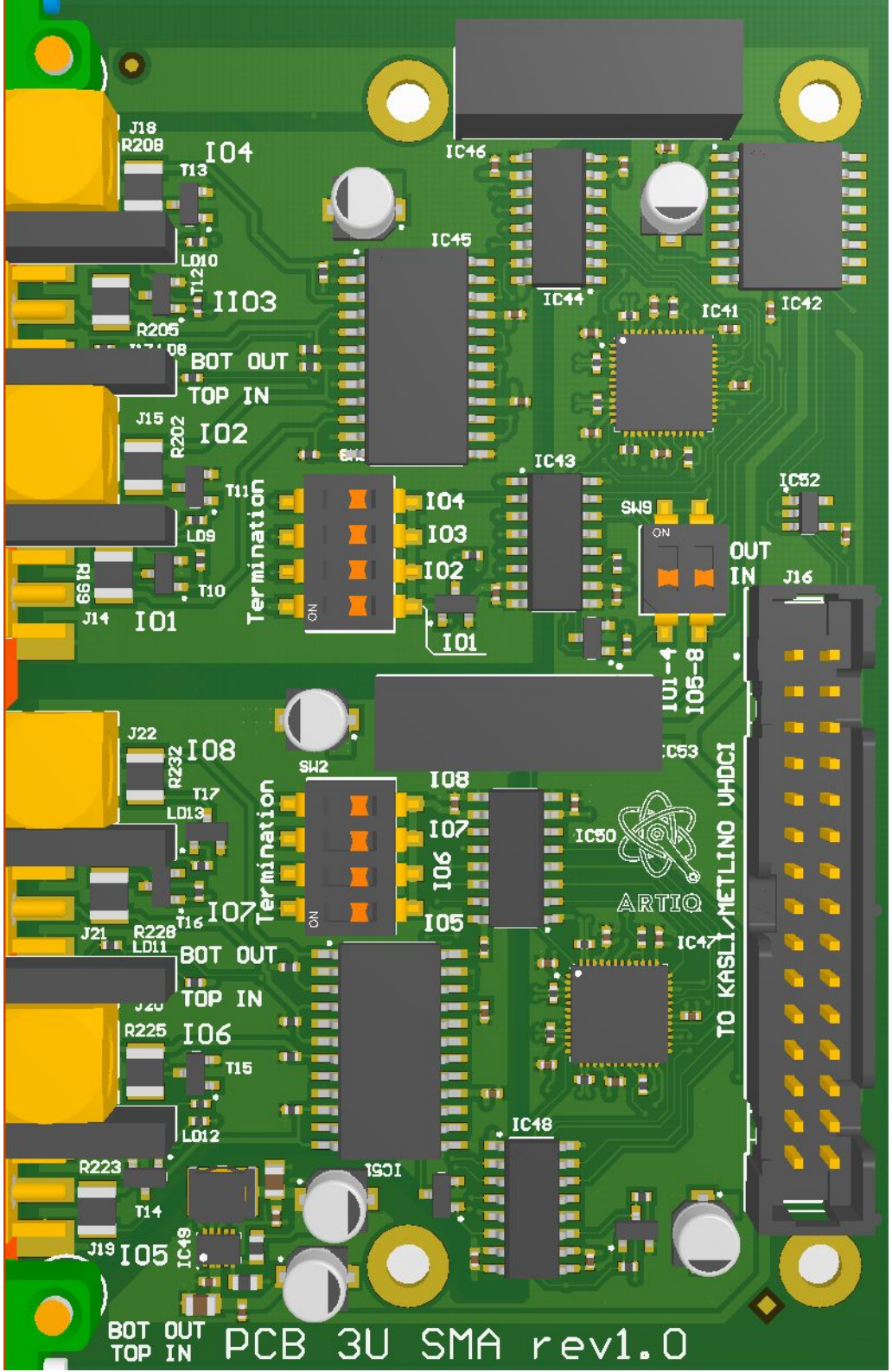
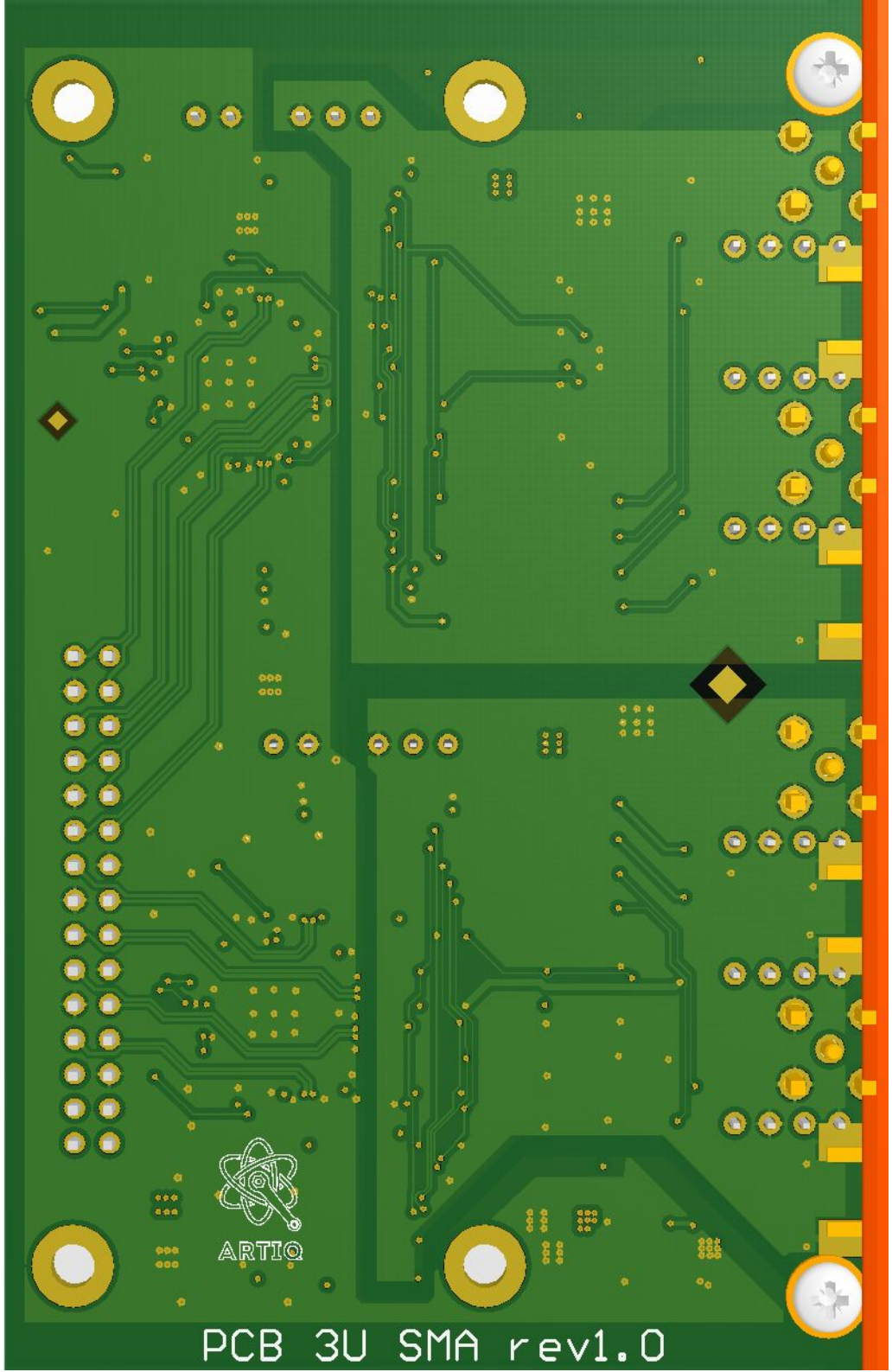
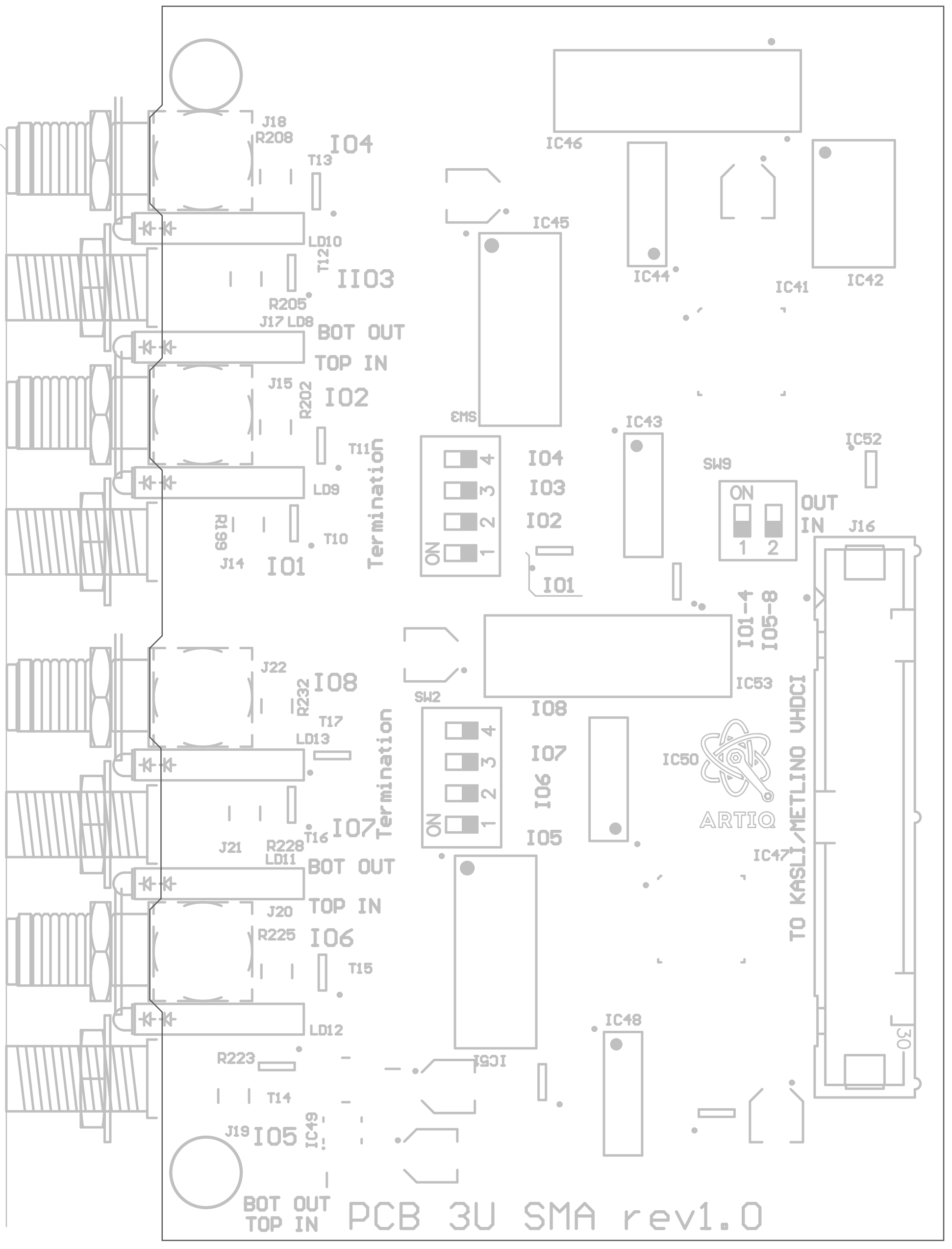
Project/Equipment	KASLI 3U subsystem
Document	**3U SMA Extension board**
Designer	G.K.
Drawn by	G.K. XX/XX/XXXX
Check.by	-
Last Mod.	- 14.05.2017
File	PCB_3U_SMA.schdoc
Print Date	14.05.2017 12:43:26 Sheet - of -
Warsaw Univeristy of Technology Nowowiejska 15/19	ISE
ARTIQ	Size A3 Rev -


J18
R208
T13
IO4
LD10
T12
IIO3
R205
BOT OUT
TOP IN
J15
R202
IO2
T11
LD9
R199
T10
J14
IO1
Termination
SW1
IO4
IO3
IO2
IO1
IC46
IC45
IC44
IC41
IC42
IC43
IC52
SW9
ON
OUT
IN
J16
IO1-4
IO5-8
IC53
J22
R232
IO8
T17
LD13
SW2
IO8
IO7
IO6
IO5
IC50
ARTIQ
IC47
J21
R228
LD11
T16
IO7
BOT OUT
TOP IN
J20
R225
IO6
T15
LD12
R223
T14
J19
IO5
IC49
IC51
IC48
TO KASLI/METLINO VHDCI
BOT OUT
TOP IN
PCB 3U SMA rev1.0


ARTIQ
PCB 3U SMA rev1.0


J18
R208
IO4
T13
LD10
T12
IIO3
R205
J17 LD8
BOT OUT
TOP IN
J15
R202
IO2
T11
LD9
R199
J14
IO1
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Termination
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ARTIQ
IC47
TO KASLI/METLINO VHDCI
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TOP IN
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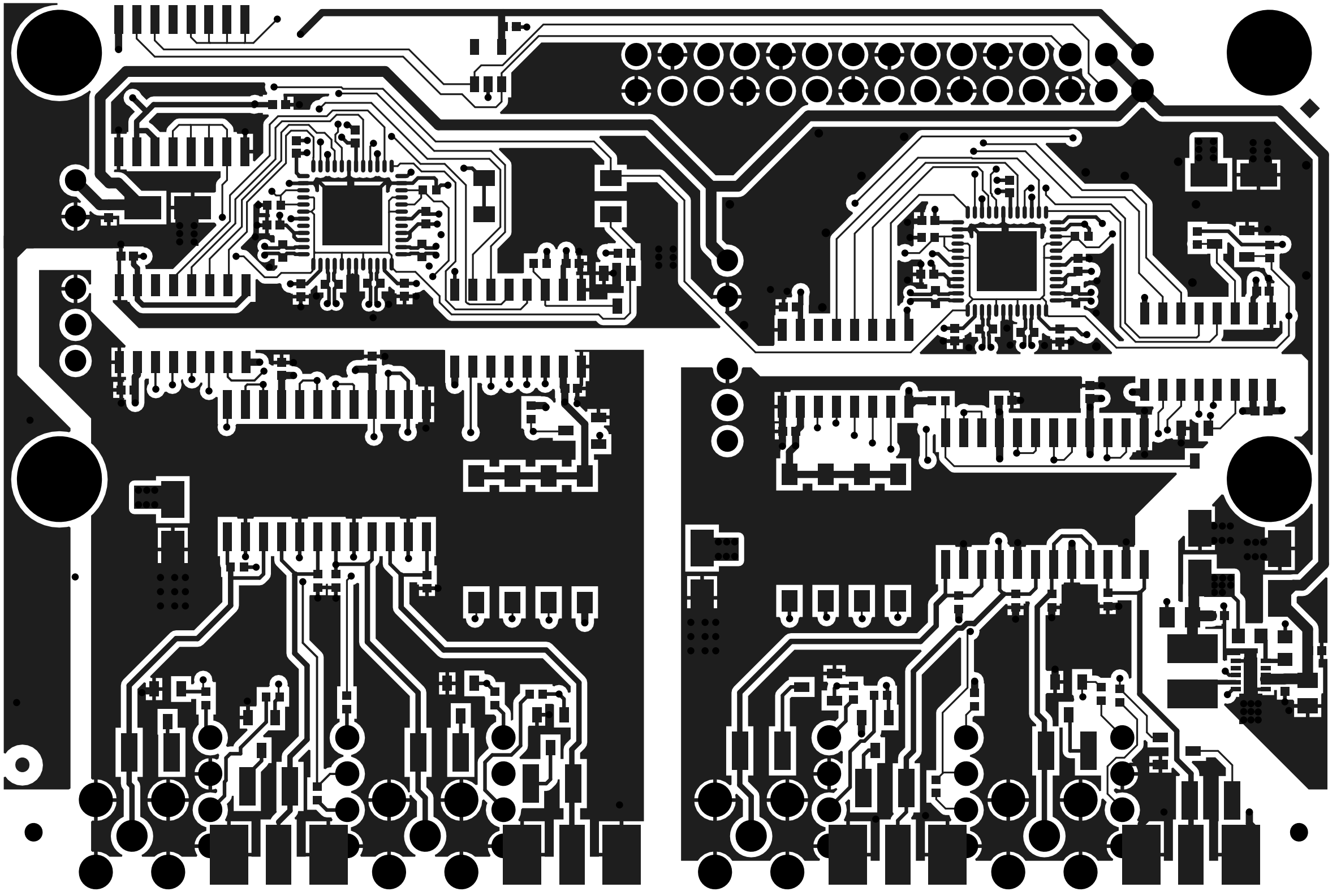
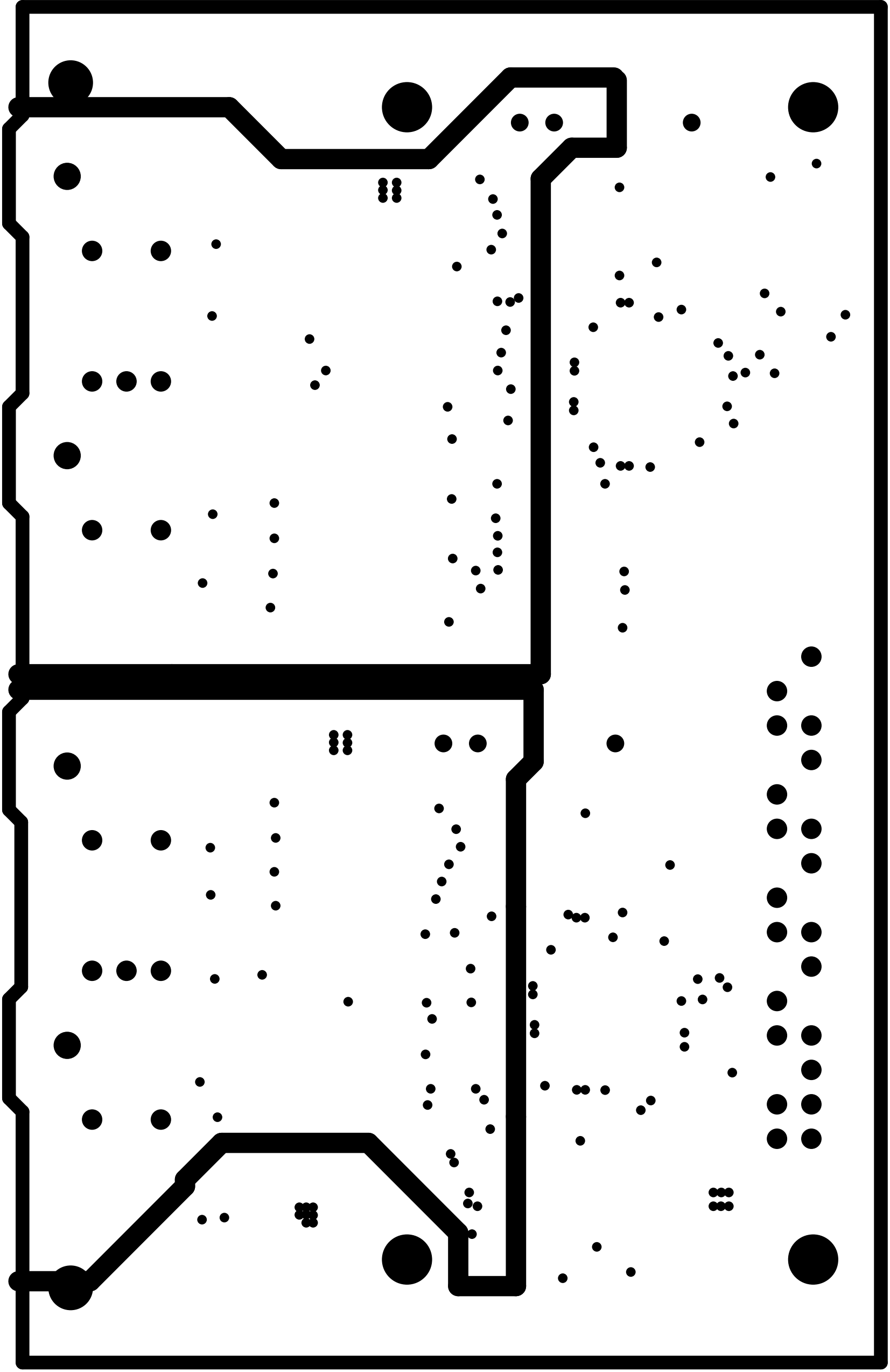
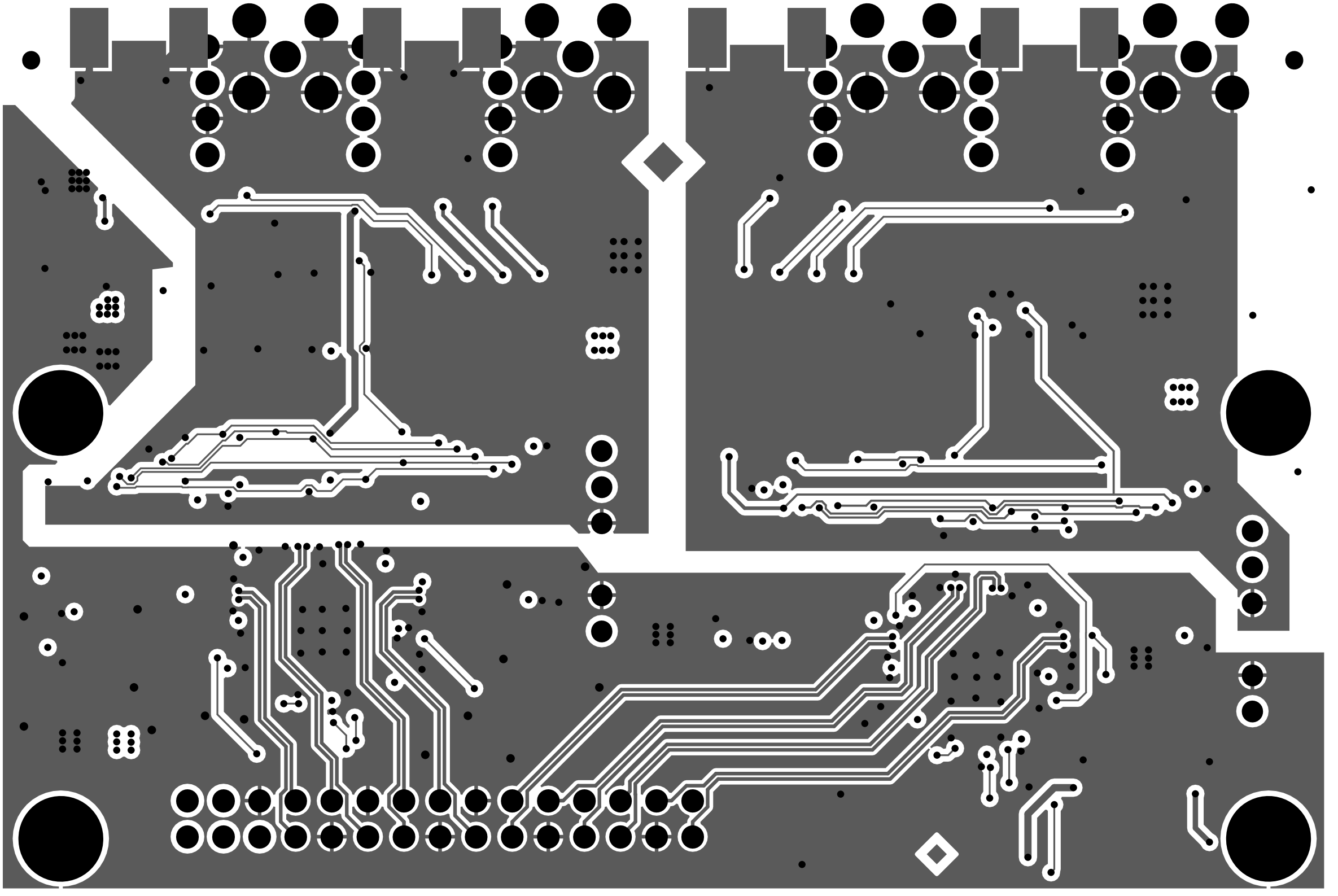
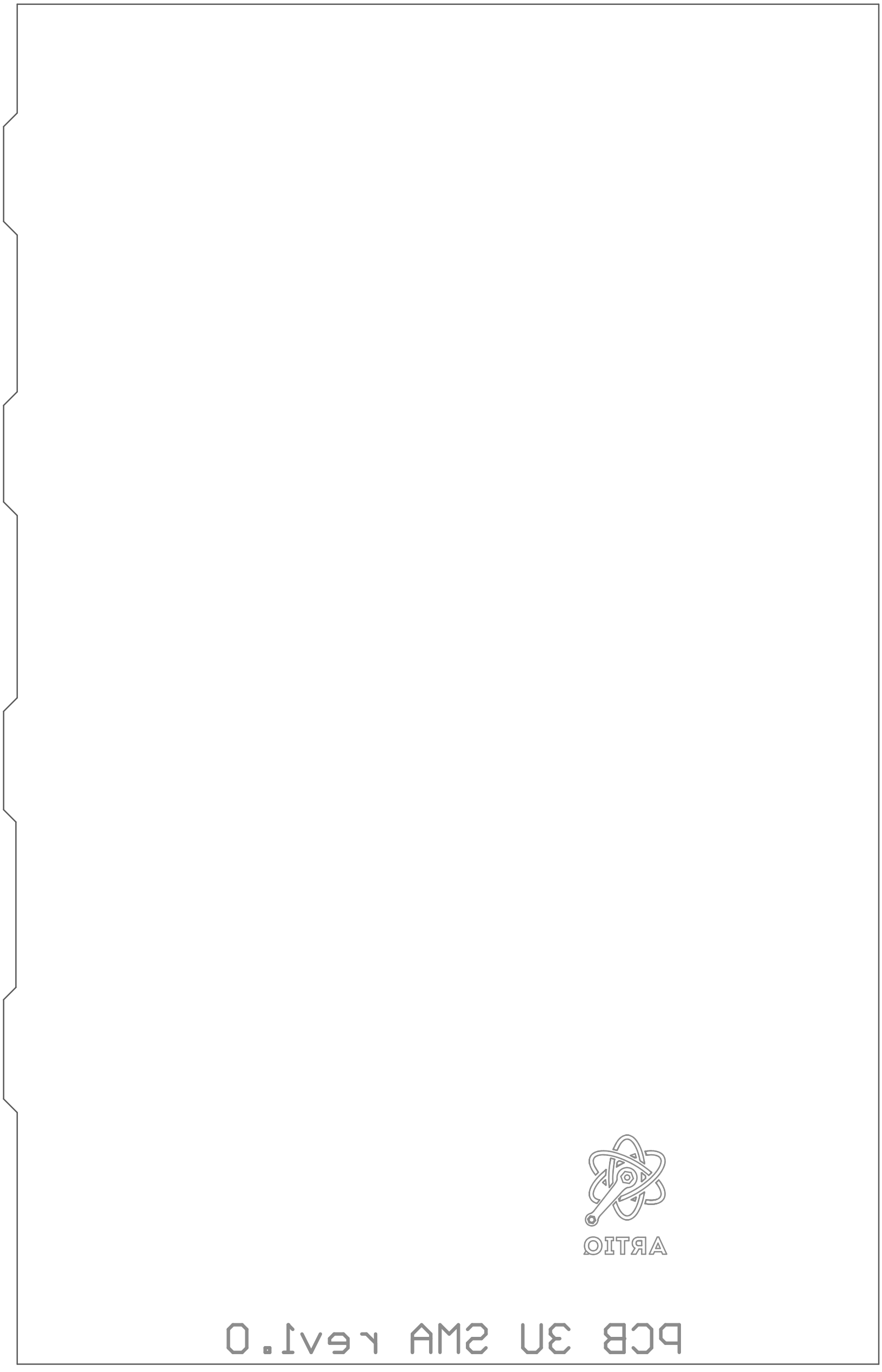
R225
IO6
T15
LD12
IC48
30
R223
IC51
T14
J19
IO5
IC49
BOT OUT
TOP IN
PCB 3U SMA rev1.0








ARTIO
PCB 3U SMA rev1.0