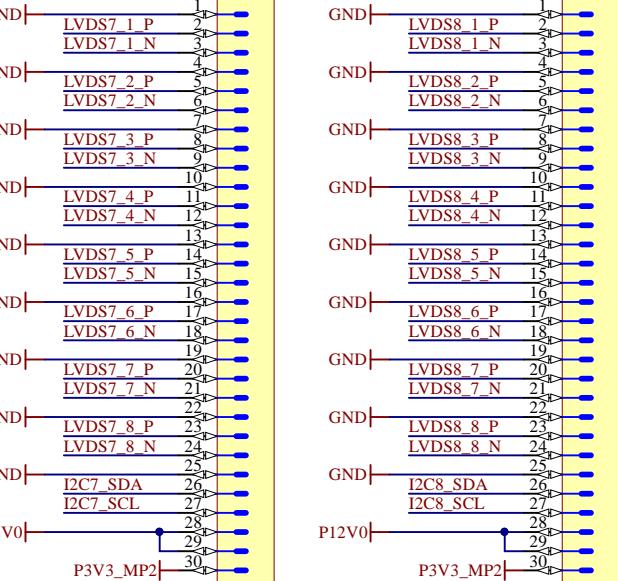
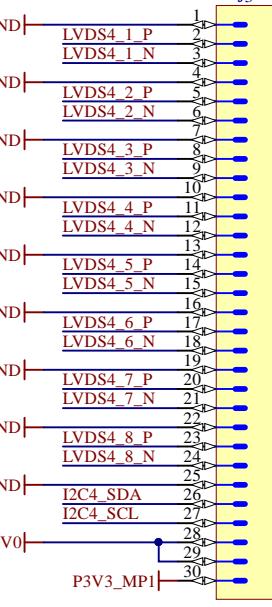
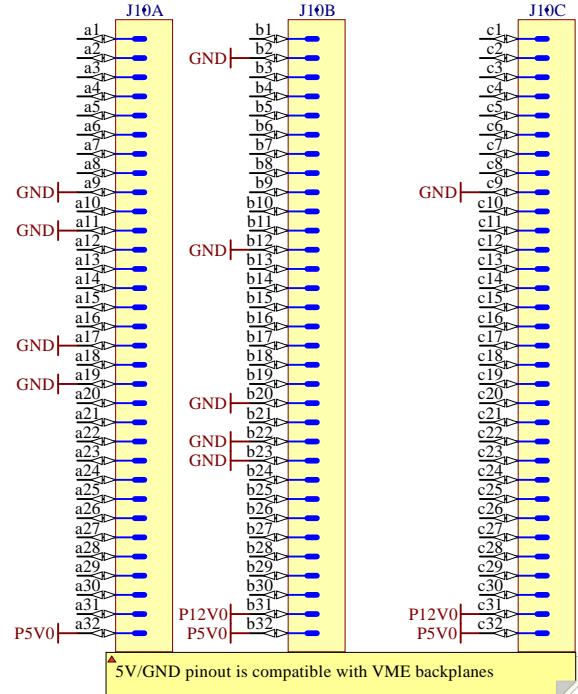
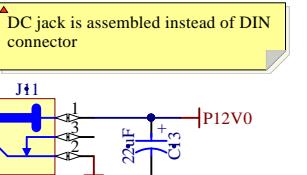
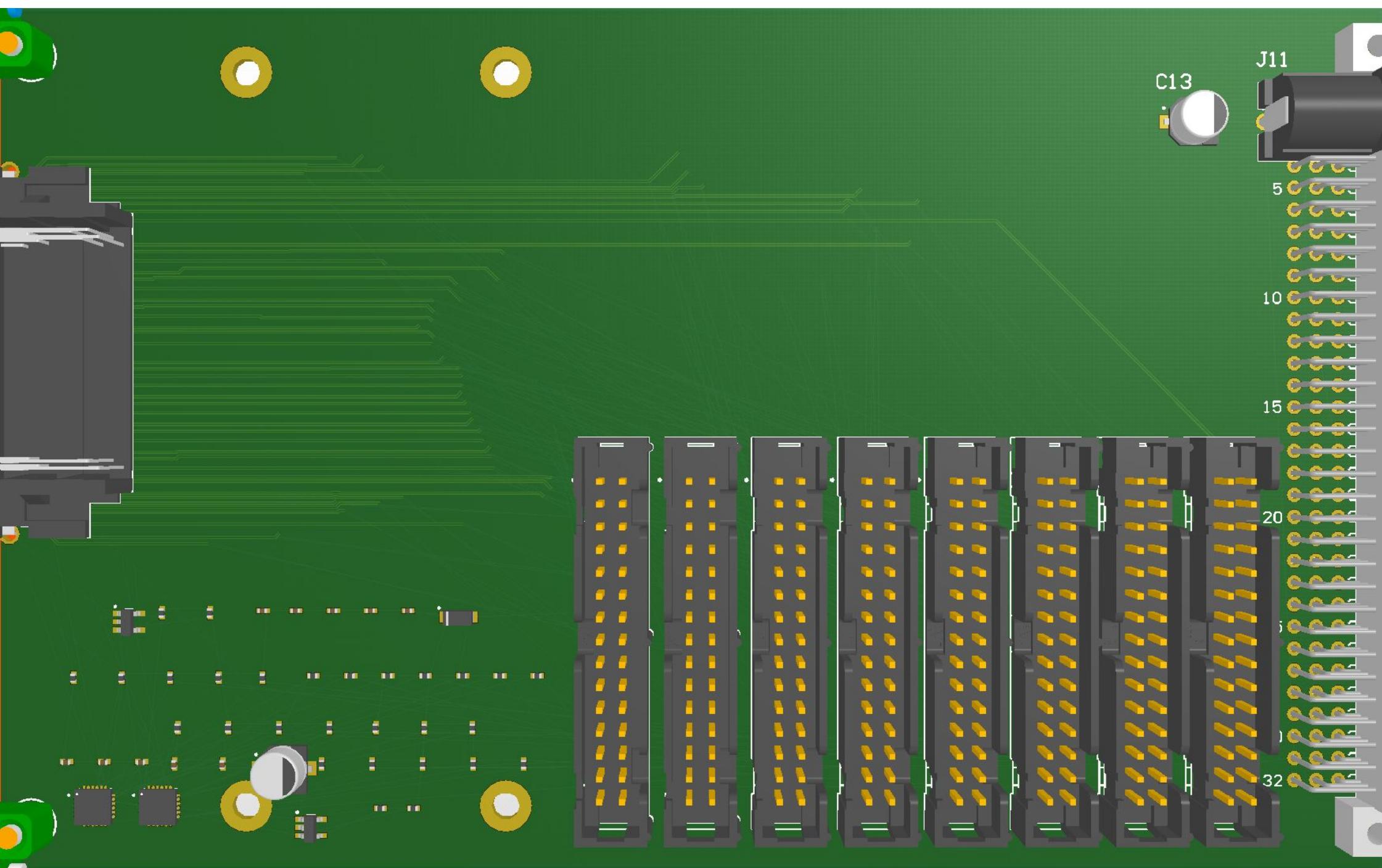
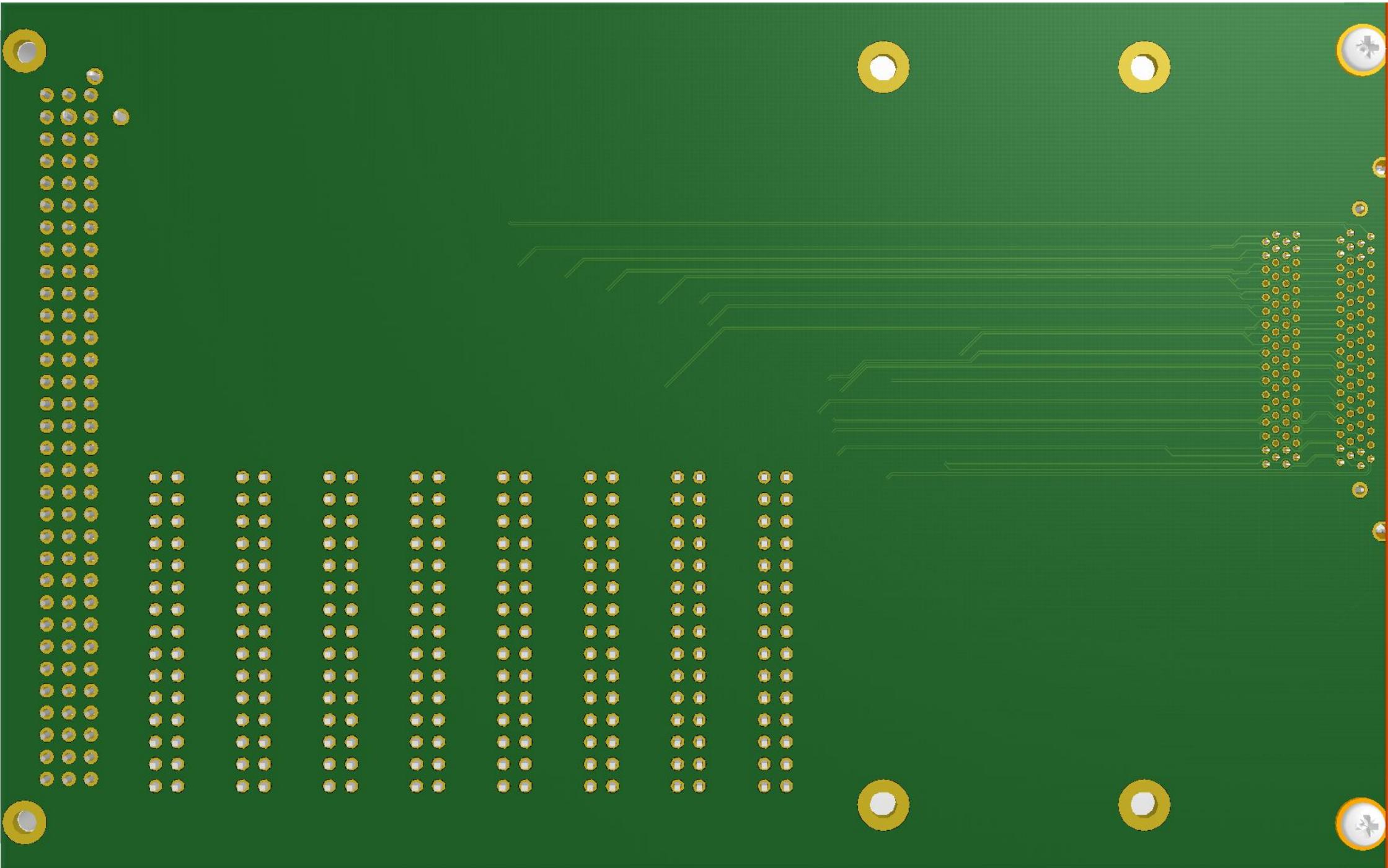


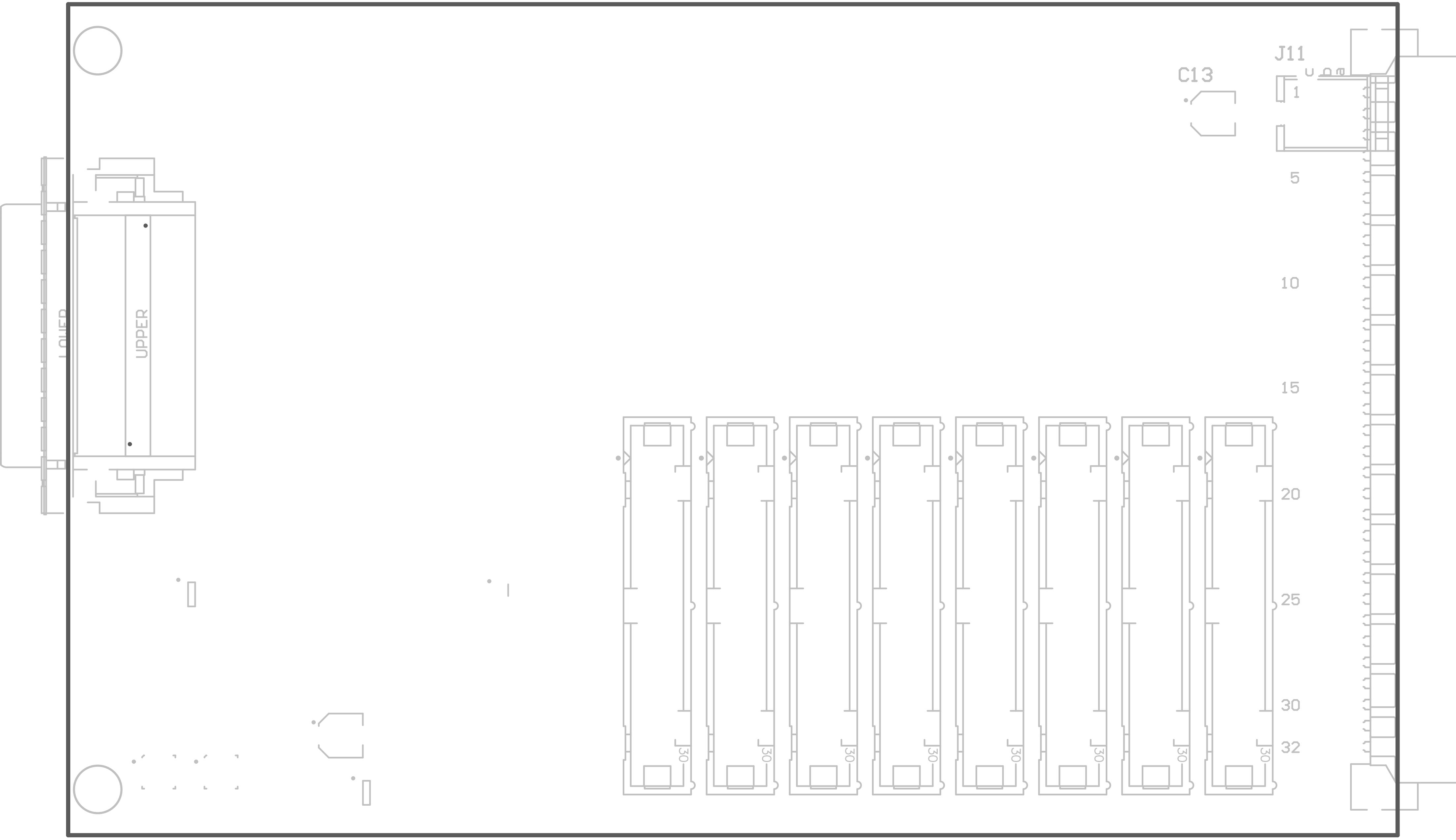
Interfaces with Sinara / Metlino
all diff lines are bidir LVDS, 1.8V
I₂C is LVCMOS, 3.3V
P3V3 is management power, up to 100mA

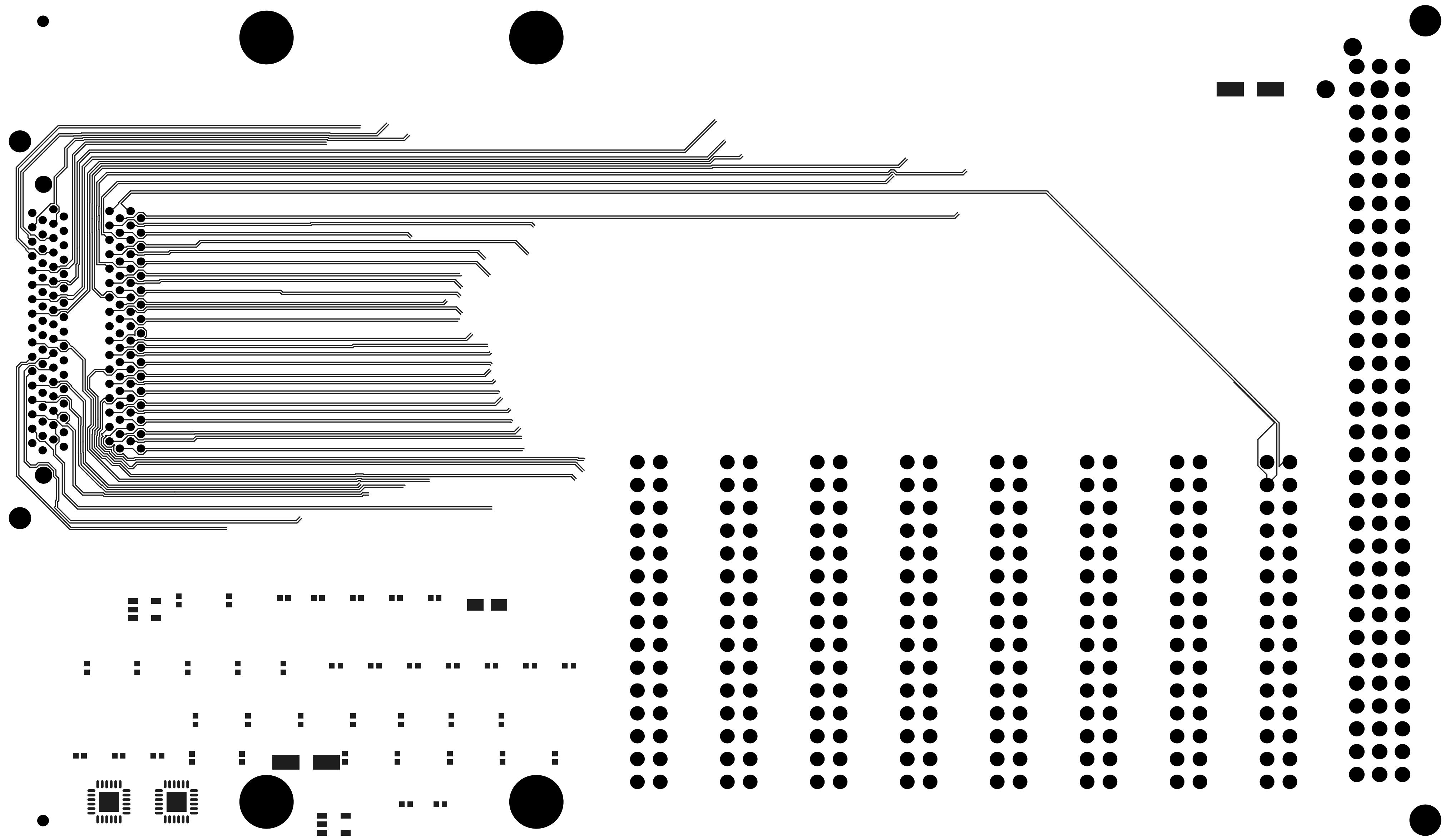


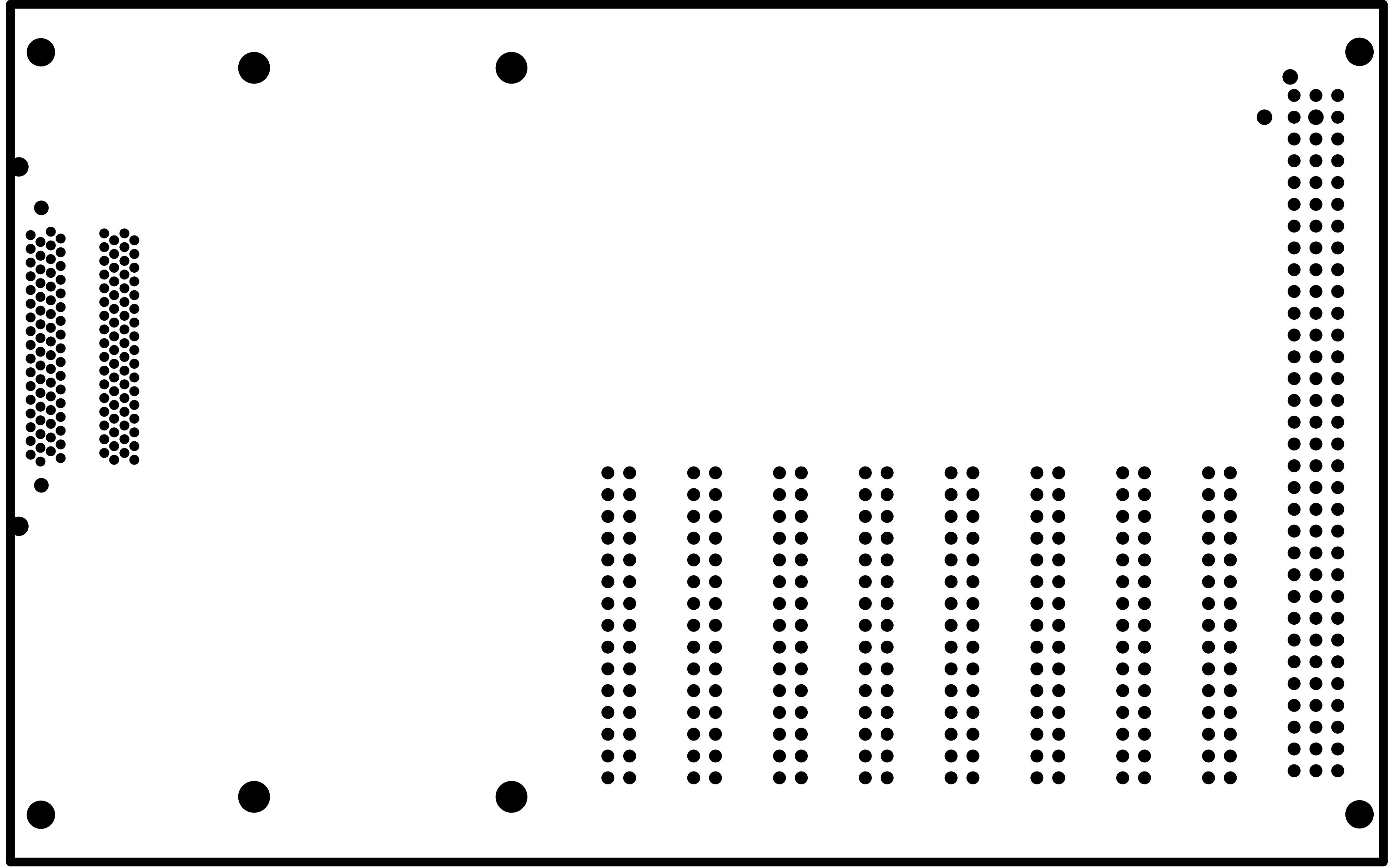
Interfaces with Metlino/Kasli ecosystem
extension boards
all diff lines are bidir LVDS, 1.8V
I₂C is LVCMOS, 3.3V
P3V3 is management power, up to 20mA

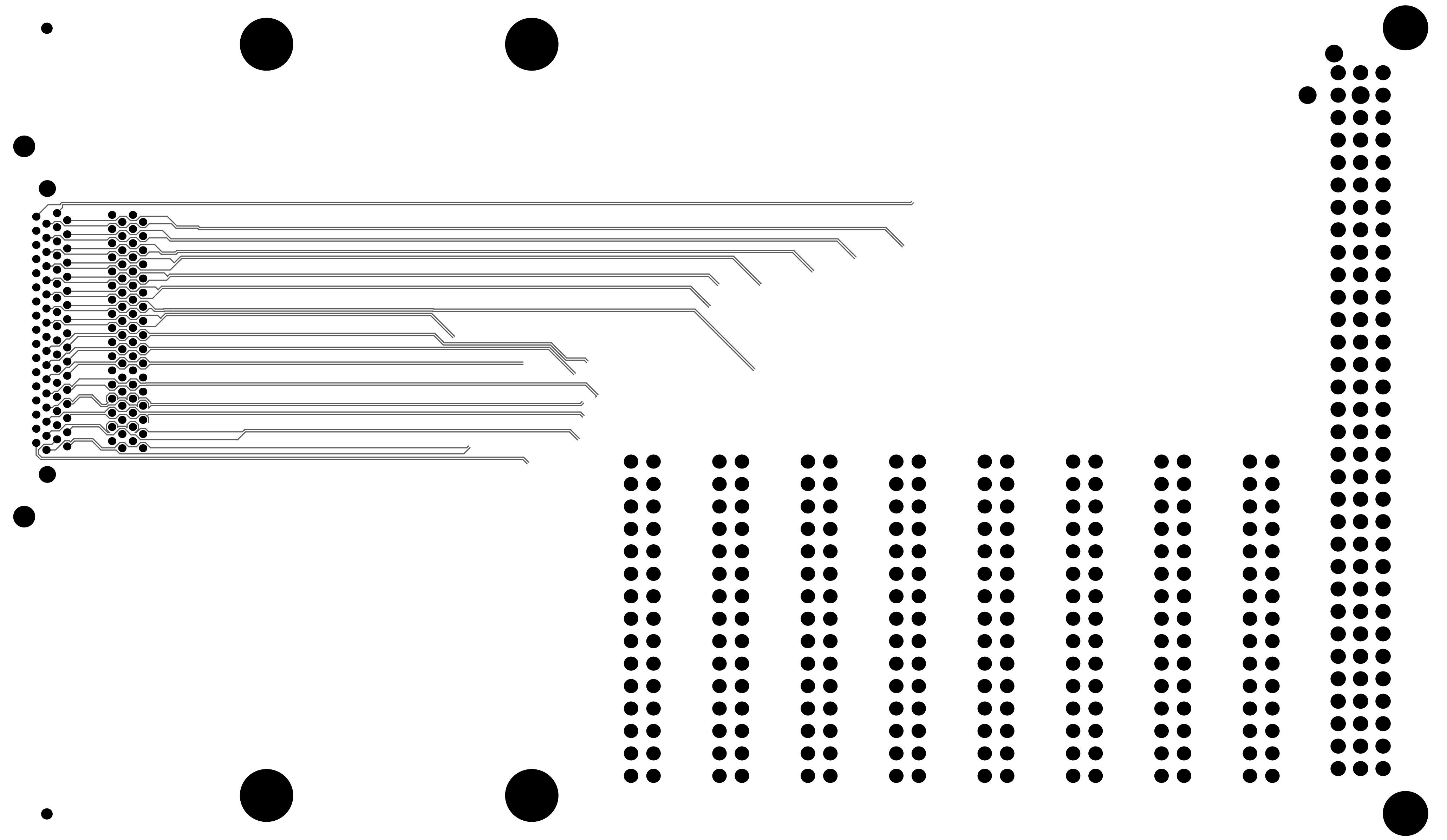












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