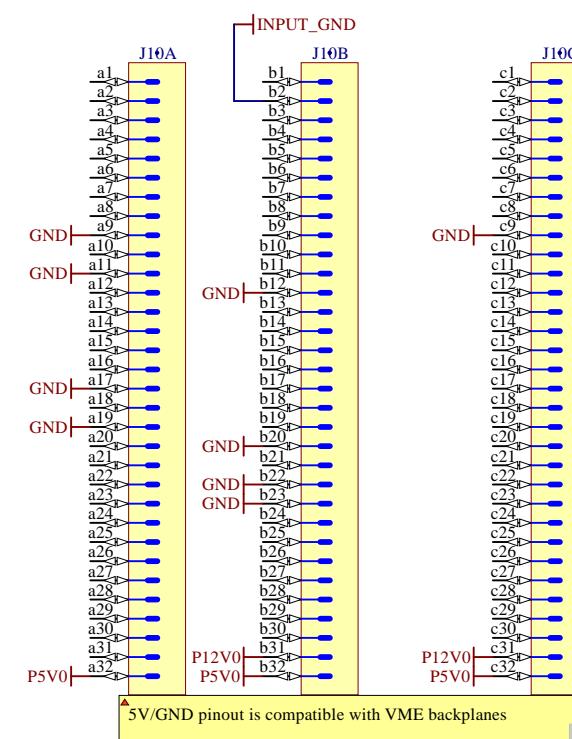
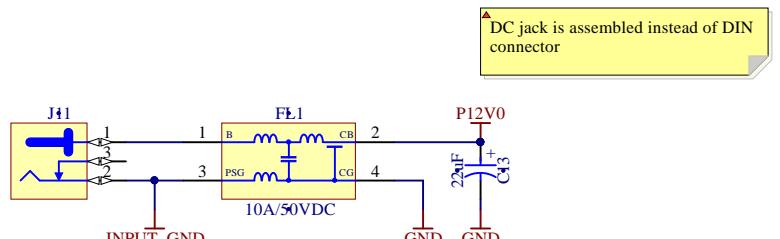


- Interfaces with Metlino/Kasli ecosystem extension boards
  - all diff lines are bidir LVDS, 1.8V
  - I<sub>C</sub> is LVCMOS, 3.3V
  - P2V2 is management power, up to 20mA

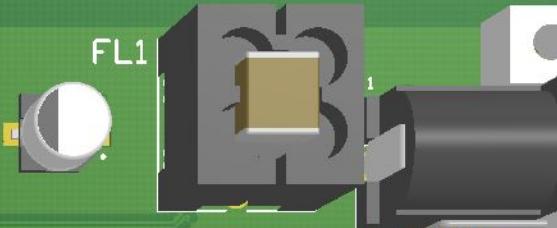
- Interfaces with Sinara / Metlino  
all diff lines are bidir LVDS, 1.8V  
I2C is LVCMOS, 3.3V  
P3V3 is management power, up to 100mA



5V/GND pinout is compatible with VME backplane



FL1



PCB VHDCI breakout rev1.0

To METLINO

8x Extension boards

J5

J1

J2

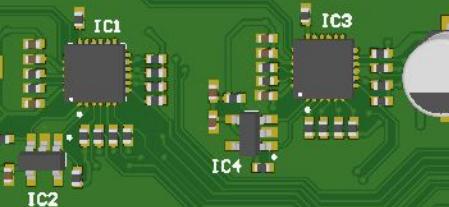
J3

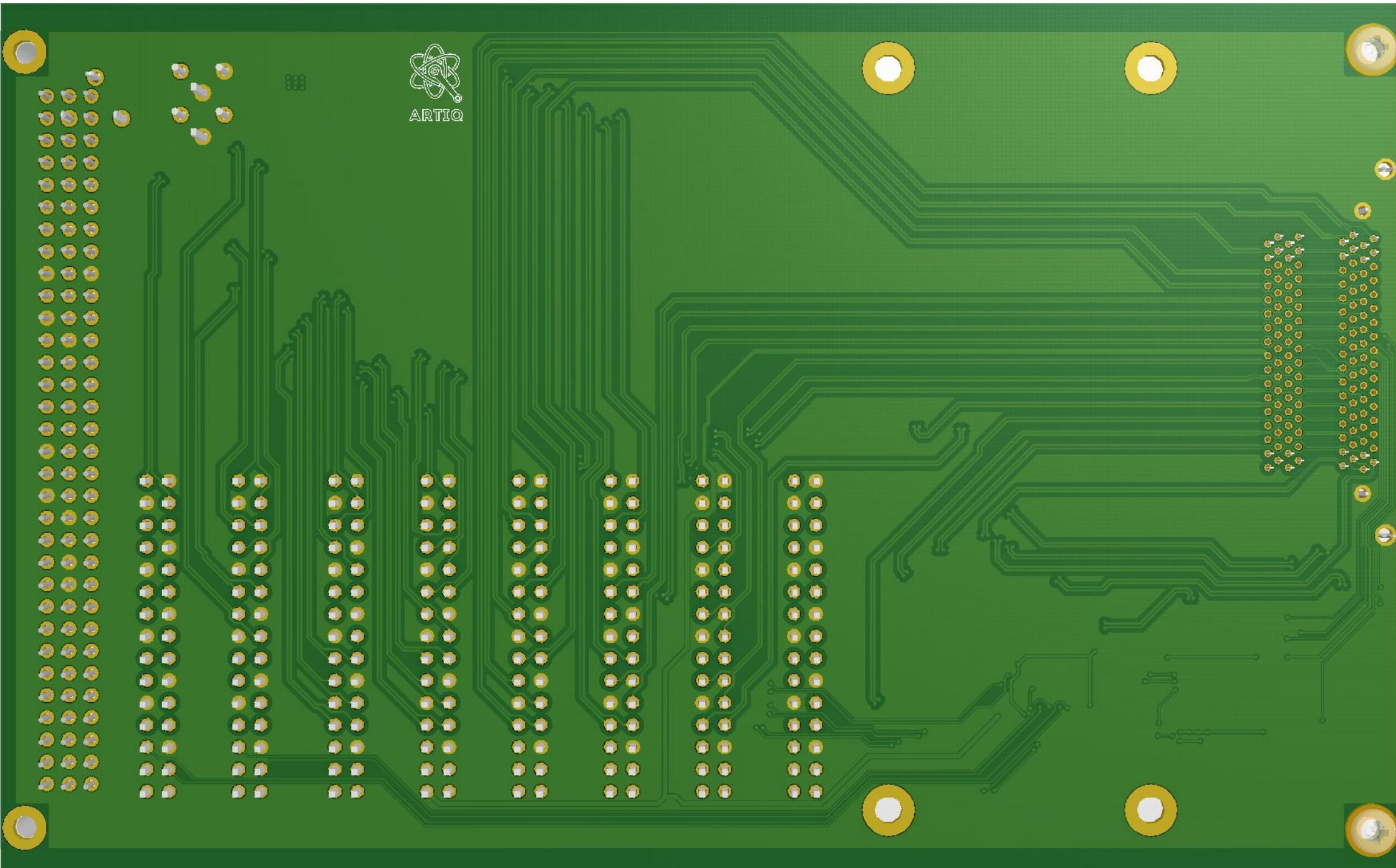
J9

J8

J7

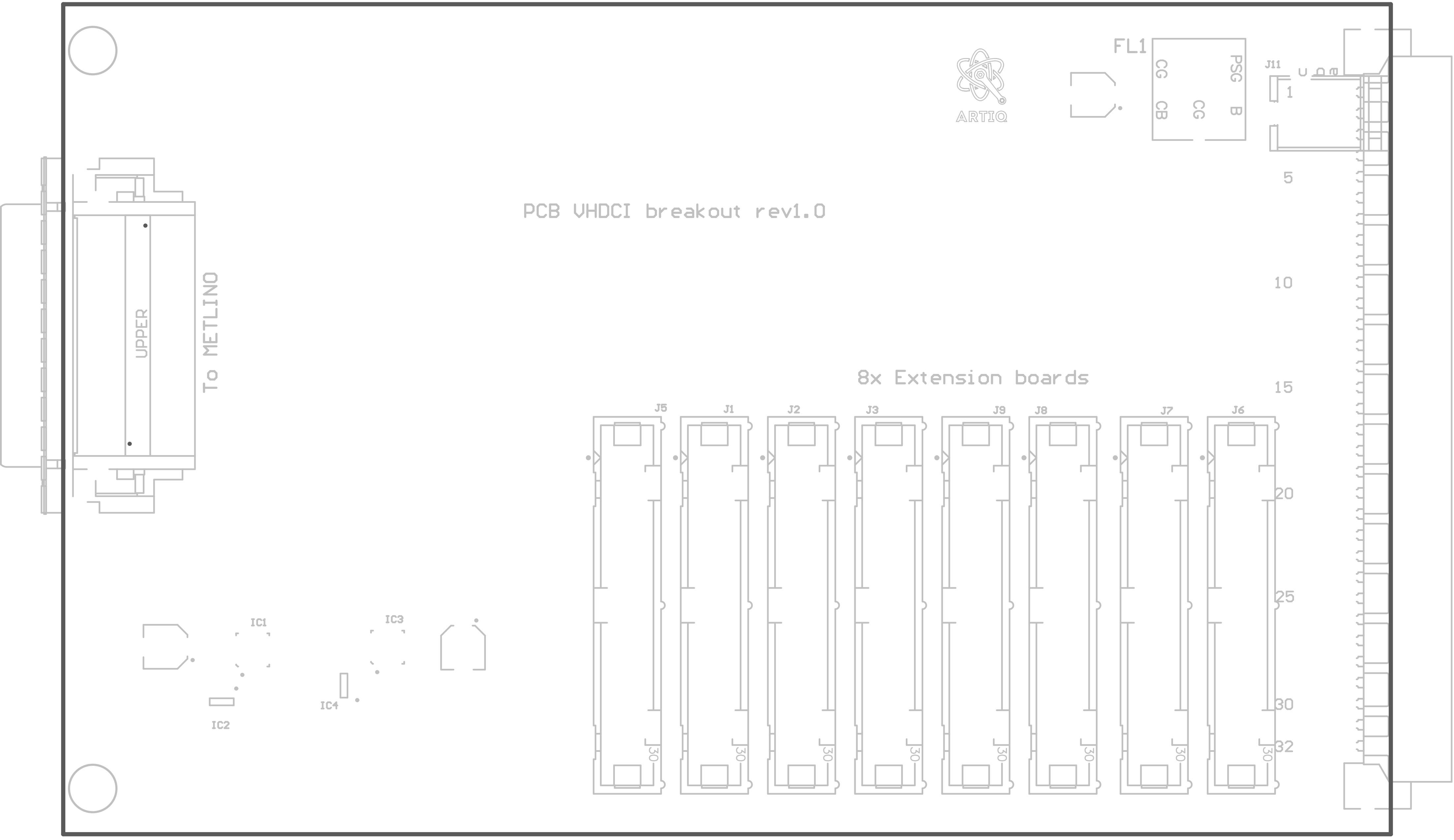
J6

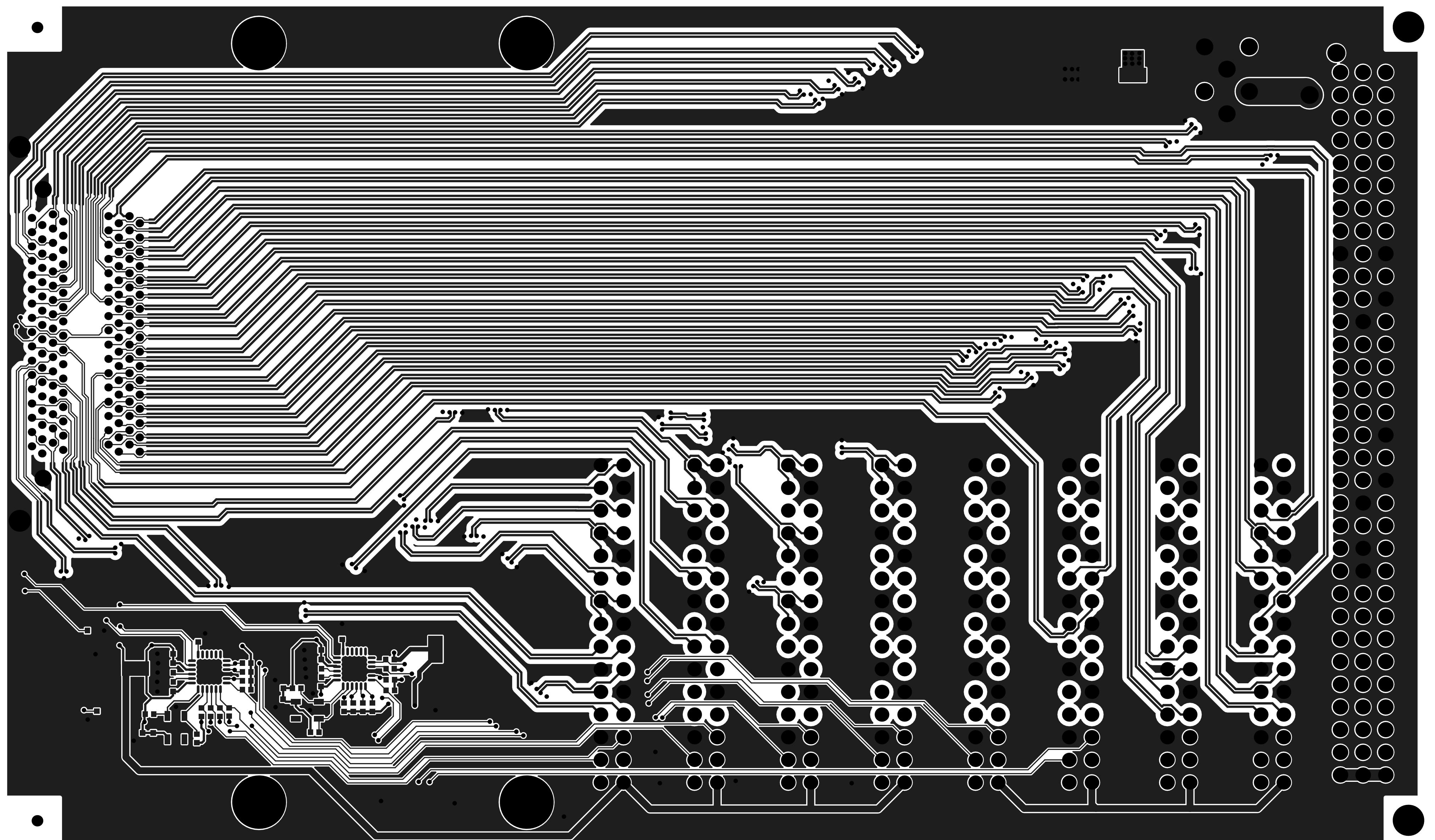


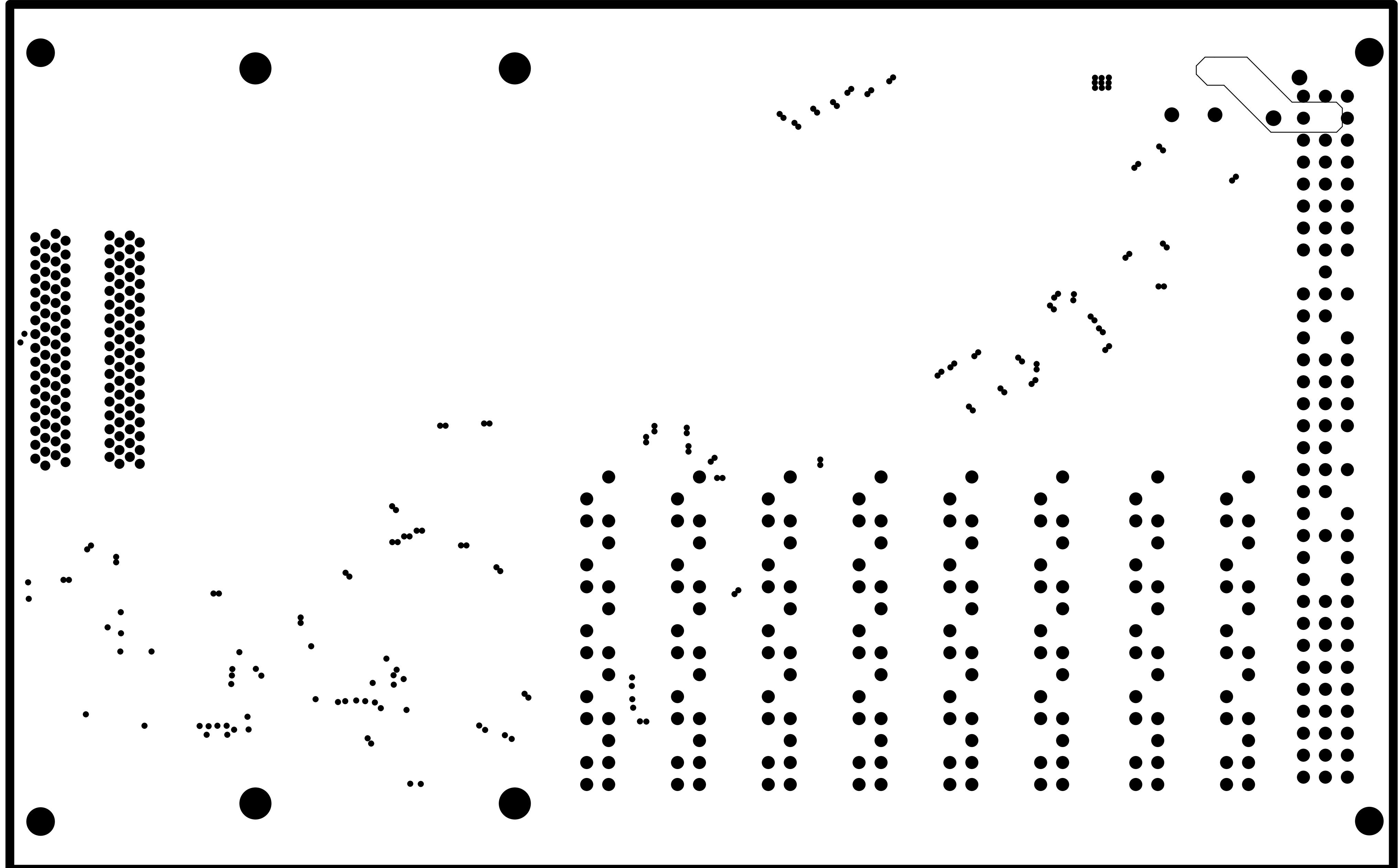




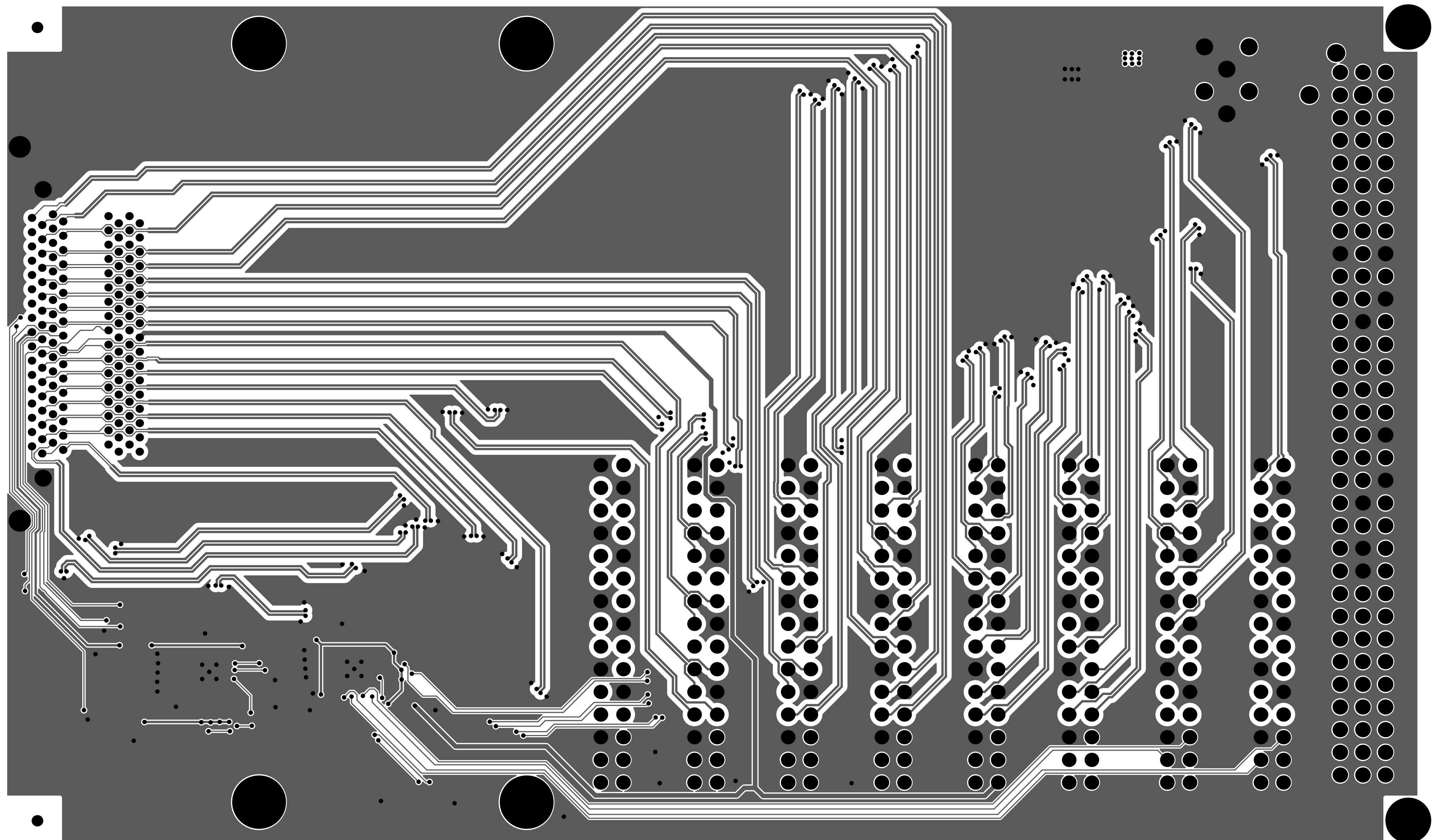
PCB VHDCI breakout rev1.0













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