

A

A

B

B

C

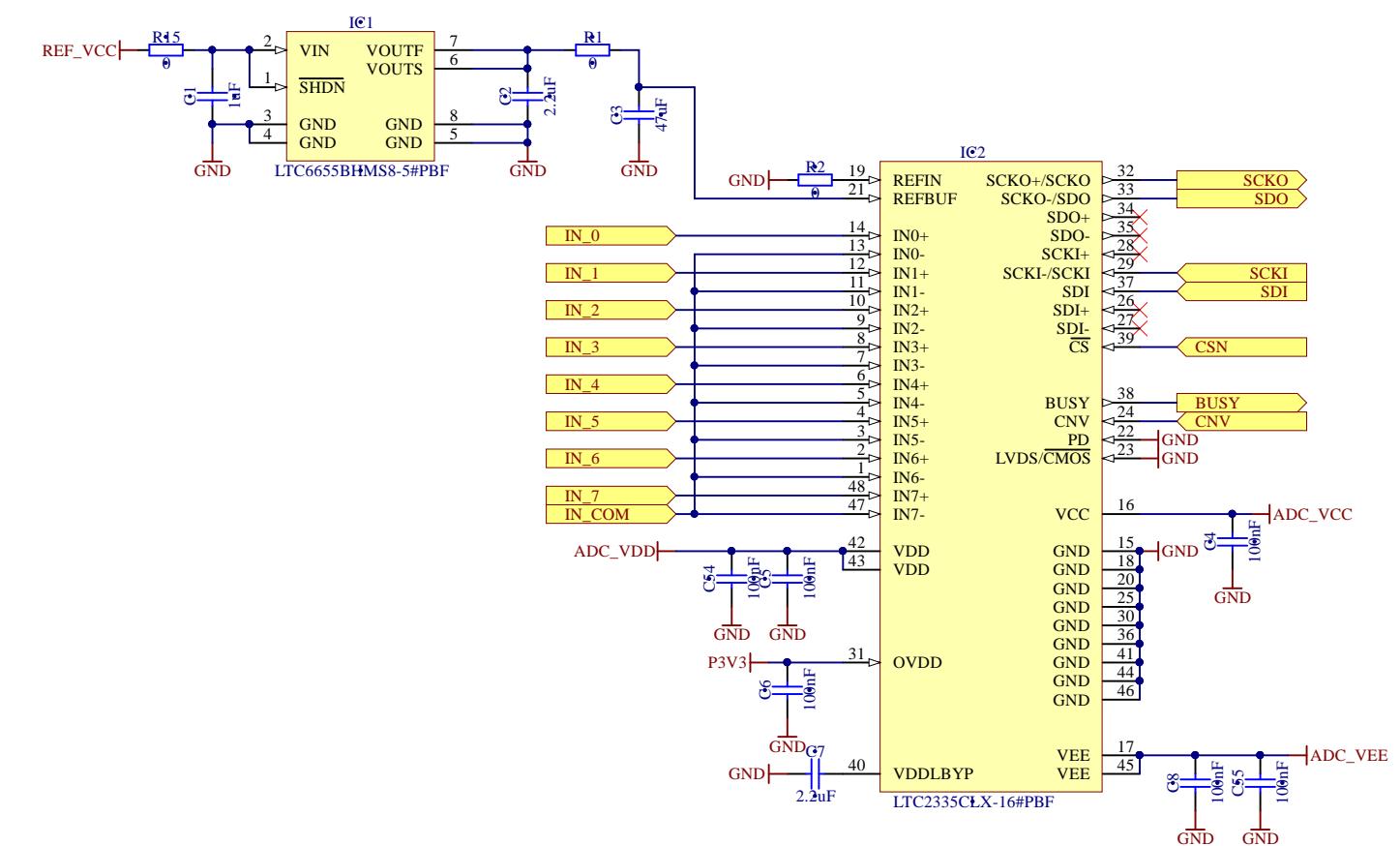
C

D

D

E

E



Project/Equipment 3U ADC

Document

***ADC + reference***

Designer G.K.	Drawn by G.K.	XX/XX/XXXX
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Last Mod. -	19.04.2017	
File ADC_8CH.SchDoc	Print Date 19.04.2017 01:12:26	Sheet of
		A3 Rev -

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Nowowiejska 15/19

ARTIQ

A

A

B

B

C

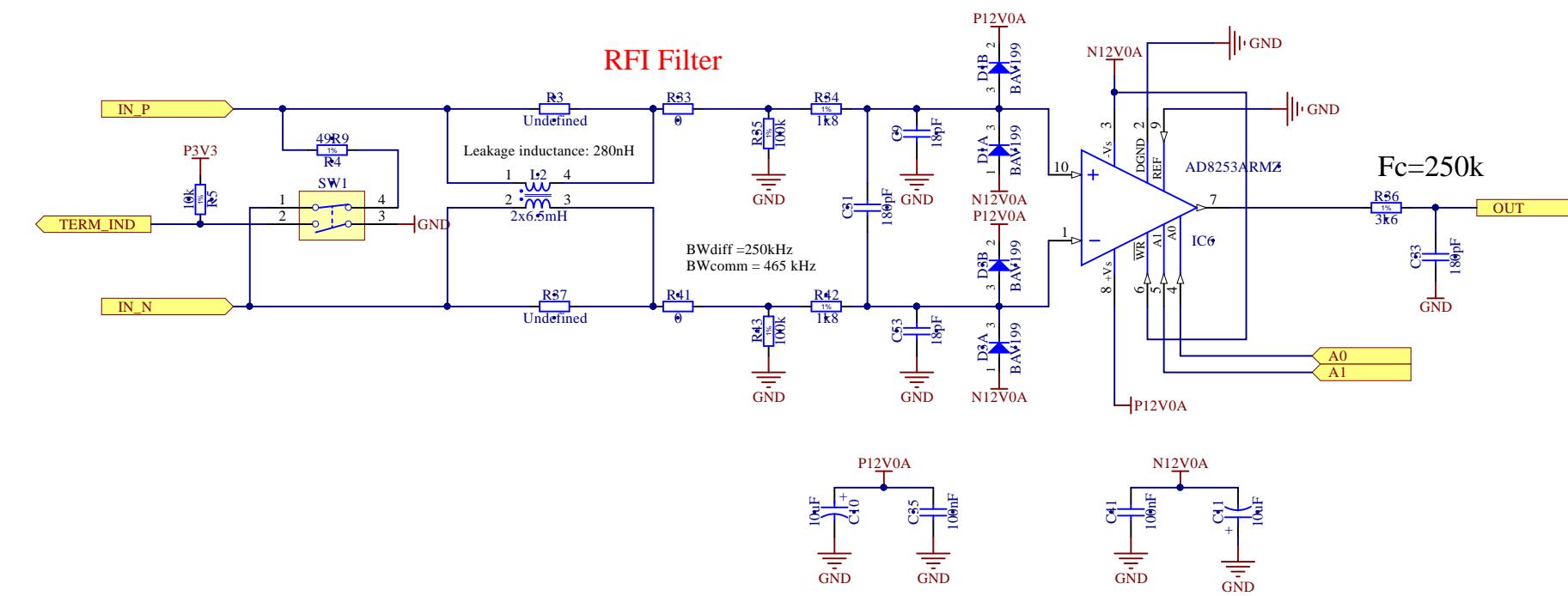
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D

D

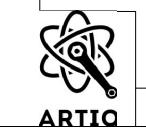
E

E



Project/Equipment 3U ADC

Document



Input chnnnel DPGA

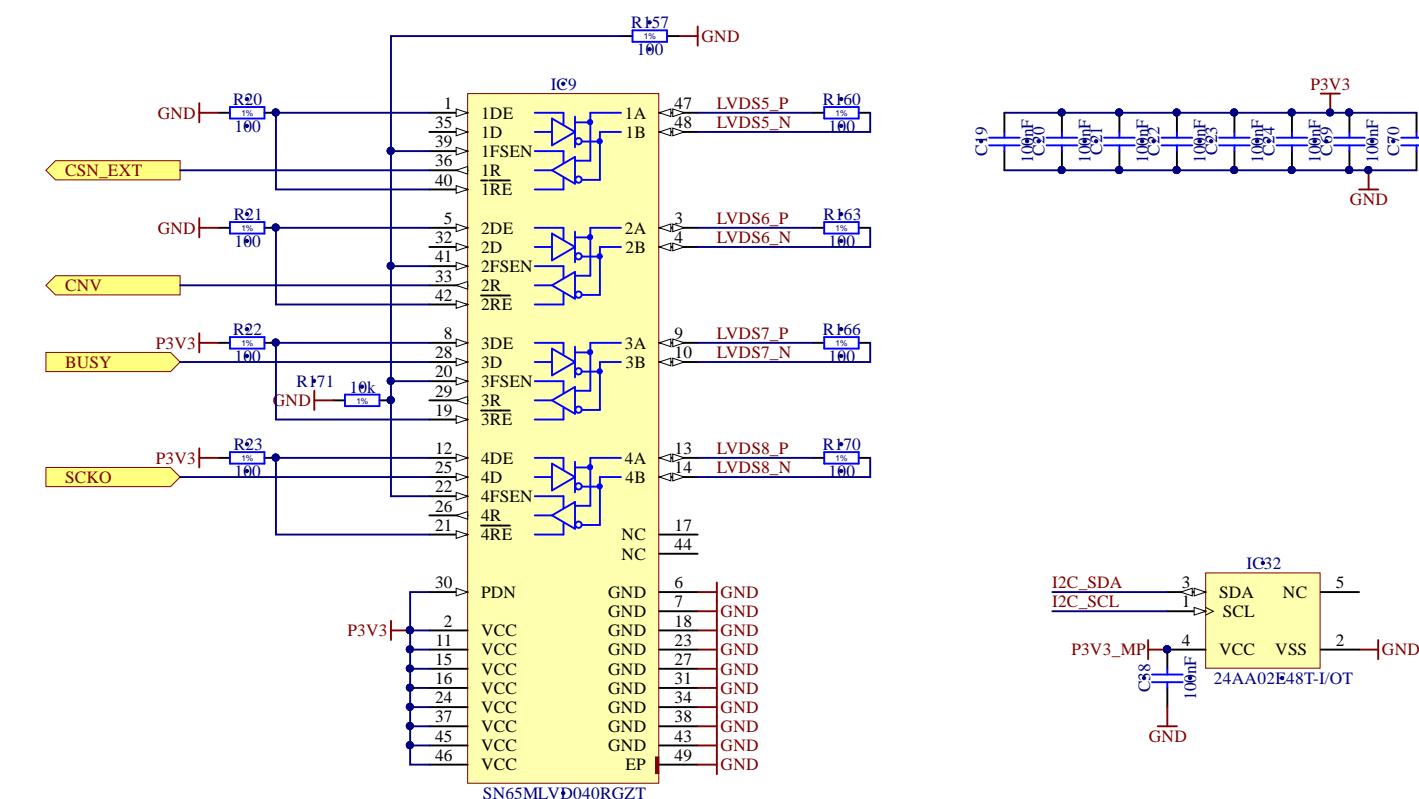
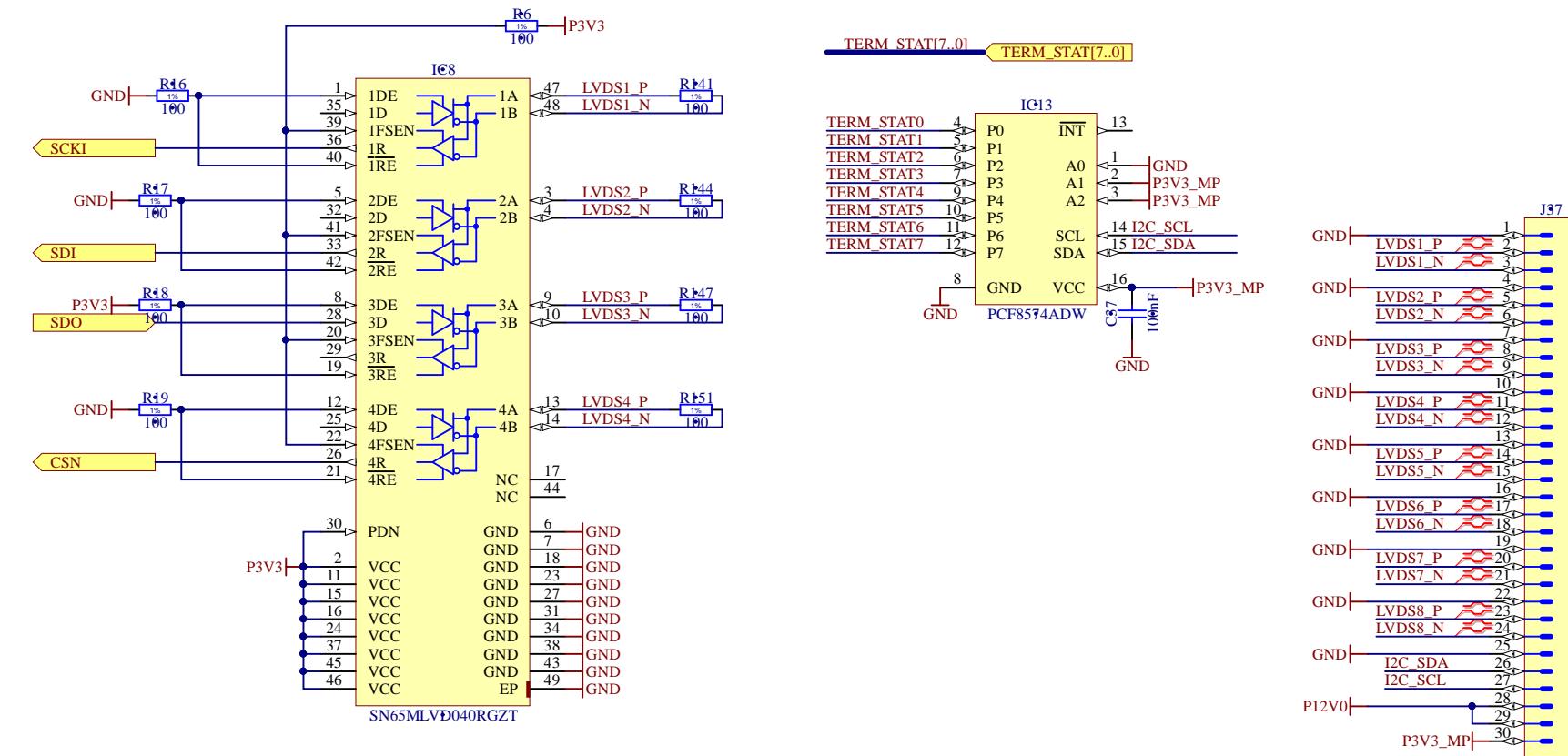
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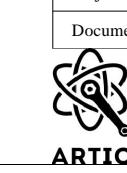
ARTIQ

This module connects to Kasli or to VHDCI Metlino breakout board
 All signals are LVDS, in case of Metlino VCC is 1.8V
 I2C is 3.3V LVCMOS
 P3V3_MP can handle up to 20mA
 P12V0 current is up to 500mA

please make a list of IC names and functions in corner of schematic for easier reading

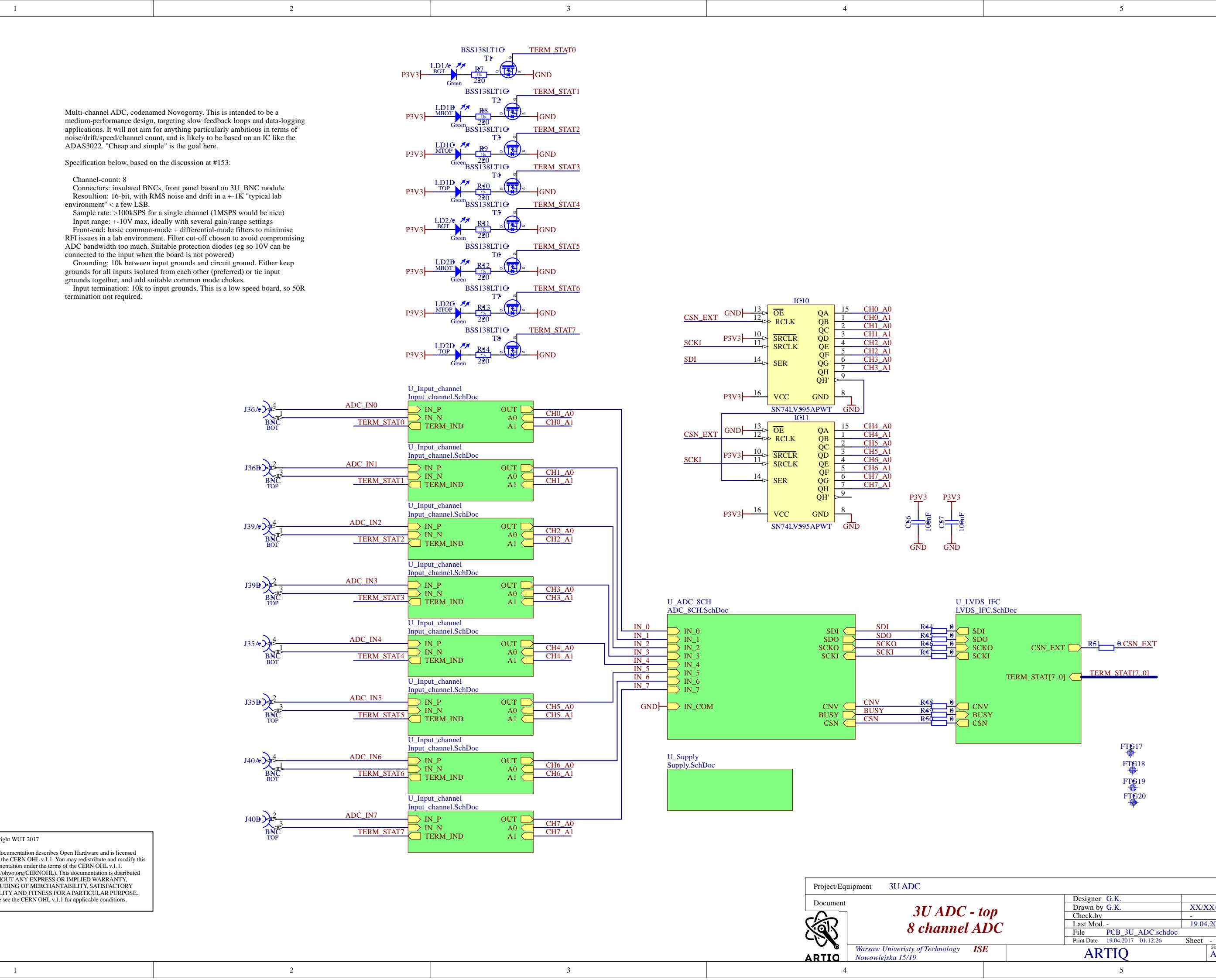


Project/Equipment 3U ADC

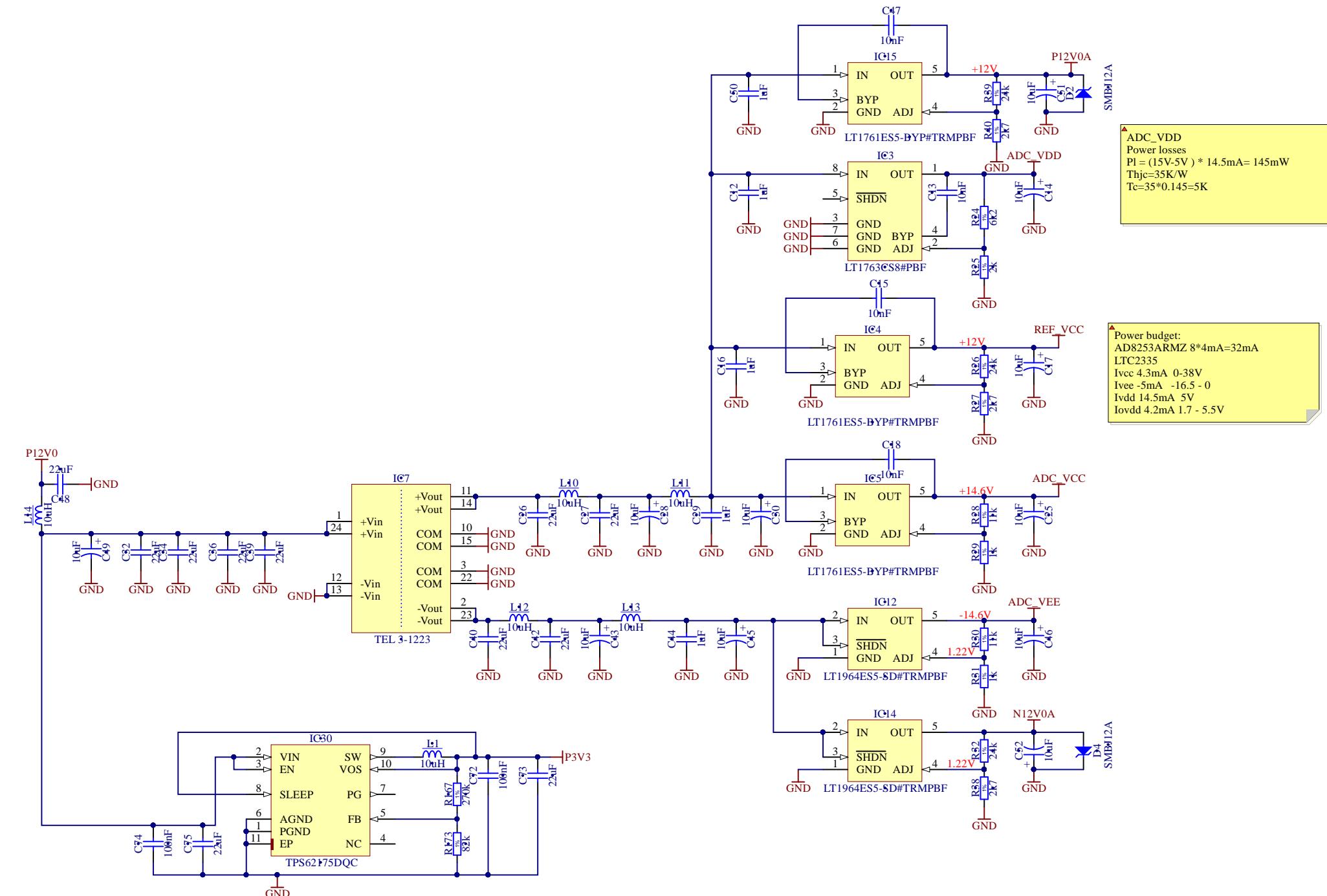


LVDS Interface I2C logic

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A

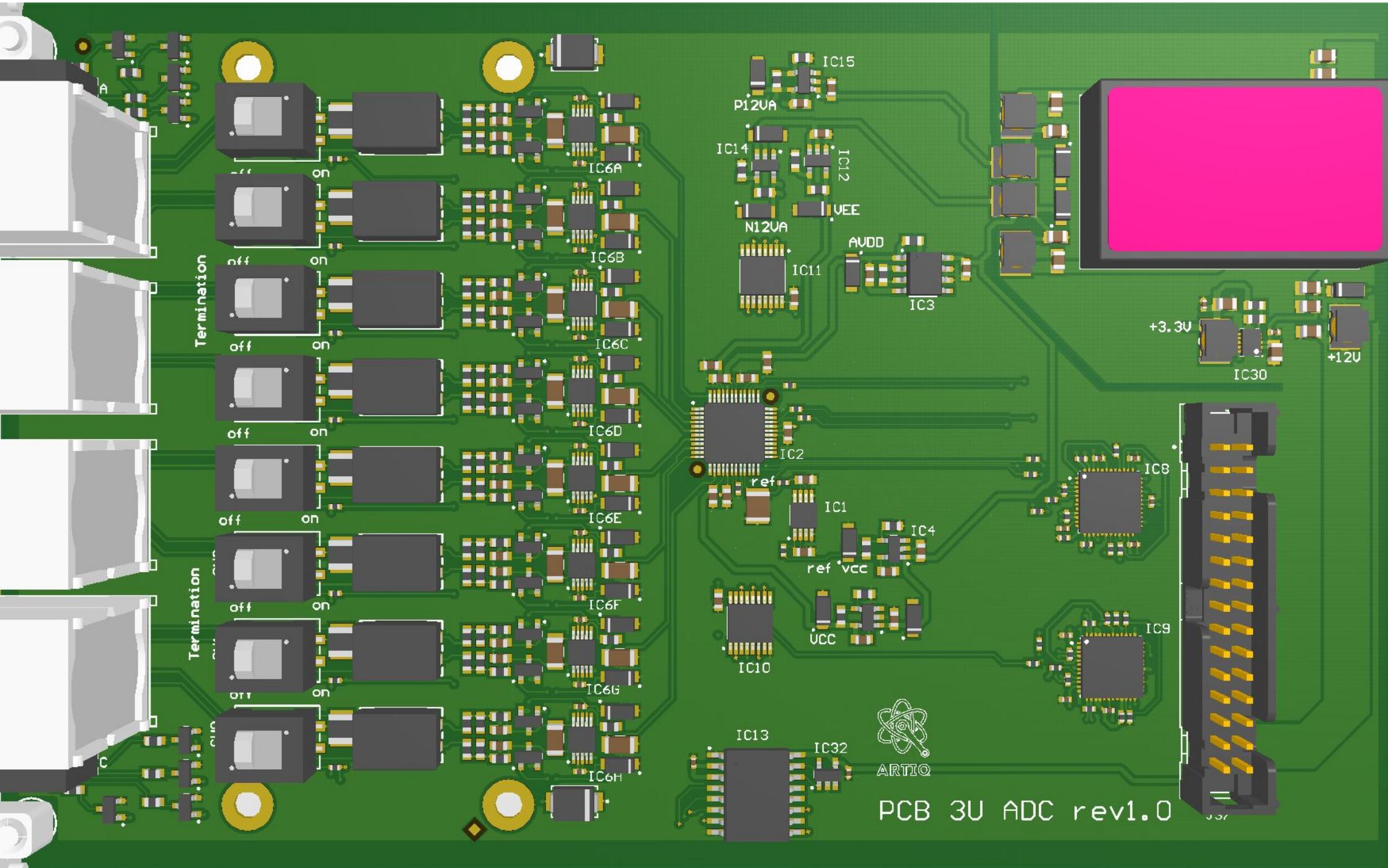


Project/Equipment 3U ADC



Power Supply converters + LDOs

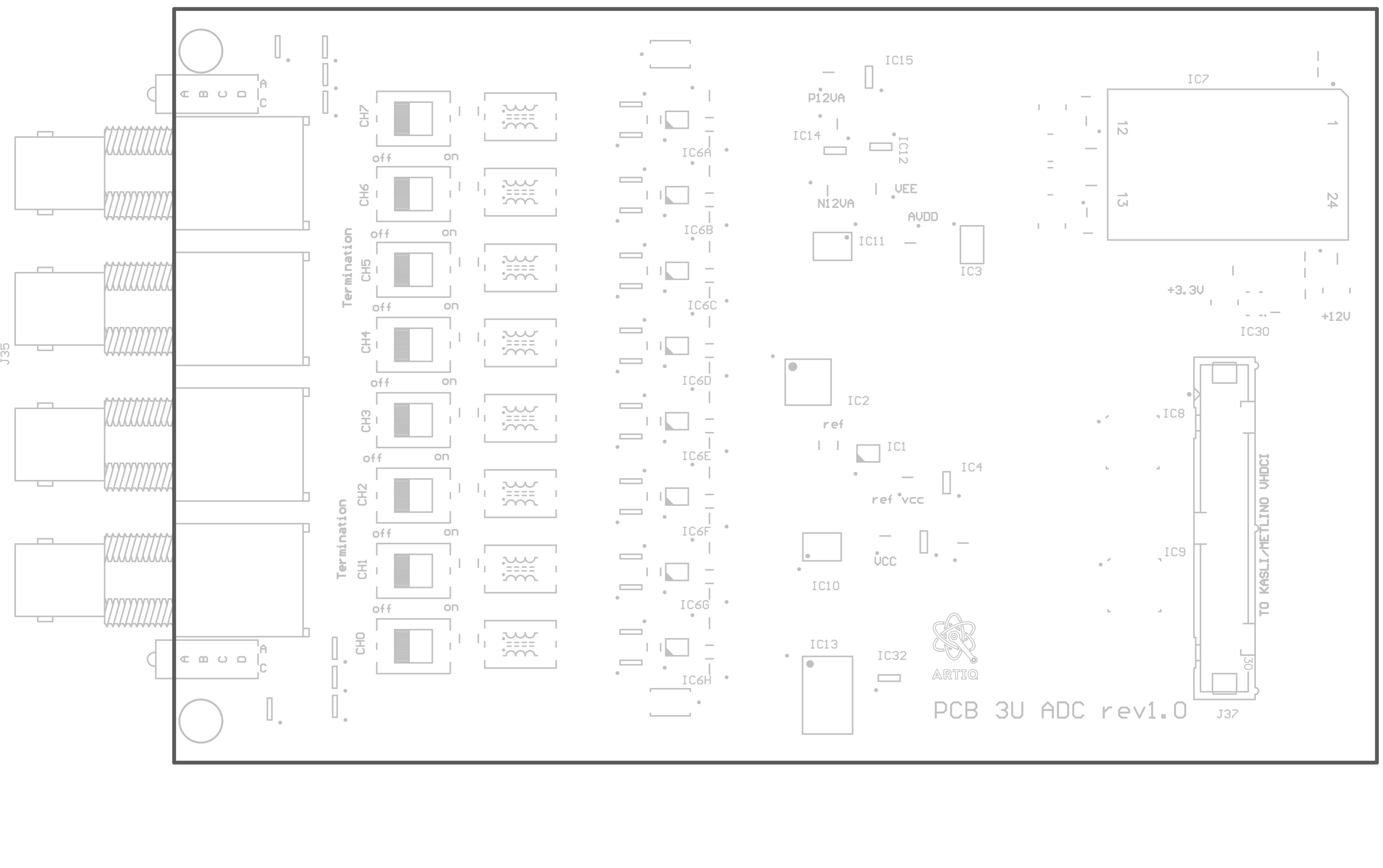
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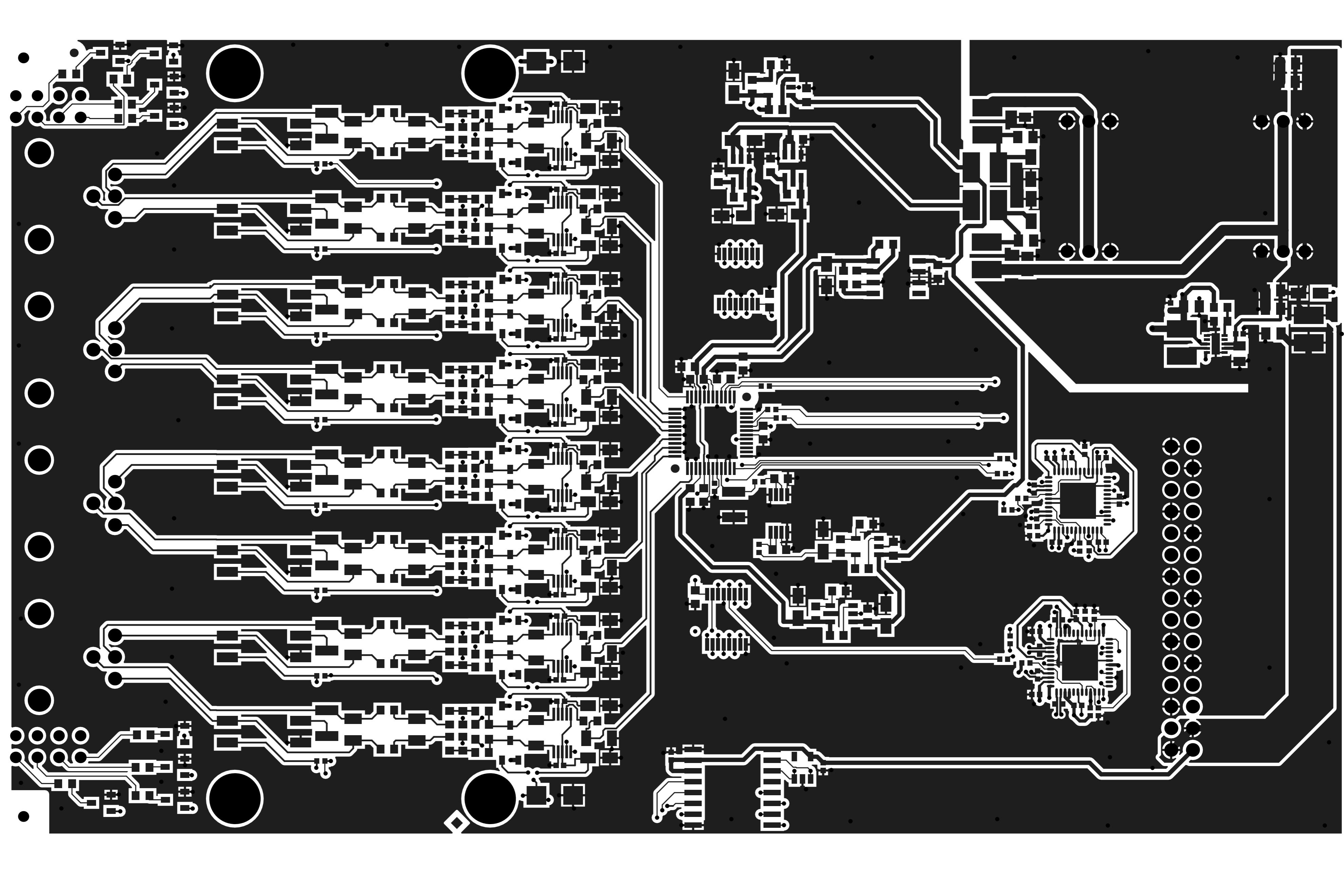


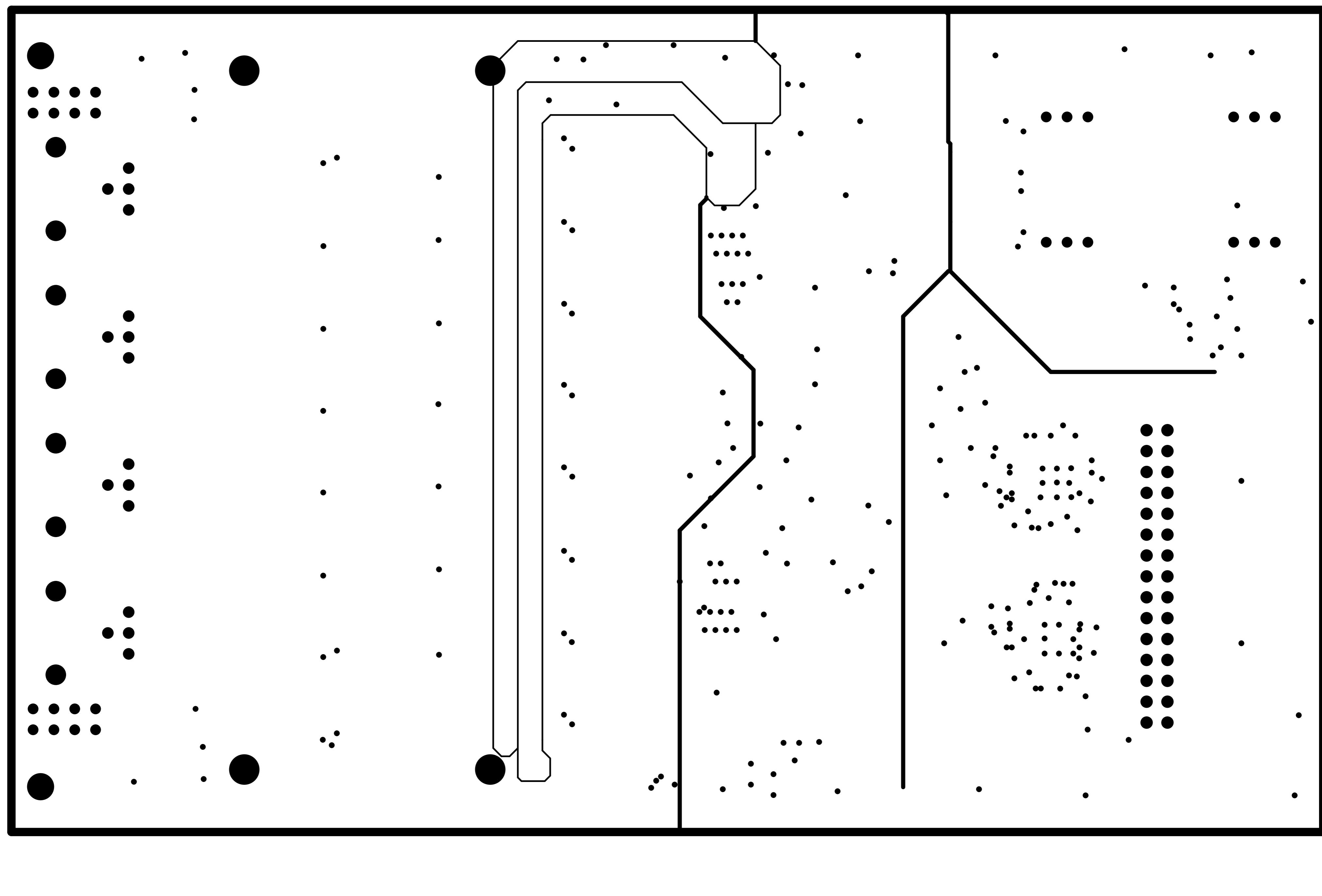


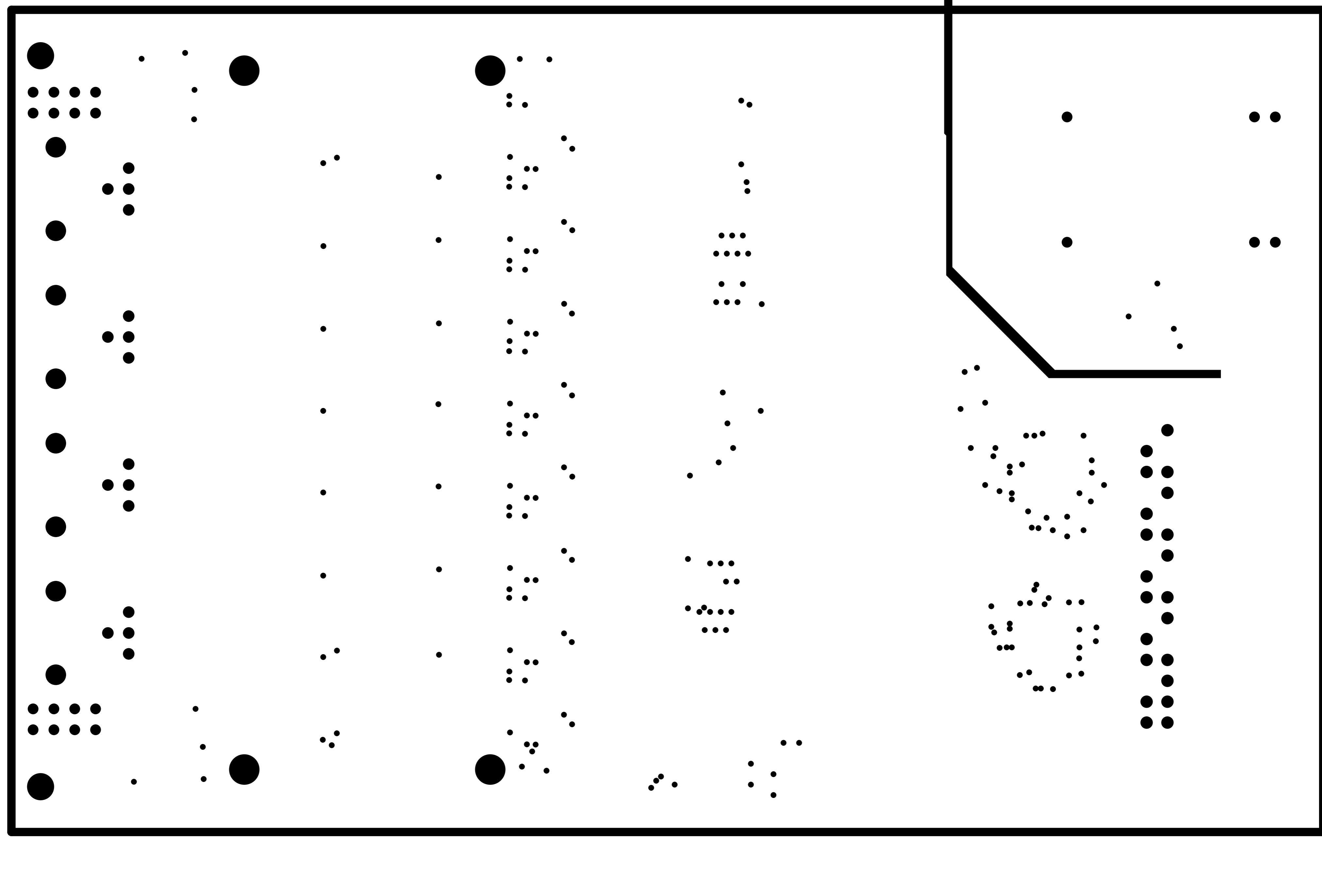
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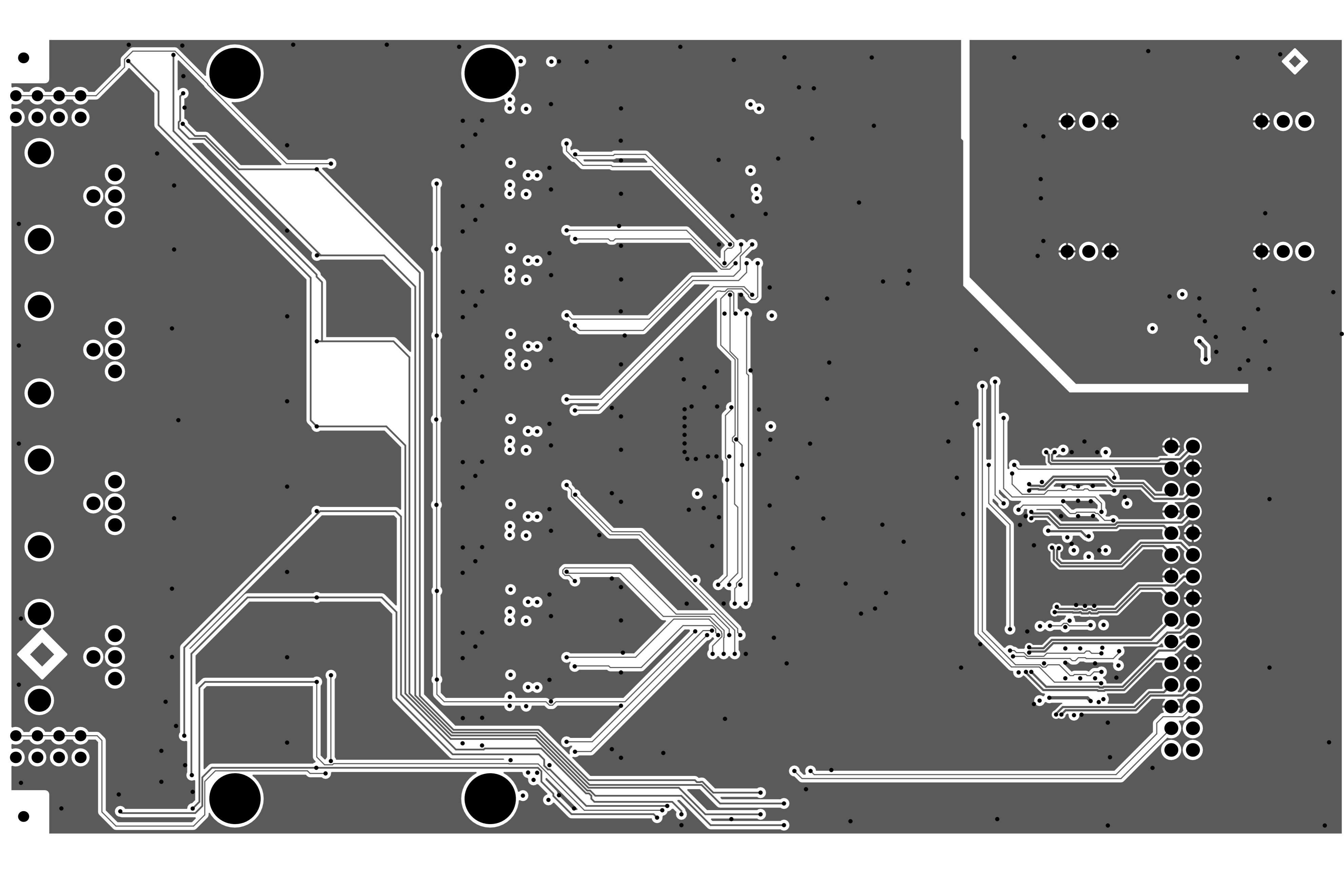
PCB 3U ADC rev1.0













ARTECO

PCB 3U ADC Rev1.0