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Project/Equipment 3U ADC

Document

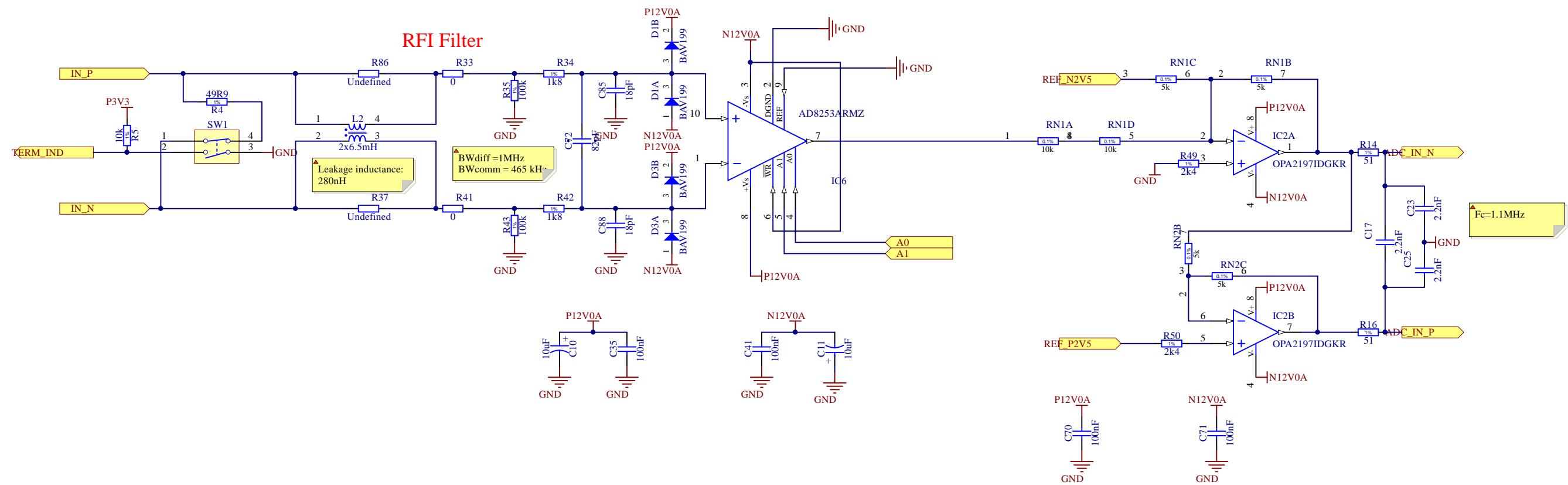
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**ADC + reference**

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Last Mod. -	17.11.2017	
File ADC_8CH.SchDoc	Print Date 18.11.2017 12:35:09	Sheet 2 of 5
		Size A3 Rev -

Warsaw University of Technology ISE  
Nowowiejska 15/19

ARTIQ



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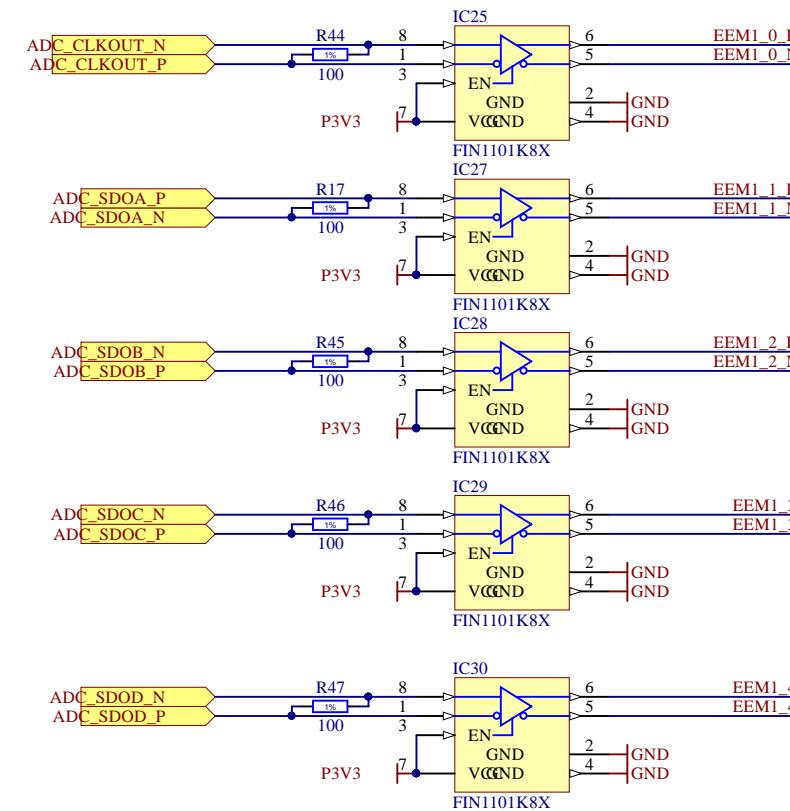
Project/Equipment 3U ADC

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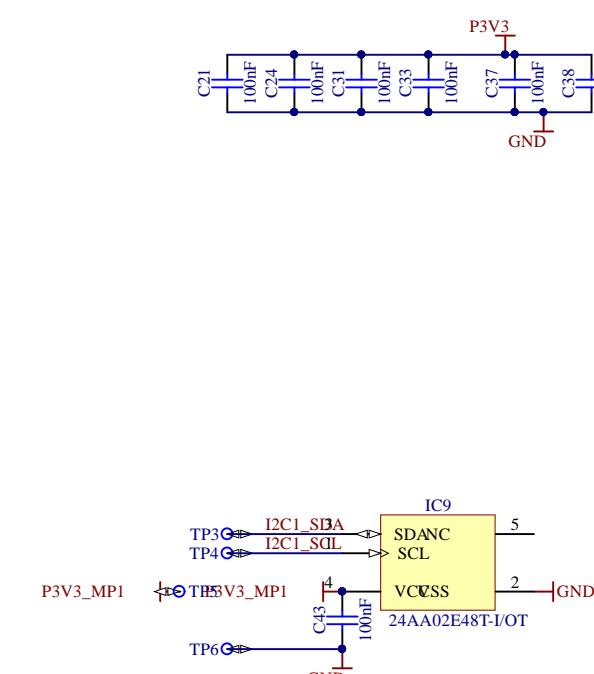
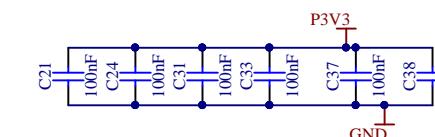
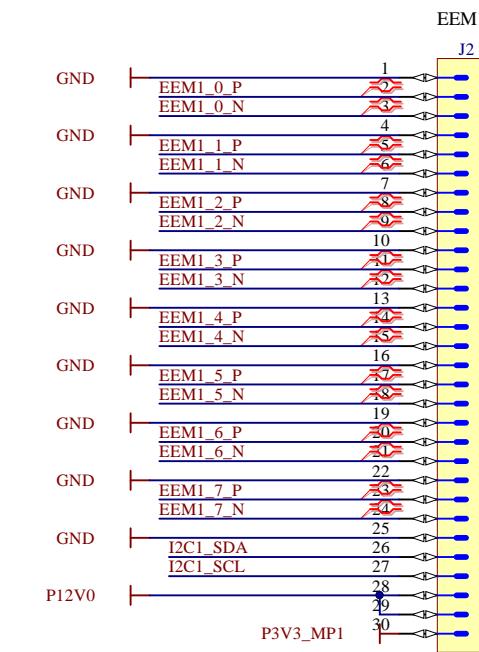
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**Input chnnnel  
DPGA**

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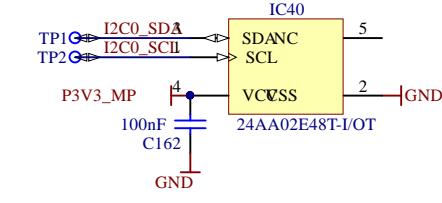
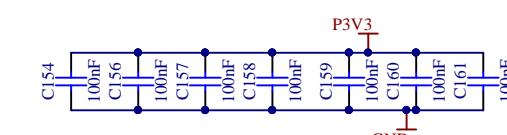
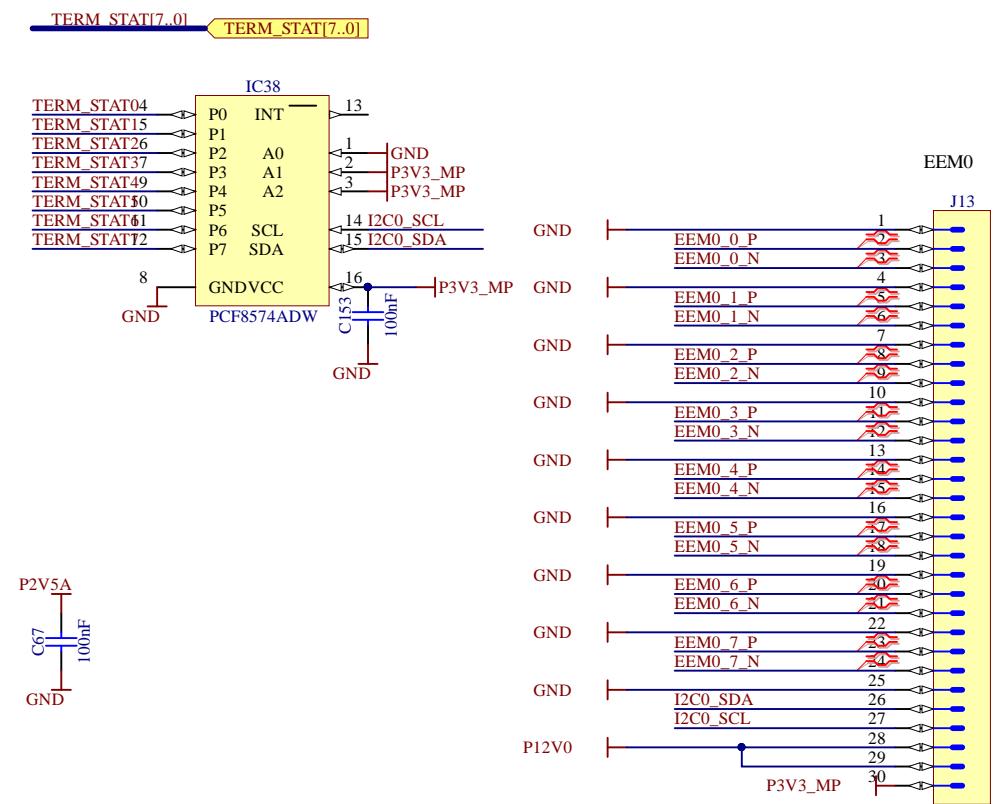
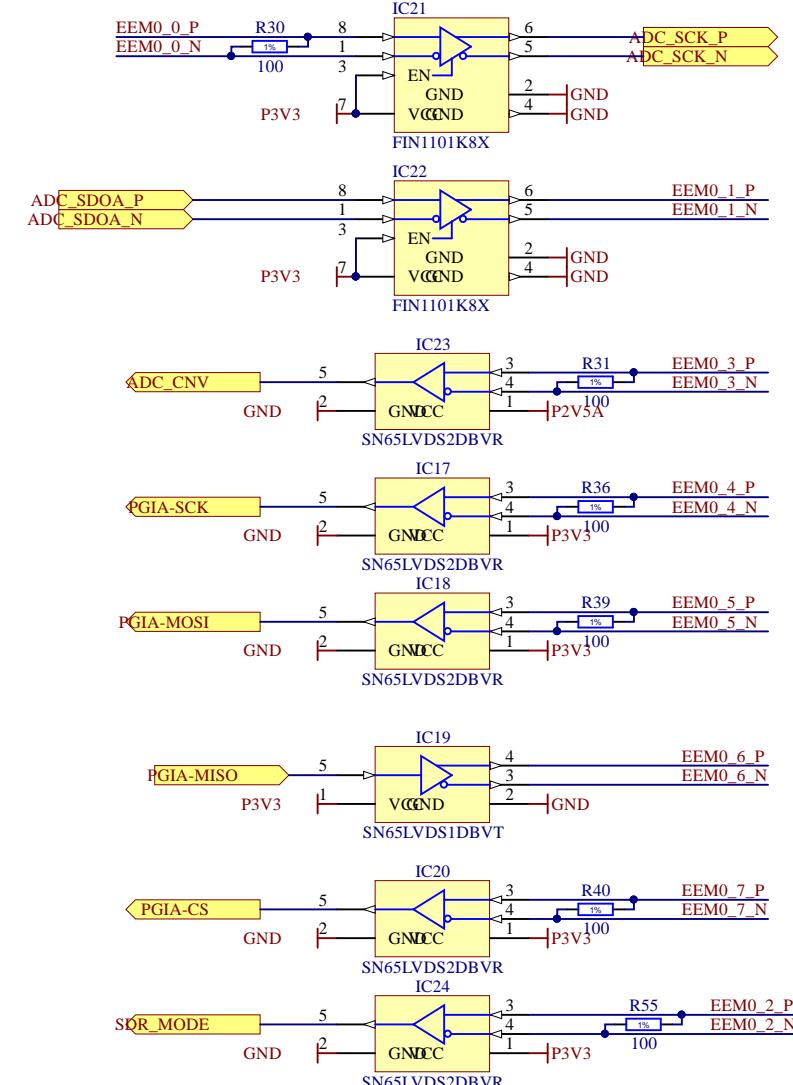
EEM connector: IO are LVDS, I2C is 3V3 LVC MOS, P3V3\_MP up to 20mA, P12V up to 1A



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EEM connector: IO are LVDS, I2C is 3V3 LVC MOS, P3V3\_MP up to 20mA, P12V up to 1A

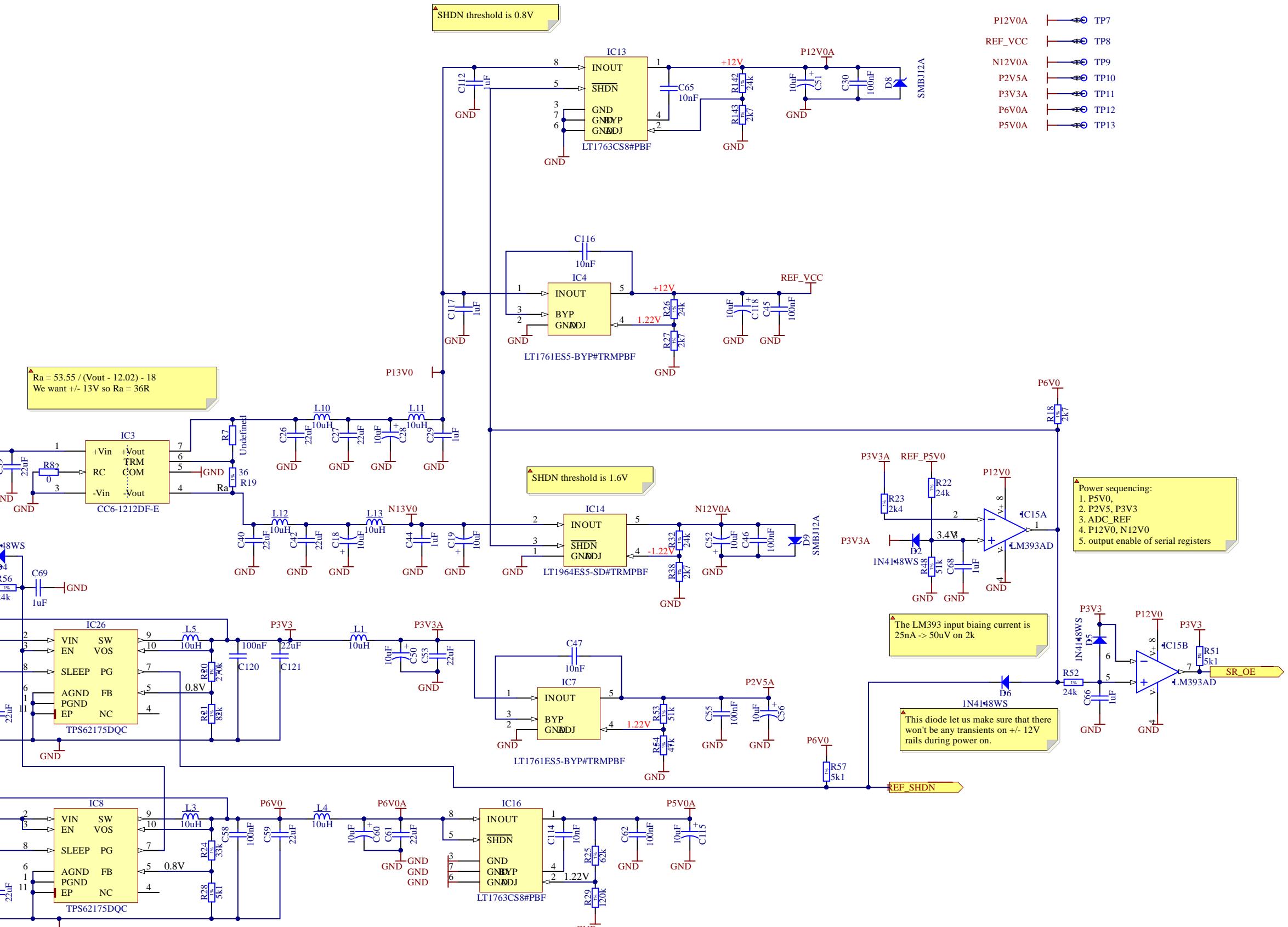
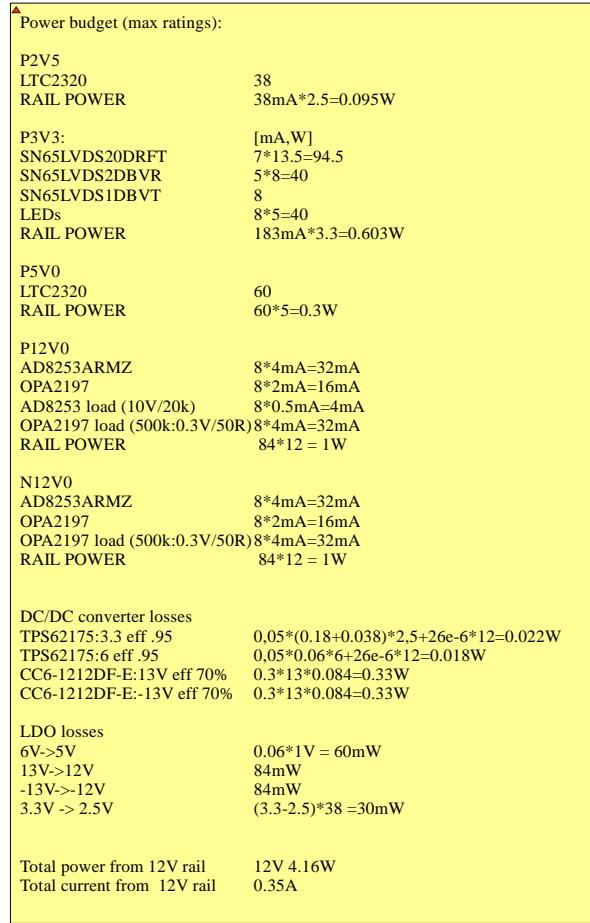


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Project/Equipment 3U ADC

## LVDS Interface I2C logic

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## *Power Supply converters + LDOs*

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PCB 3U ADC rev2.0



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+12V

REF UCC

P2U5

N12UA

P6U0A

P5U0A

SCL

SDA

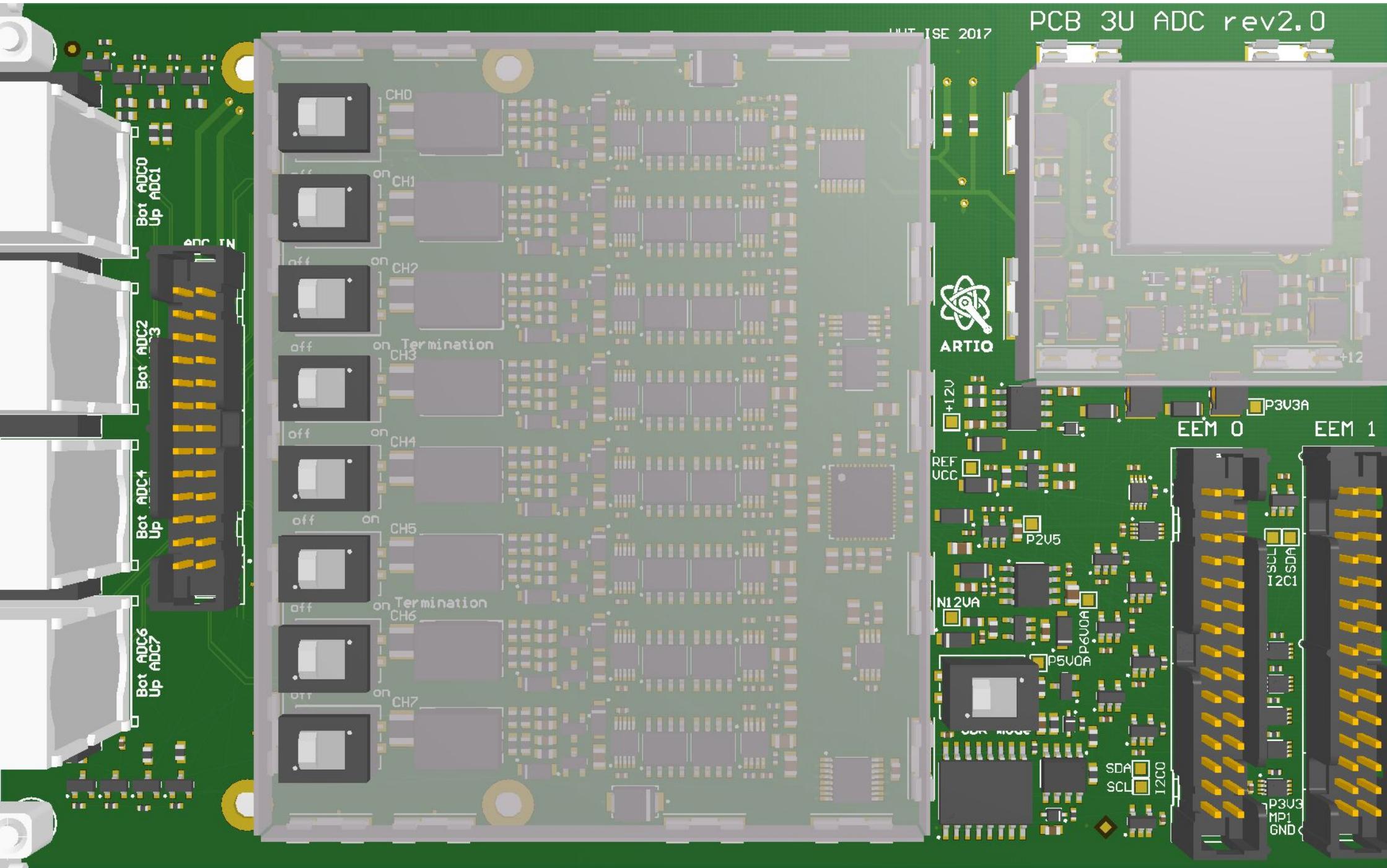
I2C0

I2C1

P3U3

MP1

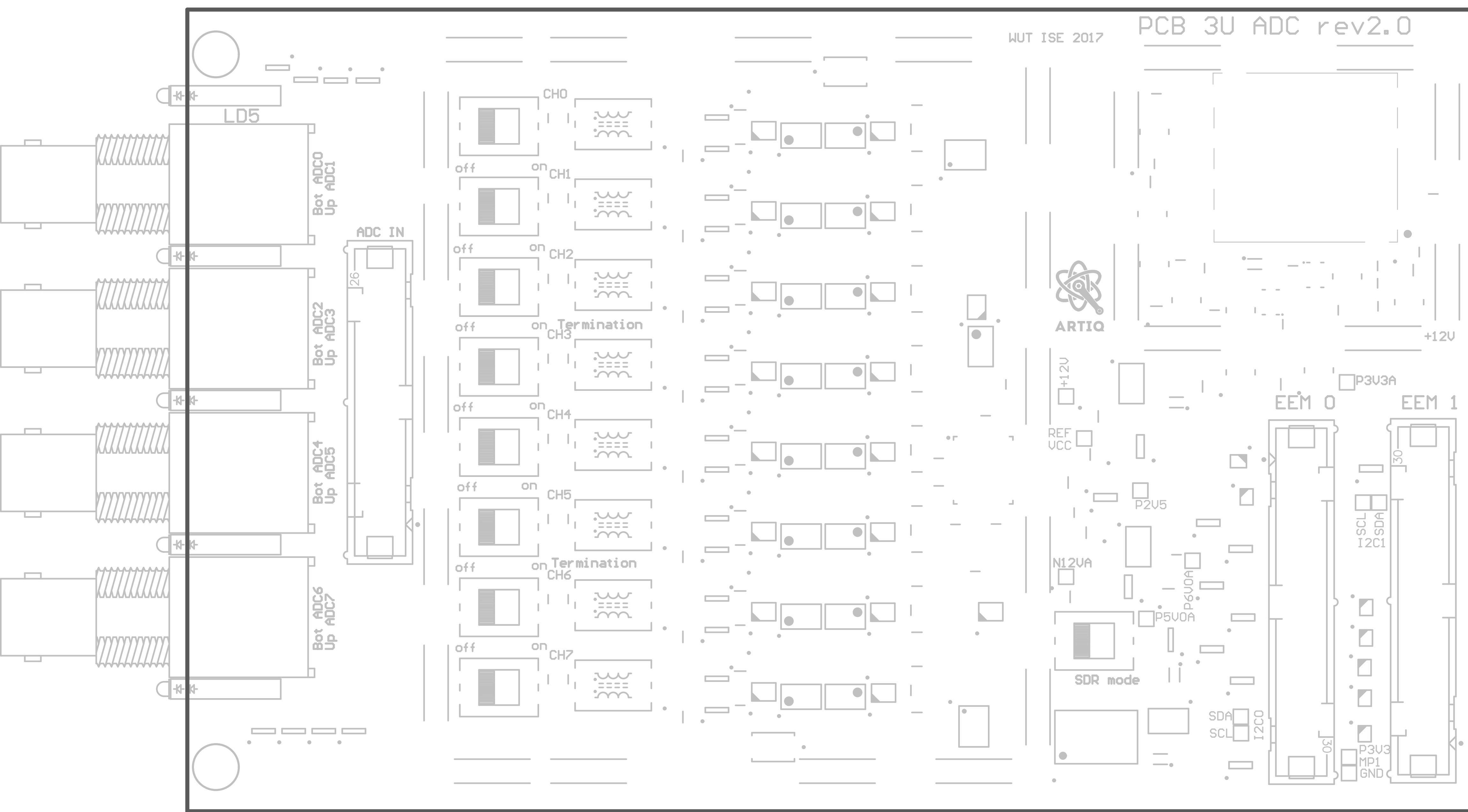
GND

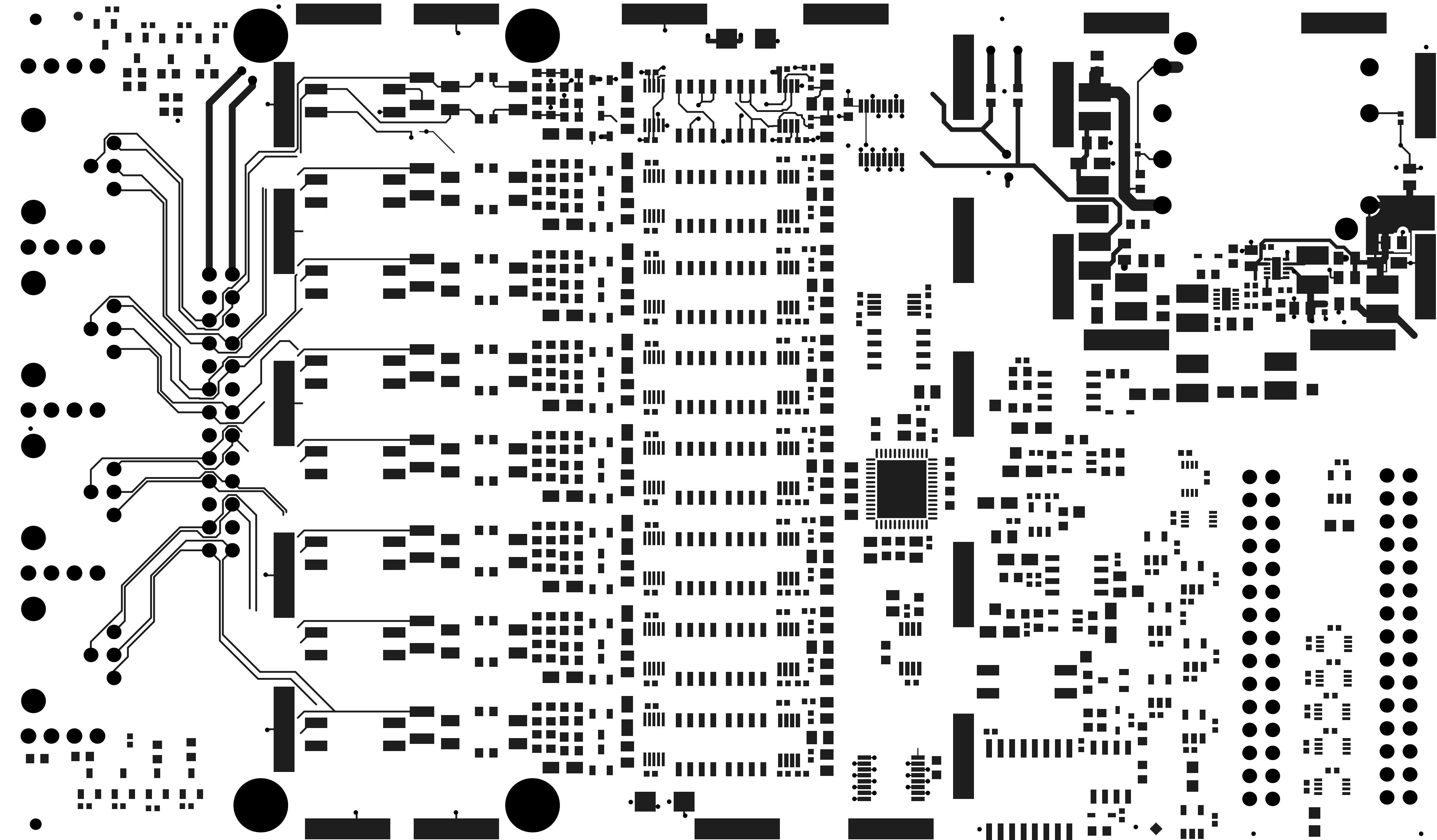




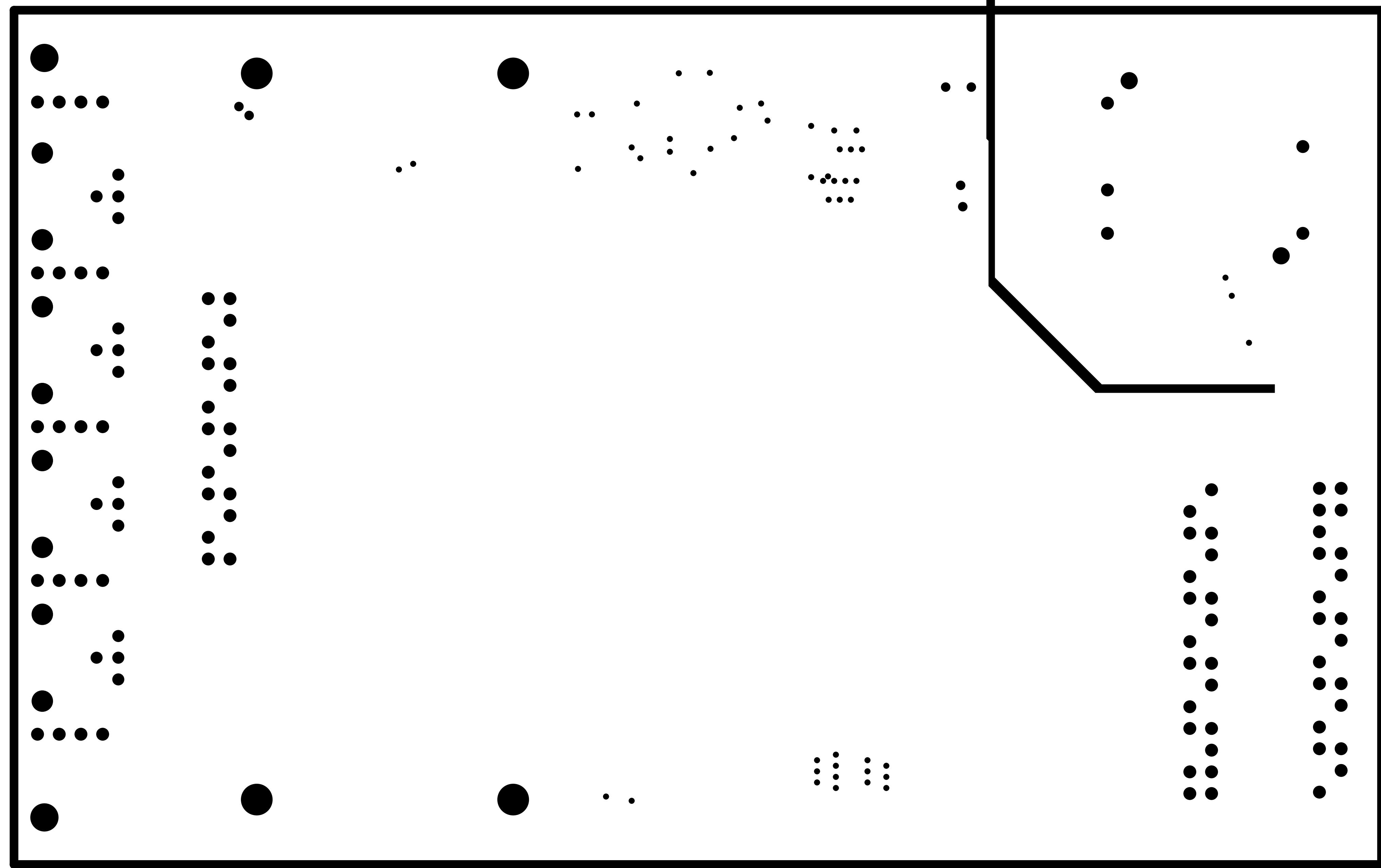
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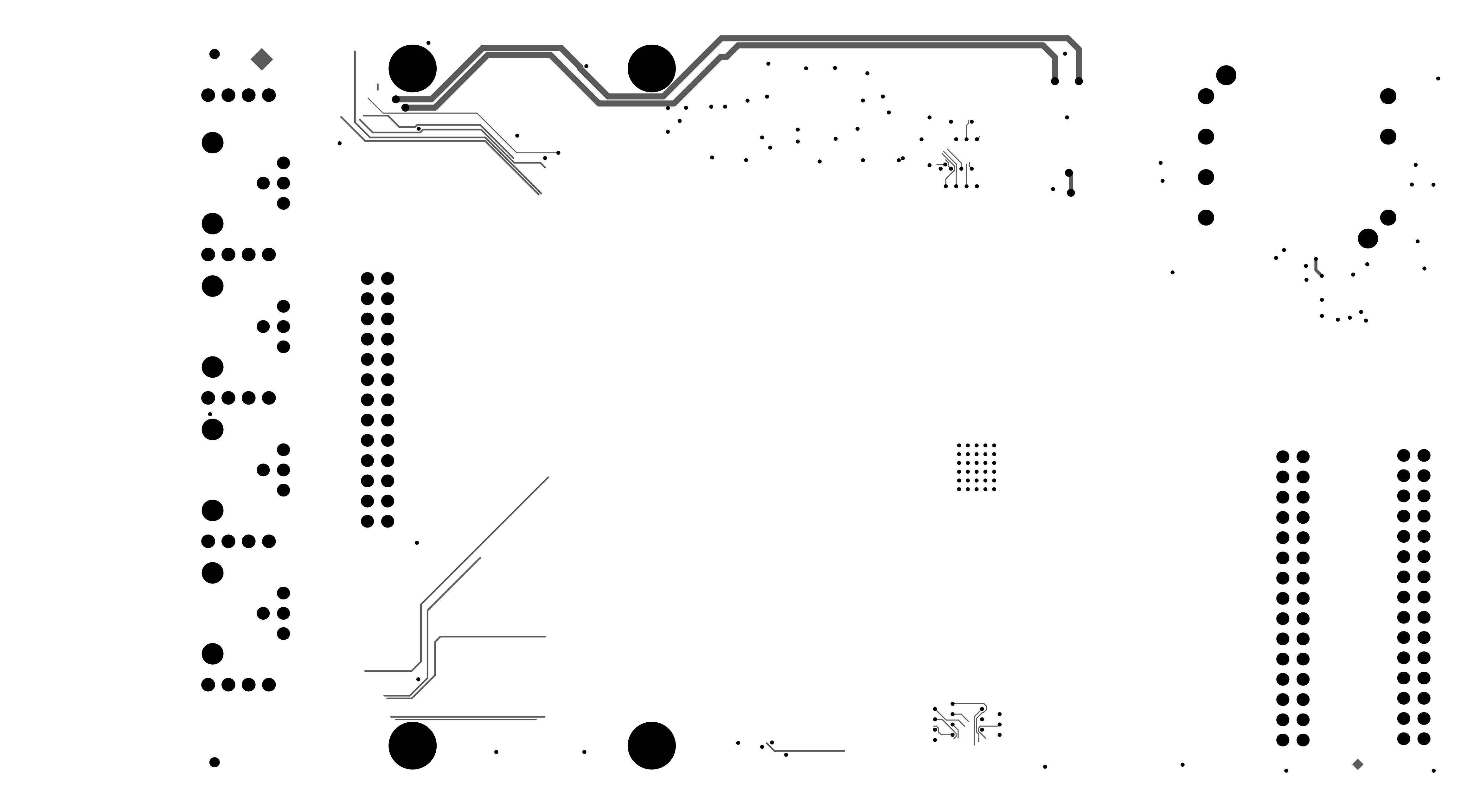
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PCB 3U ADC RevZ.0

MULTIUSE 2012