SPRING 2023

* Sequences developed:
* risc\_sequence.sv: All commands with 20 repetitions
* risc\_sequence\_sll\_srl.sv: sll and srl commands with all possible values of shift amount and destination address
* risc\_sequence\_sra\_srai.sv: sra and srai commands with all possible values of shift amount and destination address
* risc\_sequence\_srli\_slli.sv: srli and slli commands with all possible values of shift amount and destination address
* Total of 3 DUTs failed out of 8, as mentioned below:
* dut51: Failure due to command SLLI with data mismatch as the resultant value of instruction from DUT does not match the expected data of the reference model.
* dut52: Failure due to command SRLI
* dut55: Failure due to command SRL
* Run command example: ./sv\_uvm top.sv +define+RISC\_0

Note: Refer to path “report/log/” for the failed log files and overall results in report.txt