

Notes:

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Indicates POWER Ground.

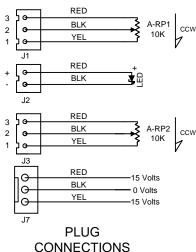
U5 - TL431 is marked CJ431 on PCB.

VR1 sets maximum Current Output.

VR2 sets maximum Voltage Limit.

A_{pwr} Control presents the voltage drop across R24 &25, averaged, to U1A, pin 2. U1A compares this voltage with the fraction of the Voltage Reference presented to pin 3. If pin 2 voltage exceeds pin3, U1A pin 1 is pulled low lighting the LED and limiting the Output Voltage.

 V_{pwr} Control is developed by U1B. Voltage divider R9, R10 presents a stable fraction of Voltage Reverence to pin 6 (Inverting Input) while pin 5 (Non-Inverting Input) receives a variable voltage via the movable tap of V-RP2. The amplifier outout, pin 7, provides V_{pwr} Control.



Linear 35 Volt 5 Amp Power Supply KIT
Schematic 1 of 3
Control System

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