B.E. Seventh Semester (Computer Technology) (C.B.S.)

Compilers

NKT/KS/17/7477 P. Pages: 4 Time: Three Hours Max. Marks: 80 Notes: 1. All questions carry marks as indicated. Solve Question 1 OR Questions No. 2. 2. 3. Solve Question 3 OR Questions No. 4. Solve Question 5 OR Questions No. 6. 4. Solve Question 7 OR Questions No. 8. 5. Solve Question 9 OR Questions No. 10. 6. Solve Question 11 OR Questions No. 12. 7. Due credit will be given to neatness and adequate dimensions. 8. Assume suitable data whenever necessary. 9. 10. Illustrate your answers whenever necessary with the help of neat sketches. 1. Define compiler. Explain different phases of compiler with suitable diagram & show 9 a) output generated by every phase of compiler for following statement A = B + C * 20.What are different compiler construction tools? b) 4 OR Write a Lex program to identify keyword, constant, identifier & operators. 2. a) 6 b) Explain the significance of FIRST & FOLLOW with suitable example. 7 Is given grammar is LR(0) or not 8 3. a) $S \rightarrow cA | ccB$ $A \rightarrow cA \mid a$ $B \rightarrow ccB | b$ 5 Compare LR(0), LR(1) & LALR Parser. b) OR Construct LR(1) parsing table for below grammar 4. 8 a) $S \rightarrow wAz \mid xBz \mid wBy \mid xAy$ $A \rightarrow r$ $B \rightarrow r$ b) Explain Bottom up parsing techniques. 5

5. Consider the SDTS a)

 $E \rightarrow E - E$ {printf "- " 3,

 $E \rightarrow E + E$ {printf "+" 3,

 $E \rightarrow E * E$ {printf "*" 3,

{printf "/" 3, $E \rightarrow E/E$

 $E \rightarrow id$ {printid.name}

 $E \rightarrow (E)$

Convert the following infix expression into postfix expression.

- id1 id2 / id3
- b) (id1 - id2) * (id3 / id4)
- ((id1 + id2) * Id3)c)
- b) Translate the expression

(-(a+b)*(c+d))-(a+b+c)

a) Quadruple b) Triple c) Indirect triple representation.

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OR

Generate TAC using SDTS for the following statement **6.**

A[I, J, K] = B[I, J] + C[I + J + K]

where

A is 3D array of size10x10x10

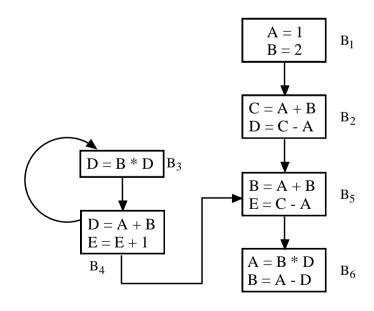
B is 2D array of size 10x10

C is 1D array of size 30

BPW = 2

Also generate annotated parse tree for the same.

7. Compute IN & OUT for following graph.



Also Compute µd chain for Block B₃ & B₅

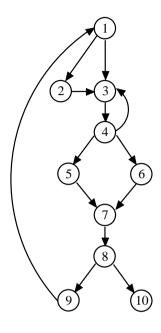
OR

8. a) Write a note on

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- a) Loop unrolling.
- b) Loop Jamming.
- c) Loop invariant computation.
- b) What is dominator? Construct dominator tree for following graph.





9. a) Explain what are different issues in code generator.

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b) Generate code for following expression using code generation algorithm X = (a+b) - ((C+d) - e)

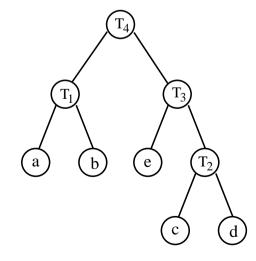
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OR

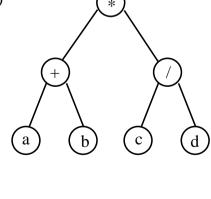
10. a) Explain first part of labelling algorithm & determine number of Register required to evaluate following tree.

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i)



ii)



b) Explain peephole optimization in detail.

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11.	a)	What are different storage allocation strategies? Explain.	(
	b)	Explain error Recovery in LR parsing.	•
		OR	
12.	a)	Define symbol table. Explain data structure use for Representation of symbol table.	•
	b)	What are different error Recovery strategies? Explain.	(
