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2.3.2 ABB\_TTX\_300\_PA 5.0-0 3

2.3.3 ABB\_TZIDC\_x10\_PA 1.3-0 3

2.3.4 BasicHwLib 6.0-0 3

2.3.5 CI854PROFIBUSHwLib 2.11-25 3

2.3.6 RGAX1\_PRCI854HwLib1 1.0-0 3

2.3.7 RGAX3\_PRCI854HwLib1 1.0-0 3

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# Compiler switches

| **Switch** | **Global** | **SIL 1-2** | **SIL3** |
| --- | --- | --- | --- |
| Simultaneous Execution in SFC | Allowed | Error | Error |
| Loops In ST | Allowed | Error | Error |
| Nested IF or CASE | Allowed | Allowed | Allowed |
| Implicit Cast | Allowed | Allowed | Allowed |
| Instruction List language | Allowed | Error | Error |
| Ladder Diagram language | Allowed | Error | Error |
| SFC language | Allowed | Allowed | Error |
| Loops in Control Modules | Warning | Error | Error |
| Force I/O from code | Allowed | Error | Error |
| Multiple calls to the same Function Block | Allowed | Allowed | Error |
| None or multiple calls to ExecuteControlModules | Allowed | Allowed | Error |
| SIL 1-2 communication variables in SIL3 applications | Allowed | Allowed | Error |
| Non-SIL communication variables in SIL1-3 applications | Allowed | Error | Error |
| No forward flow between blocks in FD | Warning | Error | Error |
| Unresolved communications variables | Warning | Warning | Warning |

Table 1. Compiler switches

# Controller\_3 (172.16.96.153)

Controller - Controller\_3

Properties and Settings:

## Controller Settings

Fatal Overrun Limit: 10

Fatal Overrun Reaction: Nothing

Online Upgrade Handover limit: 3000 ms

Enable overload compensation: True

| **Severity/Actions** | **Log** | **Event** | **Shutdown** |
| --- | --- | --- | --- |
| 1 - Low | False | False | False |
| 2 - Medium | True | False | False |
| 3 - High | True | True | False |
| 4 - Critical | True | True | True |
| 5 - Fatal | True | True | True |

Table 2. Error Reaction - System Diagnostics

| **Severity/Actions** | **Log** | **Event** | **Shutdown** |
| --- | --- | --- | --- |
| 1 - Low | False | False | False |
| 2 - Medium | True | False | False |
| 3 - High | True | True | False |
| 4 - Critical | True | True | True |
| 5 - Fatal | True | True | True |

Table 3. Error Reaction - Execution

| **Severity/Actions** | **Log** | **Event** | **Shutdown** |
| --- | --- | --- | --- |
| 1 - Low | False | False | False |
| 2 - Medium | False | False | False |
| 3 - High | False | False | False |
| 4 - Critical | True | True | True |
| 5 - Fatal | True | True | True |

Table 4. Error Reaction - I/O

## Connected Applications

### Ciklo\_Eno\_Acetal\_3

### Ciklopro\_Ad\_Enolet\_3

### Cikloprpil\_D6\_laktol

### Ellato\_3

### Nyers\_Drospiren\_3

## Connected Libraries

### ABB\_TFx12\_PA 1.2-0

### ABB\_TTX\_300\_PA 5.0-0

### ABB\_TZIDC\_x10\_PA 1.3-0

### BasicHwLib 6.0-0

### CI854PROFIBUSHwLib 2.11-25

### RGAX1\_PRCI854HwLib1 1.0-0

### RGAX3\_PRCI854HwLib1 1.0-0

### S800CI840CI854HwLib 1.4-0

### S900IoCI854HwLib 1.1-2

## Hardware AC 800M

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Copy unconnected channels | None | enum |  |  |  |
| Latched hardware state | Enabled | enum |  |  |  |
| System alarms on hardware units | Enabled | enum |  |  |  |
| Simple events on hardware units | Enabled | enum |  |  |  |
| Filter out system alarms from hardware units | Off | enum |  |  |  |
| HwStatus update cycle time | 1000 | dint | ms | 500 | 30000 |
| Clamp Analog in values | true | bool |  |  |  |
| Clamp Analog out values | true | bool |  |  |  |
| IAC Active HW | 0.5.1 | string |  |  | 150 |

Table 5. Controller\_3 (172.16.96.153). AC 800M (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW0 | AllUnitStatus | HwStatus |  |  |

Table 6. Controller\_3. AC 800M (Connections)

### 0 U (L) PM864 / TP830

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Enable SattBus on TCP/IP | false | bool |  |  |  |
| Routing method | rnrp | enum |  |  |  |
| Default gateway |  | string |  |  | 150 |
| AE System AE class | 9950 | dint |  | 1 | 9999 |
| AE System AE low severity | 920 | dint |  | 1 | 1000 |
| AE System AE medium severity | 930 | dint |  | 1 | 1000 |
| AE System AE high severity | 940 | dint |  | 1 | 1000 |
| AE System AE critical severity | 950 | dint |  | 1 | 1000 |
| AE System AE fatal severity | 960 | dint |  | 1 | 1000 |
| AE System events to controller log | Medium Severity | enum |  |  |  |
| AE Local printer event queue size | 300 | dint |  | 10 | 3000 |
| AE Max no of local printer event queues | 0 | dint |  | 0 | 5 |
| AE Event subscription queue size | 300 | dint |  | 200 | 3000 |
| AE Max number of event subscriptions | 5 | dint |  | 0 | 13 |
| AE Event subscription time out | 360 | dint | min | 0 | 1440 |
| AE Buffer size of low level event | 2 | dint |  | 2 | 4000 |
| AE Max no of Named Value items | 0 | dint |  | 0 | 9000 |
| AE Max percent of log strings | 10 | dint | % | 0 | 100 |
| AE Limit auto disable | 3 | dint |  | 0 | 127 |
| RNRP Default network ID | 172.16.4.0 | string |  |  | 150 |
| RNRP Number of own areas | 2 | dint |  | 1 | 4 |
| RNRP Number of remote areas | 4 | dint |  | 0 | 15 |
| RNRP Max Lost Messages | 3 | dint |  | 1 | 10 |
| RNRP Send Period | 1 | dint | s | 1 | 60 |
| RNRP Max no of hops | 3 | dint |  | 1 | 5 |
| RNRP Ext network |  | string |  |  | 150 |
| RNRP Ext netw mask |  | string |  |  | 150 |
| RNRP Ext router1 addr |  | string |  |  | 150 |
| RNRP Ext router2 addr |  | string |  |  | 150 |
| Tool Routing | Enabled | enum |  |  |  |
| Tool Routing max no of connections | 20 | dint |  | 5 | 50 |
| CS CNCP ClockMaster Order No | 3 | dint |  | 0 | 10 |
| CS Protocol Type | CNCP | enum |  |  |  |
| CS Time Set Enabled | True | enum |  |  |  |
| CS Synch Interval | 20 | dint | s | 1 | 240 |
| CS SNTP ServerAddr 1 |  | string |  |  | 150 |
| CS SNTP ServerAddr 2 |  | string |  |  | 150 |

Table 7. Controller\_3 (172.16.96.153).0 PM864 / TP830 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW0.0 | UnitStatus | HwStatus |  |  |

Table 8. Controller\_3.0 PM864 / TP830 (Connections)

#### 0 CF Reader

##### 0 CF Card

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Save cold retain values | 60 | dint | min | 0 | 65535 |

Table 9. Controller\_3 (172.16.96.153).0.0.0 CF Card (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW0.0.0.0 | UnitStatus | HwStatus |  |  |

Table 10. Controller\_3.0.0.0 CF Card (Connections)

#### 1 Ethernet

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| IP address | 172.16.96.153 | string |  |  | 150 |
| IP subnet mask | 255.255.252.0 | string |  |  | 150 |
| Network Area | 24 | dint |  | 0 | 63 |
| Path Number | 0 | dint |  | 0 | 1 |
| Node Number | 153 | dint |  | 0 | 500 |
| Network Area Local | false | bool |  |  |  |
| Send Period | 1 | dint | s | 1 | 60 |
| Max Lost Messages | 3 | dint |  | 1 | 10 |
| Proxy router | 0.0.0.0 | string |  |  | 150 |
| Target address | 0.0.0.0 | string |  |  | 150 |

Table 11. Controller\_3 (172.16.96.153).0.1 Ethernet (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW0.1.0 | UnitStatus | HwStatus |  |  |

Table 12. Controller\_3.0.1 Ethernet (Connections)

#### 2 Ethernet

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| IP address | 172.17.96.153 | string |  |  | 150 |
| IP subnet mask | 255.255.252.0 | string |  |  | 150 |
| Network Area | 24 | dint |  | 0 | 63 |
| Path Number | 1 | dint |  | 0 | 1 |
| Node Number | 153 | dint |  | 0 | 500 |
| Network Area Local | false | bool |  |  |  |
| Send Period | 1 | dint | s | 1 | 60 |
| Max Lost Messages | 3 | dint |  | 1 | 10 |
| Proxy router | 0.0.0.0 | string |  |  | 150 |
| Target address | 0.0.0.0 | string |  |  | 150 |
| Enable Ethernet channel | true | bool |  |  |  |

Table 13. Controller\_3 (172.16.96.153).0.2 Ethernet (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW0.2.0 | UnitStatus | HwStatus |  |  |

Table 14. Controller\_3.0.2 Ethernet (Connections)

#### 3 Com

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Baudrate | 9.6 | enum | KBIT/S |  |  |
| Parity | none | enum |  |  |  |
| Databits | 8 | enum |  |  |  |
| Stopbits | 1 | enum |  |  |  |
| Flow control | None | enum |  |  |  |
| Delay of the RTS-signal | 0 | dint | msec | 0 | 255 |

Table 15. Controller\_3 (172.16.96.153).0.3 Com (Settings)

#### 4 Com

##### PPP

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| IP address | 192.168.255.254 | string |  |  | 150 |
| IP subnet mask | 255.255.255.0 | string |  |  | 150 |
| Network Area | 0 | dint |  | 0 | 35 |
| Path Number | 0 | dint |  | 0 | 3 |
| Node Number | 0 | dint |  | 0 | 500 |
| Network Area Local | false | bool |  |  |  |
| Send Period | 5 | dint | s | 1 | 60 |
| Max Lost Messages | 3 | dint |  | 1 | 10 |
| Proxy router | 0.0.0.0 | string |  |  | 150 |
| Target address | 0.0.0.0 | string |  |  | 150 |
| Remote IP address | 192.168.255.253 | string |  |  | 150 |

Table 16. Controller\_3 (172.16.96.153).0.4 PPP (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW0.4.0.0 | UnitStatus | HwStatus |  |  |

Table 17. Controller\_3.0.4 PPP (Connections)

#### 11 ModuleBus

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| External power supervision | true | bool |  |  |  |
| Scan Cycle Time | 0 | dint | ms | 0 | 100 |

Table 18. Controller\_3 (172.16.96.153).0.11 ModuleBus (Settings)

### 1 (7) CI854

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Node address (FDL\_Add) | 1 | dint |  | 1 | 125 |
| Baudrate | 1.5M | enum | bit/s |  |  |
| Slot time (TSL) | 300 | dint | tbit | 37 | 16383 |
| Min station delay (min TSDR) | 11 | dint | tbit | 11 | 1023 |
| Max station delay (max TSDR) | 150 | dint | tbit | 37 | 65535 |
| Quiet time (TQui) | 0 | dint | tbit | 0 | 127 |
| Setup time (TSet) | 20 | dint | tbit | 1 | 255 |
| Target rotation time (TTR) | 39681 | dint | tbit | 255 | 999999 |
| Gap factor (G) | 10 | dint |  | 1 | 100 |
| Highest station address (HSA) | 126 | dint |  | 1 | 126 |
| Max retry limit | 1 | dint |  | 0 | 7 |
| BP flag | Auto Clear OFF | enum |  |  |  |
| Min slave interval | 10 | dint | 100us | 1 | 65535 |
| Poll time out | 50 | dint | ms | 1 | 65535 |
| Data control time | 1014 | dint | 10ms | 1 | 65535 |
| Watchdog time | 5070 | dint | ms | 10 | 650250 |
| Line redundancy | Disabled | enum |  |  |  |
| Delta TTR | 0 | dint | % | 0 | 100 |
| Calculation method | Actual value based | enum |  |  |  |

Table 19. Controller\_3 (172.16.96.153).1 CI854 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.0 | UnitStatus | HwStatus |  |  |

Table 20. Controller\_3.1 CI854 (Connections)

#### 11 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.11.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 21. Controller\_3.1.11 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.11.0.0 | Temp1 Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_1ax3.IND\_1\_IO | TIT00201 |
| IW1.11.0.1 | Temp1 Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_1ax3.IND\_1\_IO\_Status |  |
| IW1.11.0.2 | UnitStatus | HwStatus |  |  |

Table 22. Controller\_3.1.11.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.11.0.0 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_1ax3.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW1.11.0.1 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_1ax3.IND\_1\_IO\_Status |  |  |  |  |  |

Table 23. Controller\_3.1.11.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.11.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_1ax3.IND\_1\_IO |
| IW1.11.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_1ax3.IND\_1\_IO\_Status |

Table 24. Controller\_3.1.11.0 Temperature 1 (Status)

#### 12 Pressure Transmitter (9700)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.12.0 | UnitStatus | HwStatus |  |  |

Table 25. Controller\_3.1.12 Pressure Transmitter (9700) (Connections)

##### 0 Analog Input (AI)short

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.12.0.0 | Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_IO | PIT00211 |
| IW1.12.0.1 | Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_Status |  |
| IW1.12.0.2 | UnitStatus | HwStatus |  |  |

Table 26. Controller\_3.1.12.0 Analog Input (AI)short (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.12.0.0 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_IO | 0.0 | 10.0 | bar | 2 | false |
| IW1.12.0.1 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_Status |  |  |  |  |  |

Table 27. Controller\_3.1.12.0 Analog Input (AI)short (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.12.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_IO |
| IW1.12.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_Status |

Table 28. Controller\_3.1.12.0 Analog Input (AI)short (Status)

#### 13 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.13.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 29. Controller\_3.1.13 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.13.0.0 | Temp1 Value | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_1.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO | TIT01004 |
| IW1.13.0.1 | Temp1 Status | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_1.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |  |
| IW1.13.0.2 | UnitStatus | HwStatus |  |  |

Table 30. Controller\_3.1.13.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.13.0.0 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_1.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW1.13.0.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_1.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |  |  |  |  |  |

Table 31. Controller\_3.1.13.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.13.0.0 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_1.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO |
| IW1.13.0.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_1.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |

Table 32. Controller\_3.1.13.0 Temperature 1 (Status)

#### 14 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.14.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 33. Controller\_3.1.14 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.14.0.0 | Temp1 Value | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_2.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO | TIT01005 |
| IW1.14.0.1 | Temp1 Status | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_2.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |  |
| IW1.14.0.2 | UnitStatus | HwStatus |  |  |

Table 34. Controller\_3.1.14.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.14.0.0 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_2.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW1.14.0.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_2.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |  |  |  |  |  |

Table 35. Controller\_3.1.14.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.14.0.0 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_2.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO |
| IW1.14.0.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_2.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |

Table 36. Controller\_3.1.14.0 Temperature 1 (Status)

#### 15 Pressure Transmitter (9700)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.15.0 | UnitStatus | HwStatus |  |  |

Table 37. Controller\_3.1.15 Pressure Transmitter (9700) (Connections)

##### 0 Analog Input (AI)short

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.15.0.0 | Value | RealIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.IND\_5.IND\_5\_IO | PIT04619 |
| IW1.15.0.1 | Status | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.IND\_5.IND\_5\_Status |  |
| IW1.15.0.2 | UnitStatus | HwStatus |  |  |

Table 38. Controller\_3.1.15.0 Analog Input (AI)short (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.15.0.0 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.IND\_5.IND\_5\_IO | 0.0 | 10.0 | bar | 2 | false |
| IW1.15.0.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.IND\_5.IND\_5\_Status |  |  |  |  |  |

Table 39. Controller\_3.1.15.0 Analog Input (AI)short (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.15.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.IND\_5.IND\_5\_IO |
| IW1.15.0.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.IND\_5.IND\_5\_Status |

Table 40. Controller\_3.1.15.0 Analog Input (AI)short (Status)

#### 16 ABB\_TZIDC\_x10\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.16.0 | UnitStatus V3\_0\_11 | HwStatus |  |  |

Table 41. Controller\_3.1.16 ABB\_TZIDC\_x10\_PA (Connections)

##### 0 SP+READBACK+POS\_D

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.16.0.0 | READBACK Value | RealIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.HeaterValve\_IO.FB | KCV04658 |
| IW1.16.0.1 | READBACK Status | DintIO |  |  |
| IW1.16.0.2 | POS\_D Value | DintIO |  |  |
| IW1.16.0.3 | POS\_D Status | DintIO |  |  |
| QW1.16.0.4 | SETPOINT Value | RealIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.HeaterValve\_IO.SP | KCV04658 |
| QW1.16.0.5 | SETPOINT Status | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Heater\_SP\_Control | KCV04658 |
| IW1.16.0.6 | UnitStatus | HwStatus |  |  |

Table 42. Controller\_3.1.16.0 SP+READBACK+POS\_D (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.16.0.0 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.HeaterValve\_IO.FB | 0.0 | 100.0 |  | 1 | false |
| IW1.16.0.1 |  |  |  |  |  |  |
| IW1.16.0.2 |  |  |  |  |  |  |
| IW1.16.0.3 |  |  |  |  |  |  |
| QW1.16.0.4 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.HeaterValve\_IO.SP | 0.0 | 100.0 |  | 1 | false |
| QW1.16.0.5 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Heater\_SP\_Control |  |  |  |  |  |

Table 43. Controller\_3.1.16.0 SP+READBACK+POS\_D (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.16.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.HeaterValve\_IO.FB |
| IW1.16.0.1 |  |  |  |  |
| IW1.16.0.2 |  |  |  |  |
| IW1.16.0.3 |  |  |  |  |
| QW1.16.0.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.HeaterValve\_IO.SP |
| QW1.16.0.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Heater\_SP\_Control |

Table 44. Controller\_3.1.16.0 SP+READBACK+POS\_D (Status)

#### 17 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.17.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 45. Controller\_3.1.17 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.17.0.0 | Temp1 Value | RealIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax3\_A.IND\_1\_IO | TIT03902 |
| IW1.17.0.1 | Temp1 Status | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax3\_A.IND\_1\_IO\_Status |  |
| IW1.17.0.2 | UnitStatus | HwStatus |  |  |

Table 46. Controller\_3.1.17.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.17.0.0 | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax3\_A.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW1.17.0.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax3\_A.IND\_1\_IO\_Status |  |  |  |  |  |

Table 47. Controller\_3.1.17.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.17.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax3\_A.IND\_1\_IO |
| IW1.17.0.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax3\_A.IND\_1\_IO\_Status |

Table 48. Controller\_3.1.17.0 Temperature 1 (Status)

#### 18 ABB\_TZIDC\_x10\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.18.0 | UnitStatus V3\_0\_11 | HwStatus |  |  |

Table 49. Controller\_3.1.18 ABB\_TZIDC\_x10\_PA (Connections)

##### 0 SP+READBACK+POS\_D

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.18.0.0 | READBACK Value | RealIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.CoolerValve\_IO.FB | KCV04659 |
| IW1.18.0.1 | READBACK Status | DintIO |  |  |
| IW1.18.0.2 | POS\_D Value | DintIO |  |  |
| IW1.18.0.3 | POS\_D Status | DintIO |  |  |
| QW1.18.0.4 | SETPOINT Value | RealIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.CoolerValve\_IO.SP | KCV04659 |
| QW1.18.0.5 | SETPOINT Status | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Cooler\_SP\_Control | KCV04659 |
| IW1.18.0.6 | UnitStatus | HwStatus |  |  |

Table 50. Controller\_3.1.18.0 SP+READBACK+POS\_D (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.18.0.0 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.CoolerValve\_IO.FB | 0.0 | 100.0 |  | 1 | false |
| IW1.18.0.1 |  |  |  |  |  |  |
| IW1.18.0.2 |  |  |  |  |  |  |
| IW1.18.0.3 |  |  |  |  |  |  |
| QW1.18.0.4 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.CoolerValve\_IO.SP | 0.0 | 100.0 |  | 1 | false |
| QW1.18.0.5 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Cooler\_SP\_Control |  |  |  |  |  |

Table 51. Controller\_3.1.18.0 SP+READBACK+POS\_D (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.18.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.CoolerValve\_IO.FB |
| IW1.18.0.1 |  |  |  |  |
| IW1.18.0.2 |  |  |  |  |
| IW1.18.0.3 |  |  |  |  |
| QW1.18.0.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.CoolerValve\_IO.SP |
| QW1.18.0.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Cooler\_SP\_Control |

Table 52. Controller\_3.1.18.0 SP+READBACK+POS\_D (Status)

#### 19 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.19.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 53. Controller\_3.1.19 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.19.0.0 | Temp1 Value | RealIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO | TIT04601 |
| IW1.19.0.1 | Temp1 Status | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO\_Status |  |
| IW1.19.0.2 | UnitStatus | HwStatus |  |  |

Table 54. Controller\_3.1.19.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.19.0.0 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW1.19.0.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO\_Status |  |  |  |  |  |

Table 55. Controller\_3.1.19.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.19.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO |
| IW1.19.0.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO\_Status |

Table 56. Controller\_3.1.19.0 Temperature 1 (Status)

#### 20 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.20.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 57. Controller\_3.1.20 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.20.0.0 | Temp1 Value | RealIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM27.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO | TIT04604 |
| IW1.20.0.1 | Temp1 Status | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM27.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |  |
| IW1.20.0.2 | UnitStatus | HwStatus |  |  |

Table 58. Controller\_3.1.20.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.20.0.0 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM27.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW1.20.0.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM27.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |  |  |  |  |  |

Table 59. Controller\_3.1.20.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.20.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM27.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO |
| IW1.20.0.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM27.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |

Table 60. Controller\_3.1.20.0 Temperature 1 (Status)

#### 21 Pressure Transmitter (9700)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.21.0 | UnitStatus | HwStatus |  |  |

Table 61. Controller\_3.1.21 Pressure Transmitter (9700) (Connections)

##### 0 Analog Input (AI)short

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.21.0.0 | Value | RealIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.CNT\_1.IND\_5.IND\_5\_IO | PIT04611 |
| IW1.21.0.1 | Status | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.CNT\_1.IND\_5.IND\_5\_Status |  |
| IW1.21.0.2 | UnitStatus | HwStatus |  |  |

Table 62. Controller\_3.1.21.0 Analog Input (AI)short (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.21.0.0 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.CNT\_1.IND\_5.IND\_5\_IO | 0.0 | 10.0 | bar | 2 | false |
| IW1.21.0.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.CNT\_1.IND\_5.IND\_5\_Status |  |  |  |  |  |

Table 63. Controller\_3.1.21.0 Analog Input (AI)short (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.21.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.CNT\_1.IND\_5.IND\_5\_IO |
| IW1.21.0.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.CNT\_1.IND\_5.IND\_5\_Status |

Table 64. Controller\_3.1.21.0 Analog Input (AI)short (Status)

#### 22 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.22.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 65. Controller\_3.1.22 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.22.0.0 | Temp1 Value | RealIO | Ellato\_3.U\_04C91\_00.IND\_1ax3.IND\_1\_IO | TIT09101 |
| IW1.22.0.1 | Temp1 Status | DintIO | Ellato\_3.U\_04C91\_00.IND\_1ax3.IND\_1\_IO\_Status |  |
| IW1.22.0.2 | UnitStatus | HwStatus |  |  |

Table 66. Controller\_3.1.22.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.22.0.0 | Ellato\_3.U\_04C91\_00.IND\_1ax3.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW1.22.0.1 | Ellato\_3.U\_04C91\_00.IND\_1ax3.IND\_1\_IO\_Status |  |  |  |  |  |

Table 67. Controller\_3.1.22.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.22.0.0 |  |  |  | Ellato\_3.U\_04C91\_00.IND\_1ax3.IND\_1\_IO |
| IW1.22.0.1 |  |  |  | Ellato\_3.U\_04C91\_00.IND\_1ax3.IND\_1\_IO\_Status |

Table 68. Controller\_3.1.22.0 Temperature 1 (Status)

#### 23 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.23.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 69. Controller\_3.1.23 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.23.0.0 | Temp1 Value | RealIO | Ellato\_3.U\_02C06\_Tart.IND\_1.IND\_1\_IO | TIT00601 |
| IW1.23.0.1 | Temp1 Status | DintIO | Ellato\_3.U\_02C06\_Tart.IND\_1.IND\_1\_IO\_Status |  |
| IW1.23.0.2 | UnitStatus | HwStatus |  |  |

Table 70. Controller\_3.1.23.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.23.0.0 | Ellato\_3.U\_02C06\_Tart.IND\_1.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW1.23.0.1 | Ellato\_3.U\_02C06\_Tart.IND\_1.IND\_1\_IO\_Status |  |  |  |  |  |

Table 71. Controller\_3.1.23.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.23.0.0 |  |  |  | Ellato\_3.U\_02C06\_Tart.IND\_1.IND\_1\_IO |
| IW1.23.0.1 |  |  |  | Ellato\_3.U\_02C06\_Tart.IND\_1.IND\_1\_IO\_Status |

Table 72. Controller\_3.1.23.0 Temperature 1 (Status)

#### 24 Pressure Transmitter 2000T

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.24.0 | UnitStatus | HwStatus |  |  |

Table 73. Controller\_3.1.24 Pressure Transmitter 2000T (Connections)

##### 0 Analog Input (AI)short

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.24.0.0 | Value | RealIO | Ellato\_3.U\_02C06\_Tart.EqM01A.CNT\_1.IND\_5.IND\_5\_IO | PIT00611 |
| IW1.24.0.1 | Status | DintIO | Ellato\_3.U\_02C06\_Tart.EqM01A.CNT\_1.IND\_5.IND\_5\_Status |  |
| IW1.24.0.2 | UnitStatus | HwStatus |  |  |

Table 74. Controller\_3.1.24.0 Analog Input (AI)short (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.24.0.0 | Ellato\_3.U\_02C06\_Tart.EqM01A.CNT\_1.IND\_5.IND\_5\_IO | 0.0 | 10.0 | bar | 2 | false |
| IW1.24.0.1 | Ellato\_3.U\_02C06\_Tart.EqM01A.CNT\_1.IND\_5.IND\_5\_Status |  |  |  |  |  |

Table 75. Controller\_3.1.24.0 Analog Input (AI)short (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.24.0.0 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM01A.CNT\_1.IND\_5.IND\_5\_IO |
| IW1.24.0.1 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM01A.CNT\_1.IND\_5.IND\_5\_Status |

Table 76. Controller\_3.1.24.0 Analog Input (AI)short (Status)

#### 25 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.25.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 77. Controller\_3.1.25 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.25.0.0 | Temp1 Value | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_A.IND\_1\_IO | TIT01002 |
| IW1.25.0.1 | Temp1 Status | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_A.IND\_1\_IO\_Status |  |
| IW1.25.0.2 | UnitStatus | HwStatus |  |  |

Table 78. Controller\_3.1.25.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.25.0.0 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_A.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW1.25.0.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_A.IND\_1\_IO\_Status |  |  |  |  |  |

Table 79. Controller\_3.1.25.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.25.0.0 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_A.IND\_1\_IO |
| IW1.25.0.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_A.IND\_1\_IO\_Status |

Table 80. Controller\_3.1.25.0 Temperature 1 (Status)

#### 26 Pressure Transmitter (9700)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.26.0 | UnitStatus | HwStatus |  |  |

Table 81. Controller\_3.1.26 Pressure Transmitter (9700) (Connections)

##### 0 Analog Input (AI)short

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.26.0.0 | Value | RealIO | Ellato\_3.U\_04C91\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_IO | PIT09111 |
| IW1.26.0.1 | Status | DintIO | Ellato\_3.U\_04C91\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_Status |  |
| IW1.26.0.2 | UnitStatus | HwStatus |  |  |

Table 82. Controller\_3.1.26.0 Analog Input (AI)short (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.26.0.0 | Ellato\_3.U\_04C91\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_IO | 0.0 | 10.0 | bar | 2 | false |
| IW1.26.0.1 | Ellato\_3.U\_04C91\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_Status |  |  |  |  |  |

Table 83. Controller\_3.1.26.0 Analog Input (AI)short (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.26.0.0 |  |  |  | Ellato\_3.U\_04C91\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_IO |
| IW1.26.0.1 |  |  |  | Ellato\_3.U\_04C91\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_Status |

Table 84. Controller\_3.1.26.0 Analog Input (AI)short (Status)

#### 27 ABB\_TTX\_300\_PA

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Status format | condensed | enum |  |  |  |
| Parameterization | DPV1 only | enum |  |  |  |
| Measurement Type (PV =) | SV1 (Sensor 1) | enum |  |  |  |
| PV unit | degF | enum |  |  |  |
| Signal Characteristic | medium | enum |  |  |  |
| Sensor 1 (LIN\_TYPE) | Pt100 (IEC751) | enum |  |  |  |
| Sensor Connect. 1 / RJ Type 1 | 3 wire / internal reference | enum |  |  |  |
| Line R 1 / Fix. RJ Val. 1 (x100) | 0 | dint |  | -32000 | 32000 |
| Sensor 2 (LIN\_TYPE\_2) | Off | enum |  |  |  |
| Sensor Connect. 2 / RJ Type 2 | 3 wire / internal reference | enum |  |  |  |
| Line R 2 / Fix. RJ Val. 2 (x100) | 0 | dint |  | -32000 | 32000 |
| HMI Language | English | enum |  |  |  |
| HMI Local operation | Enable | enum |  |  |  |
| HMI Autoscroll | Disable | enum |  |  |  |
| HMI Contrast | 50 | dint |  | 0 | 100 |
| HMI Select View1 | Calculated Value | enum |  |  |  |
| HMI Select View2 Line1 | Sensor 1 | enum |  |  |  |
| HMI Select View2 Line2 | Sensor 2 | enum |  |  |  |
| DMask1: Warmstart | Disable | enum |  |  |  |
| DMask1: Coldstart | Disable | enum |  |  |  |
| DMask1: Sensor drift | Disable | enum |  |  |  |
| DMask1: S1 line res. too high | Disable | enum |  |  |  |
| DMask1: S1 short circuit | Disable | enum |  |  |  |
| DMask1: S1 wire break | Disable | enum |  |  |  |
| DMask1: S2 line res. too high | Disable | enum |  |  |  |
| DMask1: S2 short circuit | Disable | enum |  |  |  |
| DMask1: S2 wire break | Disable | enum |  |  |  |
| DMask1: Over meas. range S1 | Disable | enum |  |  |  |
| DMask1: Under meas. range S1 | Disable | enum |  |  |  |
| DMask1: Over meas. range S2 | Disable | enum |  |  |  |
| DMask1: Under meas. range S2 | Disable | enum |  |  |  |
| DMask1: Dev.Temp outside spec. | Disable | enum |  |  |  |
| DMask1: Hardware failure | Disable | enum |  |  |  |
| DMask1: Device not calibrated | Disable | enum |  |  |  |
| DMask1: Device is simulated | Disable | enum |  |  |  |
| DMask1: Configuration failure | Disable | enum |  |  |  |
| DMask1: Red. Sensor 1+2 failure | Disable | enum |  |  |  |
| DMask1: Red. S1 short circuit | Disable | enum |  |  |  |
| DMask1: Red. S1 wire break | Disable | enum |  |  |  |
| DMask1: Red. S2 short circuit | Disable | enum |  |  |  |
| DMask1: Red. S2 wire break | Disable | enum |  |  |  |
| DMask2: Warmstart | Disable | enum |  |  |  |
| DMask2: Coldstart | Disable | enum |  |  |  |
| DMask2: Sensor drift | Disable | enum |  |  |  |
| DMask2: S1 line res. too high | Disable | enum |  |  |  |
| DMask2: S1 short circuit | Disable | enum |  |  |  |
| DMask2: S1 wire break | Disable | enum |  |  |  |
| DMask2: S2 line res. too high | Disable | enum |  |  |  |
| DMask2: S2 short circuit | Disable | enum |  |  |  |
| DMask2: S2 wire break | Disable | enum |  |  |  |
| DMask2: Over meas. range S1 | Disable | enum |  |  |  |
| DMask2: Under meas. range S1 | Disable | enum |  |  |  |
| DMask2: Over meas. range S2 | Disable | enum |  |  |  |
| DMask2: Under meas. range S2 | Disable | enum |  |  |  |
| DMask2: Dev.Temp outside spec. | Disable | enum |  |  |  |
| DMask2: Hardware failure | Disable | enum |  |  |  |
| DMask2: Device not calibrated | Disable | enum |  |  |  |
| DMask2: Device is simulated | Disable | enum |  |  |  |
| DMask2: Configuration failure | Disable | enum |  |  |  |
| DMask2: Red. Sensor 1+2 failure | Disable | enum |  |  |  |
| DMask2: Red. S1 short circuit | Disable | enum |  |  |  |
| DMask2: Red. S1 wire break | Disable | enum |  |  |  |
| DMask2: Red. S2 short circuit | Disable | enum |  |  |  |
| DMask2: Red. S2 wire break | Disable | enum |  |  |  |

Table 85. Controller\_3 (172.16.96.153).1.27 ABB\_TTX\_300\_PA (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.27.0 | UnitStatus | HwStatus |  |  |

Table 86. Controller\_3.1.27 ABB\_TTX\_300\_PA (Connections)

##### 1 Analog Input (AI)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Scale START input (PVEu0 x10) | 0 | dint |  | -32000 | 32000 |
| Scale END input (PVEu100 x10) | 1000 | dint |  | -32000 | 32000 |
| Scale START output (OUTEu0 x10) | 0 | dint |  | -32000 | 32000 |
| Scale END output (OUTEu100 x10) | 1000 | dint |  | -32000 | 32000 |
| Unit | degC | enum |  |  |  |
| Damping (PVTIME) | no | enum |  |  |  |
| Failure strategy (FSAFE\_TYPE) | Use calculated value + BAD | enum |  |  |  |
| Failsafe value (FSAFE\_VALUE x10) | 32000 | dint |  | -32000 | 32000 |
| Upper lim. alarm (HI\_HI\_LIM x10) | 32001 | dint |  | -32001 | 32001 |
| Upper lim. warning (HI\_LIM x10) | 32001 | dint |  | -32001 | 32001 |
| Lower lim. warning (LO\_LIM x10) | -32001 | dint |  | -32001 | 32001 |
| Lower lim. alarm (LO\_LO\_LIM x10) | -32001 | dint |  | -32001 | 32001 |

Table 87. Controller\_3 (172.16.96.153).1.27.1 Analog Input (AI) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.27.1.0 | Analog Input | RealIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.IND\_1.IND\_1\_IO | TIT04609 |
| IW1.27.1.1 | Analog Input Status | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.IND\_1.IND\_1\_IO\_Status |  |
| IW1.27.1.2 | UnitStatus | HwStatus |  |  |

Table 88. Controller\_3.1.27.1 Analog Input (AI) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.27.1.0 | Cikloprpil\_D6\_laktol.U\_14C46\_00.IND\_1.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW1.27.1.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.IND\_1.IND\_1\_IO\_Status |  |  |  |  |  |

Table 89. Controller\_3.1.27.1 Analog Input (AI) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.27.1.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.IND\_1.IND\_1\_IO |
| IW1.27.1.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.IND\_1.IND\_1\_IO\_Status |

Table 90. Controller\_3.1.27.1 Analog Input (AI) (Status)

#### 29 ABB\_TZIDC\_x10\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.29.0 | UnitStatus V3\_0\_11 | HwStatus |  |  |

Table 91. Controller\_3.1.29 ABB\_TZIDC\_x10\_PA (Connections)

##### 0 SP+READBACK+POS\_D

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.29.0.0 | READBACK Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.CNT\_4B.CNTValve\_IO.FB | KCV02644 |
| IW1.29.0.1 | READBACK Status | DintIO |  |  |
| IW1.29.0.2 | POS\_D Value | DintIO |  |  |
| IW1.29.0.3 | POS\_D Status | DintIO |  |  |
| QW1.29.0.4 | SETPOINT Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.CNT\_4B.CNTValve\_IO.SP | KCV02644 |
| QW1.29.0.5 | SETPOINT Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.CNT\_4B.SP\_Control | KCV02644 |
| IW1.29.0.6 | UnitStatus | HwStatus |  |  |

Table 92. Controller\_3.1.29.0 SP+READBACK+POS\_D (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.29.0.0 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.CNT\_4B.CNTValve\_IO.FB | 0.0 | 100.0 |  | 1 | false |
| IW1.29.0.1 |  |  |  |  |  |  |
| IW1.29.0.2 |  |  |  |  |  |  |
| IW1.29.0.3 |  |  |  |  |  |  |
| QW1.29.0.4 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.CNT\_4B.CNTValve\_IO.SP | 0.0 | 100.0 |  | 1 | false |
| QW1.29.0.5 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.CNT\_4B.SP\_Control |  |  |  |  |  |

Table 93. Controller\_3.1.29.0 SP+READBACK+POS\_D (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.29.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.CNT\_4B.CNTValve\_IO.FB |
| IW1.29.0.1 |  |  |  |  |
| IW1.29.0.2 |  |  |  |  |
| IW1.29.0.3 |  |  |  |  |
| QW1.29.0.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.CNT\_4B.CNTValve\_IO.SP |
| QW1.29.0.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.CNT\_4B.SP\_Control |

Table 94. Controller\_3.1.29.0 SP+READBACK+POS\_D (Status)

#### 30 Pressure Transmitter 2000T

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.30.0 | UnitStatus | HwStatus |  |  |

Table 95. Controller\_3.1.30 Pressure Transmitter 2000T (Connections)

##### 0 Analog Input (AI)short

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.30.0.0 | Value | RealIO | Ellato\_3.U\_02C06\_Tart.IND\_5ax3.IND\_5\_IO | PIT00613 |
| IW1.30.0.1 | Status | DintIO | Ellato\_3.U\_02C06\_Tart.IND\_5ax3.IND\_5\_Status |  |
| IW1.30.0.2 | UnitStatus | HwStatus |  |  |

Table 96. Controller\_3.1.30.0 Analog Input (AI)short (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.30.0.0 | Ellato\_3.U\_02C06\_Tart.IND\_5ax3.IND\_5\_IO | 0.0 | 1000.0 | mbar | 2 | false |
| IW1.30.0.1 | Ellato\_3.U\_02C06\_Tart.IND\_5ax3.IND\_5\_Status |  |  |  |  |  |

Table 97. Controller\_3.1.30.0 Analog Input (AI)short (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.30.0.0 |  |  |  | Ellato\_3.U\_02C06\_Tart.IND\_5ax3.IND\_5\_IO |
| IW1.30.0.1 |  |  |  | Ellato\_3.U\_02C06\_Tart.IND\_5ax3.IND\_5\_Status |

Table 98. Controller\_3.1.30.0 Analog Input (AI)short (Status)

#### 81 S900

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.81.0 | UnitStatus | HwStatus |  |  |

Table 99. Controller\_3.1.81 S900 (Connections)

##### 0 CI920\* (CIPB)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Grid Frequency | 50 Hz | enum |  |  |  |
| Analog Status | on | enum |  |  |  |
| Internal Bus Redundancy | false | bool |  |  |  |
| CIPB Redundancy | false | bool |  |  |  |
| Power Supply Redundancy | false | bool |  |  |  |

Table 100. Controller\_3 (172.16.96.153).1.81.0 CI920\* (CIPB) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.81.0.1 | UnitStatus | HwStatus |  |  |

Table 101. Controller\_3.1.81.0 CI920\* (CIPB) (Connections)

##### 1 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 102. Controller\_3 (172.16.96.153).1.81.1 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.81.1.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM12A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG00236 |
| QX1.81.1.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3\_A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG00247 |
| QX1.81.1.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3B.GCS\_1A.GCS\_1\_IO.Cmd | KSCG00249 |
| QX1.81.1.4 | Output 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd | KSVG00262 |
| QW1.81.1.5 | All outputs | DwordIO |  |  |
| IW1.81.1.6 | UnitStatus | HwStatus |  |  |

Table 103. Controller\_3.1.81.1 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.81.1.1 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM12A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.1.2 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3\_A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.1.3 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3B.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.1.4 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.81.1.5 |  |  |  |  |  |  |

Table 104. Controller\_3.1.81.1 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.81.1.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM12A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.1.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3\_A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.1.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3B.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.1.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |
| QW1.81.1.5 |  |  |  |  |

Table 105. Controller\_3.1.81.1 DO910\* (DO4) (Status)

##### 2 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 106. Controller\_3 (172.16.96.153).1.81.2 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.81.2.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG00263 |
| QX1.81.2.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG00264 |
| QX1.81.2.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3C.GCS\_1A.GCS\_1\_IO.Cmd | KSVG00266 |
| QX1.81.2.4 | Output 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG0268 |
| QW1.81.2.5 | All outputs | DwordIO |  |  |
| IW1.81.2.6 | UnitStatus | HwStatus |  |  |

Table 107. Controller\_3.1.81.2 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.81.2.1 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.2.2 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.2.3 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3C.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.2.4 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.81.2.5 |  |  |  |  |  |  |

Table 108. Controller\_3.1.81.2 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.81.2.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX1.81.2.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.2.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3C.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.2.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QW1.81.2.5 |  |  |  |  |

Table 109. Controller\_3.1.81.2 DO910\* (DO4) (Status)

##### 3 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 110. Controller\_3 (172.16.96.153).1.81.3 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.81.3.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG00269 |
| QX1.81.3.2 | Output 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM21A\_1.GCS\_1A.GCS\_1\_IO.Cmd | KSVG01056 |
| QX1.81.3.3 | Output 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM21A\_2.GCS\_1A.GCS\_1\_IO.Cmd | KSVG01057 |
| QX1.81.3.4 | Output 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1A.GCS\_1\_IO.Cmd | KSVG01072 |
| QW1.81.3.5 | All outputs | DwordIO |  |  |
| IW1.81.3.6 | UnitStatus | HwStatus |  |  |

Table 111. Controller\_3.1.81.3 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.81.3.1 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.3.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM21A\_1.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.3.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM21A\_2.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.3.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.81.3.5 |  |  |  |  |  |  |

Table 112. Controller\_3.1.81.3 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.81.3.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.3.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM21A\_1.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.3.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM21A\_2.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.3.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1A.GCS\_1\_IO.Cmd |
| QW1.81.3.5 |  |  |  |  |

Table 113. Controller\_3.1.81.3 DO910\* (DO4) (Status)

##### 4 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 114. Controller\_3 (172.16.96.153).1.81.4 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.81.4.1 | Output 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1B.GCS\_1\_IO.Cmd | KSVG01073 |
| QX1.81.4.2 | Output 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1C.GCS\_1\_IO.Cmd | KSVG01074 |
| QX1.81.4.3 | Output 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_1.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd | KSVG01075 |
| QX1.81.4.4 | Output 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM11ax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG01080 |
| QW1.81.4.5 | All outputs | DwordIO |  |  |
| IW1.81.4.6 | UnitStatus | HwStatus |  |  |

Table 115. Controller\_3.1.81.4 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.81.4.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.4.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.4.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_1.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.4.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM11ax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.81.4.5 |  |  |  |  |  |  |

Table 116. Controller\_3.1.81.4 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.81.4.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1B.GCS\_1\_IO.Cmd |
| QX1.81.4.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1C.GCS\_1\_IO.Cmd |
| QX1.81.4.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_1.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd |
| QX1.81.4.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM11ax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QW1.81.4.5 |  |  |  |  |

Table 117. Controller\_3.1.81.4 DO910\* (DO4) (Status)

##### 5 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 118. Controller\_3 (172.16.96.153).1.81.5 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.81.5.1 | Output 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1A.GCS\_1\_IO.Cmd | KSVG01082 |
| QX1.81.5.2 | Output 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1B.GCS\_1\_IO.Cmd | KSVG01083 |
| QX1.81.5.3 | Output 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1C.GCS\_1\_IO.Cmd | KSVG01084 |
| QX1.81.5.4 | Output 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_2.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd | KSVG01085 |
| QW1.81.5.5 | All outputs | DwordIO |  |  |
| IW1.81.5.6 | UnitStatus | HwStatus |  |  |

Table 119. Controller\_3.1.81.5 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.81.5.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.5.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.5.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.5.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_2.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.81.5.5 |  |  |  |  |  |  |

Table 120. Controller\_3.1.81.5 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.81.5.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.5.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1B.GCS\_1\_IO.Cmd |
| QX1.81.5.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1C.GCS\_1\_IO.Cmd |
| QX1.81.5.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_2.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd |
| QW1.81.5.5 |  |  |  |  |

Table 121. Controller\_3.1.81.5 DO910\* (DO4) (Status)

##### 6 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 122. Controller\_3 (172.16.96.153).1.81.6 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.81.6.1 | Output 1 | BoolIO | Ellato\_3.KSVG06785\_Cmd | KSVG06785 |
| QX1.81.6.2 | Output 2 | BoolIO | Ellato\_3.KSVG06883\_Cmd | KSVG06883 |
| QX1.81.6.3 | Output 3 | BoolIO |  |  |
| QX1.81.6.4 | Output 4 | BoolIO |  |  |
| QW1.81.6.5 | All outputs | DwordIO |  |  |
| IW1.81.6.6 | UnitStatus | HwStatus |  |  |

Table 123. Controller\_3.1.81.6 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.81.6.1 | Ellato\_3.KSVG06785\_Cmd |  |  |  |  | false |
| QX1.81.6.2 | Ellato\_3.KSVG06883\_Cmd |  |  |  |  | false |
| QX1.81.6.3 |  |  |  |  |  | false |
| QX1.81.6.4 |  |  |  |  |  | false |
| QW1.81.6.5 |  |  |  |  |  |  |

Table 124. Controller\_3.1.81.6 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.81.6.1 |  |  |  | Ellato\_3.KSVG06785\_Cmd |
| QX1.81.6.2 |  |  |  | Ellato\_3.KSVG06883\_Cmd |
| QX1.81.6.3 |  |  |  |  |
| QX1.81.6.4 |  |  |  |  |
| QW1.81.6.5 |  |  |  |  |

Table 125. Controller\_3.1.81.6 DO910\* (DO4) (Status)

##### 7 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 126. Controller\_3 (172.16.96.153).1.81.7 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.81.7.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03964 |
| QX1.81.7.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03968 |
| QX1.81.7.3 | Output 3 | BoolIO |  |  |
| QX1.81.7.4 | Output 4 | BoolIO |  |  |
| QW1.81.7.5 | All outputs | DwordIO |  |  |
| IW1.81.7.6 | UnitStatus | HwStatus |  |  |

Table 127. Controller\_3.1.81.7 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.81.7.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.7.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.7.3 |  |  |  |  |  | false |
| QX1.81.7.4 |  |  |  |  |  | false |
| QW1.81.7.5 |  |  |  |  |  |  |

Table 128. Controller\_3.1.81.7 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.81.7.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.7.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.7.3 |  |  |  |  |
| QX1.81.7.4 |  |  |  |  |
| QW1.81.7.5 |  |  |  |  |

Table 129. Controller\_3.1.81.7 DO910\* (DO4) (Status)

##### 8 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 130. Controller\_3 (172.16.96.153).1.81.8 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.81.8.1 | Output 1 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM02A\_A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG00610 |
| QX1.81.8.2 | Output 2 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM02A\_C.GCS\_1A.GCS\_1\_IO.Cmd | KSVG00620 |
| QX1.81.8.3 | Output 3 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM05A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG00661 |
| QX1.81.8.4 | Output 4 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM01A.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd | KSVG00662 |
| QW1.81.8.5 | All outputs | DwordIO |  |  |
| IW1.81.8.6 | UnitStatus | HwStatus |  |  |

Table 131. Controller\_3.1.81.8 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.81.8.1 | Ellato\_3.U\_02C06\_Tart.EqM02A\_A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.8.2 | Ellato\_3.U\_02C06\_Tart.EqM02A\_C.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.8.3 | Ellato\_3.U\_02C06\_Tart.EqM05A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.8.4 | Ellato\_3.U\_02C06\_Tart.EqM01A.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.81.8.5 |  |  |  |  |  |  |

Table 132. Controller\_3.1.81.8 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.81.8.1 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM02A\_A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.8.2 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM02A\_C.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.8.3 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM05A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.8.4 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM01A.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |
| QW1.81.8.5 |  |  |  |  |

Table 133. Controller\_3.1.81.8 DO910\* (DO4) (Status)

##### 9 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 134. Controller\_3 (172.16.96.153).1.81.9 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.81.9.1 | Output 1 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM01A.GCS\_1B.GCS\_1\_IO.Cmd | KSVG00663 |
| QX1.81.9.2 | Output 2 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM04D.GCS\_1A.GCS\_1\_IO.Cmd | KSVG00665 |
| QX1.81.9.3 | Output 3 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM02A\_B.GCS\_1A.GCS\_1\_IO.Cmd | KSVG00666 |
| QX1.81.9.4 | Output 4 | BoolIO | Ellato\_3.KSVG06786\_Cmd | KSVG06786 |
| QW1.81.9.5 | All outputs | DwordIO |  |  |
| IW1.81.9.6 | UnitStatus | HwStatus |  |  |

Table 135. Controller\_3.1.81.9 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.81.9.1 | Ellato\_3.U\_02C06\_Tart.EqM01A.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.9.2 | Ellato\_3.U\_02C06\_Tart.EqM04D.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.9.3 | Ellato\_3.U\_02C06\_Tart.EqM02A\_B.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.9.4 | Ellato\_3.KSVG06786\_Cmd |  |  |  |  | false |
| QW1.81.9.5 |  |  |  |  |  |  |

Table 136. Controller\_3.1.81.9 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.81.9.1 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM01A.GCS\_1B.GCS\_1\_IO.Cmd |
| QX1.81.9.2 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM04D.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.9.3 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM02A\_B.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.9.4 |  |  |  | Ellato\_3.KSVG06786\_Cmd |
| QW1.81.9.5 |  |  |  |  |

Table 137. Controller\_3.1.81.9 DO910\* (DO4) (Status)

##### 10 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 138. Controller\_3 (172.16.96.153).1.81.10 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.81.10.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1J.GCS\_1\_IO.Cmd | KSVG04697 |
| QX1.81.10.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1K.GCS\_1\_IO.Cmd | KSVG04698 |
| QX1.81.10.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04688 |
| QX1.81.10.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1H.GCS\_1\_IO.Cmd | KSVG04695 |
| QW1.81.10.5 | All outputs | DwordIO |  |  |
| IW1.81.10.6 | UnitStatus | HwStatus |  |  |

Table 139. Controller\_3.1.81.10 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.81.10.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1J.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.10.2 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1K.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.10.3 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.81.10.4 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1H.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.81.10.5 |  |  |  |  |  |  |

Table 140. Controller\_3.1.81.10 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.81.10.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1J.GCS\_1\_IO.Cmd |
| QX1.81.10.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1K.GCS\_1\_IO.Cmd |
| QX1.81.10.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.81.10.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1H.GCS\_1\_IO.Cmd |
| QW1.81.10.5 |  |  |  |  |

Table 141. Controller\_3.1.81.10 DO910\* (DO4) (Status)

##### 11 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 142. Controller\_3 (172.16.96.153).1.81.11 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.81.11.1 | Input 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM12A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG00236 |
| IX1.81.11.2 | Input 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3\_A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG00247 |
| IX1.81.11.3 | Input 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3B.GCS\_1A.GCS\_1\_IO.FB0 | KSVG00249 |
| IX1.81.11.4 | Input 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 | KSVG00262 |
| IX1.81.11.5 | Input 5 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG00263 |
| IX1.81.11.6 | Input 6 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG00264 |
| IX1.81.11.7 | Input 7 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3C.GCS\_1A.GCS\_1\_IO.FB0 | KSVG00266 |
| IX1.81.11.8 | Input 8 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG0268 |
| IW1.81.11.9 | All inputs | DwordIO |  |  |
| QX1.81.11.10 | Output 1 | BoolIO |  |  |
| QX1.81.11.11 | Output 2 | BoolIO |  |  |
| QX1.81.11.12 | Output 3 | BoolIO |  |  |
| QX1.81.11.13 | Output 4 | BoolIO |  |  |
| QX1.81.11.14 | Output 5 | BoolIO |  |  |
| QX1.81.11.15 | Output 6 | BoolIO |  |  |
| QX1.81.11.16 | Output 7 | BoolIO |  |  |
| QX1.81.11.17 | Output 8 | BoolIO |  |  |
| QW1.81.11.18 | All outputs | DwordIO |  |  |
| IX1.81.11.19 | Status Input 1 | BoolIO |  |  |
| IX1.81.11.20 | Status Input 2 | BoolIO |  |  |
| IX1.81.11.21 | Status Input 3 | BoolIO |  |  |
| IX1.81.11.22 | Status Input 4 | BoolIO |  |  |
| IX1.81.11.23 | Status Input 5 | BoolIO |  |  |
| IX1.81.11.24 | Status Input 6 | BoolIO |  |  |
| IX1.81.11.25 | Status Input 7 | BoolIO |  |  |
| IX1.81.11.26 | Status Input 8 | BoolIO |  |  |
| IW1.81.11.27 | UnitStatus | HwStatus |  |  |

Table 143. Controller\_3.1.81.11 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.81.11.1 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM12A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.11.2 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3\_A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.11.3 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3B.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.11.4 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.11.5 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.11.6 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.11.7 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3C.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.11.8 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW1.81.11.9 |  |  |  |  |  |  |
| QX1.81.11.10 |  |  |  |  |  | false |
| QX1.81.11.11 |  |  |  |  |  | false |
| QX1.81.11.12 |  |  |  |  |  | false |
| QX1.81.11.13 |  |  |  |  |  | false |
| QX1.81.11.14 |  |  |  |  |  | false |
| QX1.81.11.15 |  |  |  |  |  | false |
| QX1.81.11.16 |  |  |  |  |  | false |
| QX1.81.11.17 |  |  |  |  |  | false |
| QW1.81.11.18 |  |  |  |  |  |  |
| IX1.81.11.19 |  |  |  |  |  | false |
| IX1.81.11.20 |  |  |  |  |  | false |
| IX1.81.11.21 |  |  |  |  |  | false |
| IX1.81.11.22 |  |  |  |  |  | false |
| IX1.81.11.23 |  |  |  |  |  | false |
| IX1.81.11.24 |  |  |  |  |  | false |
| IX1.81.11.25 |  |  |  |  |  | false |
| IX1.81.11.26 |  |  |  |  |  | false |

Table 144. Controller\_3.1.81.11 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.81.11.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM12A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.11.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3\_A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.11.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3B.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.11.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |
| IX1.81.11.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IX1.81.11.6 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.11.7 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02Aax3C.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.11.8 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IW1.81.11.9 |  |  |  |  |
| QX1.81.11.10 |  |  |  |  |
| QX1.81.11.11 |  |  |  |  |
| QX1.81.11.12 |  |  |  |  |
| QX1.81.11.13 |  |  |  |  |
| QX1.81.11.14 |  |  |  |  |
| QX1.81.11.15 |  |  |  |  |
| QX1.81.11.16 |  |  |  |  |
| QX1.81.11.17 |  |  |  |  |
| QW1.81.11.18 |  |  |  |  |
| IX1.81.11.19 |  |  |  |  |
| IX1.81.11.20 |  |  |  |  |
| IX1.81.11.21 |  |  |  |  |
| IX1.81.11.22 |  |  |  |  |
| IX1.81.11.23 |  |  |  |  |
| IX1.81.11.24 |  |  |  |  |
| IX1.81.11.25 |  |  |  |  |
| IX1.81.11.26 |  |  |  |  |

Table 145. Controller\_3.1.81.11 DX910\* (DIO8 S) (Status)

##### 12 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 146. Controller\_3 (172.16.96.153).1.81.12 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.81.12.1 | Input 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG00269 |
| IX1.81.12.2 | Input 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM21A\_1.GCS\_1A.GCS\_1\_IO.FB0 | KSVG01056 |
| IX1.81.12.3 | Input 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM21A\_2.GCS\_1A.GCS\_1\_IO.FB0 | KSVG01057 |
| IX1.81.12.4 | Input 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1A.GCS\_1\_IO.FB0 | KSVG01072 |
| IX1.81.12.5 | Input 5 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1B.GCS\_1\_IO.FB0 | KSVG01073 |
| IX1.81.12.6 | Input 6 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1C.GCS\_1\_IO.FB0 | KSVG01074 |
| IX1.81.12.7 | Input 7 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_1.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 | KSVG01075 |
| IX1.81.12.8 | Input 8 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM11ax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG01080 |
| IW1.81.12.9 | All inputs | DwordIO |  |  |
| QX1.81.12.10 | Output 1 | BoolIO |  |  |
| QX1.81.12.11 | Output 2 | BoolIO |  |  |
| QX1.81.12.12 | Output 3 | BoolIO |  |  |
| QX1.81.12.13 | Output 4 | BoolIO |  |  |
| QX1.81.12.14 | Output 5 | BoolIO |  |  |
| QX1.81.12.15 | Output 6 | BoolIO |  |  |
| QX1.81.12.16 | Output 7 | BoolIO |  |  |
| QX1.81.12.17 | Output 8 | BoolIO |  |  |
| QW1.81.12.18 | All outputs | DwordIO |  |  |
| IX1.81.12.19 | Status Input 1 | BoolIO |  |  |
| IX1.81.12.20 | Status Input 2 | BoolIO |  |  |
| IX1.81.12.21 | Status Input 3 | BoolIO |  |  |
| IX1.81.12.22 | Status Input 4 | BoolIO |  |  |
| IX1.81.12.23 | Status Input 5 | BoolIO |  |  |
| IX1.81.12.24 | Status Input 6 | BoolIO |  |  |
| IX1.81.12.25 | Status Input 7 | BoolIO |  |  |
| IX1.81.12.26 | Status Input 8 | BoolIO |  |  |
| IW1.81.12.27 | UnitStatus | HwStatus |  |  |

Table 147. Controller\_3.1.81.12 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.81.12.1 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.12.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM21A\_1.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.12.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM21A\_2.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.12.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.12.5 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.12.6 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.12.7 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_1.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.12.8 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM11ax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW1.81.12.9 |  |  |  |  |  |  |
| QX1.81.12.10 |  |  |  |  |  | false |
| QX1.81.12.11 |  |  |  |  |  | false |
| QX1.81.12.12 |  |  |  |  |  | false |
| QX1.81.12.13 |  |  |  |  |  | false |
| QX1.81.12.14 |  |  |  |  |  | false |
| QX1.81.12.15 |  |  |  |  |  | false |
| QX1.81.12.16 |  |  |  |  |  | false |
| QX1.81.12.17 |  |  |  |  |  | false |
| QW1.81.12.18 |  |  |  |  |  |  |
| IX1.81.12.19 |  |  |  |  |  | false |
| IX1.81.12.20 |  |  |  |  |  | false |
| IX1.81.12.21 |  |  |  |  |  | false |
| IX1.81.12.22 |  |  |  |  |  | false |
| IX1.81.12.23 |  |  |  |  |  | false |
| IX1.81.12.24 |  |  |  |  |  | false |
| IX1.81.12.25 |  |  |  |  |  | false |
| IX1.81.12.26 |  |  |  |  |  | false |

Table 148. Controller\_3.1.81.12 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.81.12.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.12.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM21A\_1.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.12.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM21A\_2.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.12.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.12.5 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1B.GCS\_1\_IO.FB0 |
| IX1.81.12.6 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_1.GCS\_1C.GCS\_1\_IO.FB0 |
| IX1.81.12.7 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_1.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 |
| IX1.81.12.8 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM11ax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IW1.81.12.9 |  |  |  |  |
| QX1.81.12.10 |  |  |  |  |
| QX1.81.12.11 |  |  |  |  |
| QX1.81.12.12 |  |  |  |  |
| QX1.81.12.13 |  |  |  |  |
| QX1.81.12.14 |  |  |  |  |
| QX1.81.12.15 |  |  |  |  |
| QX1.81.12.16 |  |  |  |  |
| QX1.81.12.17 |  |  |  |  |
| QW1.81.12.18 |  |  |  |  |
| IX1.81.12.19 |  |  |  |  |
| IX1.81.12.20 |  |  |  |  |
| IX1.81.12.21 |  |  |  |  |
| IX1.81.12.22 |  |  |  |  |
| IX1.81.12.23 |  |  |  |  |
| IX1.81.12.24 |  |  |  |  |
| IX1.81.12.25 |  |  |  |  |
| IX1.81.12.26 |  |  |  |  |

Table 149. Controller\_3.1.81.12 DX910\* (DIO8 S) (Status)

##### 13 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | true | bool |  |  |  |
| ch. 8: deactivate | true | bool |  |  |  |

Table 150. Controller\_3 (172.16.96.153).1.81.13 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.81.13.1 | Input 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1A.GCS\_1\_IO.FB0 | KSVG01082 |
| IX1.81.13.2 | Input 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1B.GCS\_1\_IO.FB0 | KSVG01083 |
| IX1.81.13.3 | Input 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1C.GCS\_1\_IO.FB0 | KSVG01084 |
| IX1.81.13.4 | Input 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_2.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 | KSVG01085 |
| IX1.81.13.5 | Input 5 | BoolIO | Ellato\_3.KSVG06785\_FB0 | KSVG06785 |
| IX1.81.13.6 | Input 6 | BoolIO | Ellato\_3.KSVG06883\_FB0 | KSVG06883 |
| IX1.81.13.7 | Input 7 | BoolIO |  |  |
| IX1.81.13.8 | Input 8 | BoolIO |  |  |
| IW1.81.13.9 | All inputs | DwordIO |  |  |
| QX1.81.13.10 | Output 1 | BoolIO |  |  |
| QX1.81.13.11 | Output 2 | BoolIO |  |  |
| QX1.81.13.12 | Output 3 | BoolIO |  |  |
| QX1.81.13.13 | Output 4 | BoolIO |  |  |
| QX1.81.13.14 | Output 5 | BoolIO |  |  |
| QX1.81.13.15 | Output 6 | BoolIO |  |  |
| QX1.81.13.16 | Output 7 | BoolIO |  |  |
| QX1.81.13.17 | Output 8 | BoolIO |  |  |
| QW1.81.13.18 | All outputs | DwordIO |  |  |
| IX1.81.13.19 | Status Input 1 | BoolIO |  |  |
| IX1.81.13.20 | Status Input 2 | BoolIO |  |  |
| IX1.81.13.21 | Status Input 3 | BoolIO |  |  |
| IX1.81.13.22 | Status Input 4 | BoolIO |  |  |
| IX1.81.13.23 | Status Input 5 | BoolIO |  |  |
| IX1.81.13.24 | Status Input 6 | BoolIO |  |  |
| IX1.81.13.25 | Status Input 7 | BoolIO |  |  |
| IX1.81.13.26 | Status Input 8 | BoolIO |  |  |
| IW1.81.13.27 | UnitStatus | HwStatus |  |  |

Table 151. Controller\_3.1.81.13 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.81.13.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.13.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.13.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.13.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_2.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.13.5 | Ellato\_3.KSVG06785\_FB0 |  |  |  |  | false |
| IX1.81.13.6 | Ellato\_3.KSVG06883\_FB0 |  |  |  |  | false |
| IX1.81.13.7 |  |  |  |  |  | false |
| IX1.81.13.8 |  |  |  |  |  | false |
| QW1.81.13.9 |  |  |  |  |  |  |
| QX1.81.13.10 |  |  |  |  |  | false |
| QX1.81.13.11 |  |  |  |  |  | false |
| QX1.81.13.12 |  |  |  |  |  | false |
| QX1.81.13.13 |  |  |  |  |  | false |
| QX1.81.13.14 |  |  |  |  |  | false |
| QX1.81.13.15 |  |  |  |  |  | false |
| QX1.81.13.16 |  |  |  |  |  | false |
| QX1.81.13.17 |  |  |  |  |  | false |
| QW1.81.13.18 |  |  |  |  |  |  |
| IX1.81.13.19 |  |  |  |  |  | false |
| IX1.81.13.20 |  |  |  |  |  | false |
| IX1.81.13.21 |  |  |  |  |  | false |
| IX1.81.13.22 |  |  |  |  |  | false |
| IX1.81.13.23 |  |  |  |  |  | false |
| IX1.81.13.24 |  |  |  |  |  | false |
| IX1.81.13.25 |  |  |  |  |  | false |
| IX1.81.13.26 |  |  |  |  |  | false |

Table 152. Controller\_3.1.81.13 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.81.13.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.13.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1B.GCS\_1\_IO.FB0 |
| IX1.81.13.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM22B\_2.GCS\_1C.GCS\_1\_IO.FB0 |
| IX1.81.13.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM27\_2.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 |
| IX1.81.13.5 |  |  |  | Ellato\_3.KSVG06785\_FB0 |
| IX1.81.13.6 |  |  |  | Ellato\_3.KSVG06883\_FB0 |
| IX1.81.13.7 |  |  |  |  |
| IX1.81.13.8 |  |  |  |  |
| IW1.81.13.9 |  |  |  |  |
| QX1.81.13.10 |  |  |  |  |
| QX1.81.13.11 |  |  |  |  |
| QX1.81.13.12 |  |  |  |  |
| QX1.81.13.13 |  |  |  |  |
| QX1.81.13.14 |  |  |  |  |
| QX1.81.13.15 |  |  |  |  |
| QX1.81.13.16 |  |  |  |  |
| QX1.81.13.17 |  |  |  |  |
| QW1.81.13.18 |  |  |  |  |
| IX1.81.13.19 |  |  |  |  |
| IX1.81.13.20 |  |  |  |  |
| IX1.81.13.21 |  |  |  |  |
| IX1.81.13.22 |  |  |  |  |
| IX1.81.13.23 |  |  |  |  |
| IX1.81.13.24 |  |  |  |  |
| IX1.81.13.25 |  |  |  |  |
| IX1.81.13.26 |  |  |  |  |

Table 153. Controller\_3.1.81.13 DX910\* (DIO8 S) (Status)

##### 14 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | true | bool |  |  |  |
| ch. 4: deactivate | true | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | true | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 154. Controller\_3 (172.16.96.153).1.81.14 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.81.14.1 | Input 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03964 |
| IX1.81.14.2 | Input 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03968 |
| IX1.81.14.3 | Input 3 | BoolIO |  |  |
| IX1.81.14.4 | Input 4 | BoolIO |  |  |
| IX1.81.14.5 | Input 5 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM02A\_A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG00610 |
| IX1.81.14.6 | Input 6 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM02A\_C.GCS\_1A.GCS\_1\_IO.FB0 | KSVG00620 |
| IX1.81.14.7 | Input 7 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM05A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG00661 |
| IX1.81.14.8 | Input 8 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM01A.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 | KSVG00662 |
| IW1.81.14.9 | All inputs | DwordIO |  |  |
| QX1.81.14.10 | Output 1 | BoolIO |  |  |
| QX1.81.14.11 | Output 2 | BoolIO |  |  |
| QX1.81.14.12 | Output 3 | BoolIO |  |  |
| QX1.81.14.13 | Output 4 | BoolIO |  |  |
| QX1.81.14.14 | Output 5 | BoolIO |  |  |
| QX1.81.14.15 | Output 6 | BoolIO |  |  |
| QX1.81.14.16 | Output 7 | BoolIO |  |  |
| QX1.81.14.17 | Output 8 | BoolIO |  |  |
| QW1.81.14.18 | All outputs | DwordIO |  |  |
| IX1.81.14.19 | Status Input 1 | BoolIO |  |  |
| IX1.81.14.20 | Status Input 2 | BoolIO |  |  |
| IX1.81.14.21 | Status Input 3 | BoolIO |  |  |
| IX1.81.14.22 | Status Input 4 | BoolIO |  |  |
| IX1.81.14.23 | Status Input 5 | BoolIO |  |  |
| IX1.81.14.24 | Status Input 6 | BoolIO |  |  |
| IX1.81.14.25 | Status Input 7 | BoolIO |  |  |
| IX1.81.14.26 | Status Input 8 | BoolIO |  |  |
| IW1.81.14.27 | UnitStatus | HwStatus |  |  |

Table 155. Controller\_3.1.81.14 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.81.14.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.14.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.14.3 |  |  |  |  |  | false |
| IX1.81.14.4 |  |  |  |  |  | false |
| IX1.81.14.5 | Ellato\_3.U\_02C06\_Tart.EqM02A\_A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.14.6 | Ellato\_3.U\_02C06\_Tart.EqM02A\_C.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.14.7 | Ellato\_3.U\_02C06\_Tart.EqM05A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.14.8 | Ellato\_3.U\_02C06\_Tart.EqM01A.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW1.81.14.9 |  |  |  |  |  |  |
| QX1.81.14.10 |  |  |  |  |  | false |
| QX1.81.14.11 |  |  |  |  |  | false |
| QX1.81.14.12 |  |  |  |  |  | false |
| QX1.81.14.13 |  |  |  |  |  | false |
| QX1.81.14.14 |  |  |  |  |  | false |
| QX1.81.14.15 |  |  |  |  |  | false |
| QX1.81.14.16 |  |  |  |  |  | false |
| QX1.81.14.17 |  |  |  |  |  | false |
| QW1.81.14.18 |  |  |  |  |  |  |
| IX1.81.14.19 |  |  |  |  |  | false |
| IX1.81.14.20 |  |  |  |  |  | false |
| IX1.81.14.21 |  |  |  |  |  | false |
| IX1.81.14.22 |  |  |  |  |  | false |
| IX1.81.14.23 |  |  |  |  |  | false |
| IX1.81.14.24 |  |  |  |  |  | false |
| IX1.81.14.25 |  |  |  |  |  | false |
| IX1.81.14.26 |  |  |  |  |  | false |

Table 156. Controller\_3.1.81.14 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.81.14.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.14.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.14.3 |  |  |  |  |
| IX1.81.14.4 |  |  |  |  |
| IX1.81.14.5 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM02A\_A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.14.6 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM02A\_C.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.14.7 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM05A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.14.8 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM01A.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |
| IW1.81.14.9 |  |  |  |  |
| QX1.81.14.10 |  |  |  |  |
| QX1.81.14.11 |  |  |  |  |
| QX1.81.14.12 |  |  |  |  |
| QX1.81.14.13 |  |  |  |  |
| QX1.81.14.14 |  |  |  |  |
| QX1.81.14.15 |  |  |  |  |
| QX1.81.14.16 |  |  |  |  |
| QX1.81.14.17 |  |  |  |  |
| QW1.81.14.18 |  |  |  |  |
| IX1.81.14.19 |  |  |  |  |
| IX1.81.14.20 |  |  |  |  |
| IX1.81.14.21 |  |  |  |  |
| IX1.81.14.22 |  |  |  |  |
| IX1.81.14.23 |  |  |  |  |
| IX1.81.14.24 |  |  |  |  |
| IX1.81.14.25 |  |  |  |  |
| IX1.81.14.26 |  |  |  |  |

Table 157. Controller\_3.1.81.14 DX910\* (DIO8 S) (Status)

##### 15 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 158. Controller\_3 (172.16.96.153).1.81.15 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.81.15.1 | Input 1 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM01A.GCS\_1B.GCS\_1\_IO.FB0 | KSVG00663 |
| IX1.81.15.2 | Input 2 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM04D.GCS\_1A.GCS\_1\_IO.FB0 | KSVG00665 |
| IX1.81.15.3 | Input 3 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM02A\_B.GCS\_1A.GCS\_1\_IO.FB0 | KSVG00666 |
| IX1.81.15.4 | Input 4 | BoolIO | Ellato\_3.KSVG06786\_FB0 | KSVG06786 |
| IX1.81.15.5 | Input 5 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1J.GCS\_1\_IO.FB0 | KSVG04697 |
| IX1.81.15.6 | Input 6 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1K.GCS\_1\_IO.FB0 | KSVG04698 |
| IX1.81.15.7 | Input 7 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04688 |
| IX1.81.15.8 | Input 8 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1H.GCS\_1\_IO.FB0 | KSVG04695 |
| IW1.81.15.9 | All inputs | DwordIO |  |  |
| QX1.81.15.10 | Output 1 | BoolIO |  |  |
| QX1.81.15.11 | Output 2 | BoolIO |  |  |
| QX1.81.15.12 | Output 3 | BoolIO |  |  |
| QX1.81.15.13 | Output 4 | BoolIO |  |  |
| QX1.81.15.14 | Output 5 | BoolIO |  |  |
| QX1.81.15.15 | Output 6 | BoolIO |  |  |
| QX1.81.15.16 | Output 7 | BoolIO |  |  |
| QX1.81.15.17 | Output 8 | BoolIO |  |  |
| QW1.81.15.18 | All outputs | DwordIO |  |  |
| IX1.81.15.19 | Status Input 1 | BoolIO |  |  |
| IX1.81.15.20 | Status Input 2 | BoolIO |  |  |
| IX1.81.15.21 | Status Input 3 | BoolIO |  |  |
| IX1.81.15.22 | Status Input 4 | BoolIO |  |  |
| IX1.81.15.23 | Status Input 5 | BoolIO |  |  |
| IX1.81.15.24 | Status Input 6 | BoolIO |  |  |
| IX1.81.15.25 | Status Input 7 | BoolIO |  |  |
| IX1.81.15.26 | Status Input 8 | BoolIO |  |  |
| IW1.81.15.27 | UnitStatus | HwStatus |  |  |

Table 159. Controller\_3.1.81.15 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.81.15.1 | Ellato\_3.U\_02C06\_Tart.EqM01A.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.15.2 | Ellato\_3.U\_02C06\_Tart.EqM04D.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.15.3 | Ellato\_3.U\_02C06\_Tart.EqM02A\_B.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.15.4 | Ellato\_3.KSVG06786\_FB0 |  |  |  |  | false |
| IX1.81.15.5 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1J.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.15.6 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1K.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.15.7 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.81.15.8 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1H.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW1.81.15.9 |  |  |  |  |  |  |
| QX1.81.15.10 |  |  |  |  |  | false |
| QX1.81.15.11 |  |  |  |  |  | false |
| QX1.81.15.12 |  |  |  |  |  | false |
| QX1.81.15.13 |  |  |  |  |  | false |
| QX1.81.15.14 |  |  |  |  |  | false |
| QX1.81.15.15 |  |  |  |  |  | false |
| QX1.81.15.16 |  |  |  |  |  | false |
| QX1.81.15.17 |  |  |  |  |  | false |
| QW1.81.15.18 |  |  |  |  |  |  |
| IX1.81.15.19 |  |  |  |  |  | false |
| IX1.81.15.20 |  |  |  |  |  | false |
| IX1.81.15.21 |  |  |  |  |  | false |
| IX1.81.15.22 |  |  |  |  |  | false |
| IX1.81.15.23 |  |  |  |  |  | false |
| IX1.81.15.24 |  |  |  |  |  | false |
| IX1.81.15.25 |  |  |  |  |  | false |
| IX1.81.15.26 |  |  |  |  |  | false |

Table 160. Controller\_3.1.81.15 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.81.15.1 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM01A.GCS\_1B.GCS\_1\_IO.FB0 |
| IX1.81.15.2 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM04D.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.15.3 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM02A\_B.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.15.4 |  |  |  | Ellato\_3.KSVG06786\_FB0 |
| IX1.81.15.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1J.GCS\_1\_IO.FB0 |
| IX1.81.15.6 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1K.GCS\_1\_IO.FB0 |
| IX1.81.15.7 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.81.15.8 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1H.GCS\_1\_IO.FB0 |
| IW1.81.15.9 |  |  |  |  |
| QX1.81.15.10 |  |  |  |  |
| QX1.81.15.11 |  |  |  |  |
| QX1.81.15.12 |  |  |  |  |
| QX1.81.15.13 |  |  |  |  |
| QX1.81.15.14 |  |  |  |  |
| QX1.81.15.15 |  |  |  |  |
| QX1.81.15.16 |  |  |  |  |
| QX1.81.15.17 |  |  |  |  |
| QW1.81.15.18 |  |  |  |  |
| IX1.81.15.19 |  |  |  |  |
| IX1.81.15.20 |  |  |  |  |
| IX1.81.15.21 |  |  |  |  |
| IX1.81.15.22 |  |  |  |  |
| IX1.81.15.23 |  |  |  |  |
| IX1.81.15.24 |  |  |  |  |
| IX1.81.15.25 |  |  |  |  |
| IX1.81.15.26 |  |  |  |  |

Table 161. Controller\_3.1.81.15 DX910\* (DIO8 S) (Status)

##### 16 AI930\* (AI4H A 1H)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: HART status / range | off / 4..20mA | enum |  |  |  |
| ch. 2: HART status / range | off / 4..20mA | enum |  |  |  |
| ch. 3: HART status / range | off / 4..20mA | enum |  |  |  |
| ch. 4: HART status / range | off / 4..20mA | enum |  |  |  |
| ch. 1: filter (PT1) | off | enum |  |  |  |
| ch. 2: filter (PT1) | off | enum |  |  |  |
| ch. 3: filter (PT1) | off | enum |  |  |  |
| ch. 4: filter (PT1) | off | enum |  |  |  |
| HART variable | secondary 1 | enum |  |  |  |
| HART var. of channel | ch. 1 | enum |  |  |  |

Table 162. Controller\_3 (172.16.96.153).1.81.16 AI930\* (AI4H A 1H) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.81.16.1 | Input 1 | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_2\_A.IND\_2\_IO | LIT01023 |
| IW1.81.16.2 | Input 2 | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_2\_B.IND\_2\_IO | LIT01024 |
| IW1.81.16.3 | Input 3 | RealIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.IND\_2A.IND\_2\_IO | LIT04623 |
| IW1.81.16.4 | Input 4 | RealIO | Ellato\_3.U\_04C91\_00.IND\_1ax3B.IND\_1\_IO | TT09137 |
| IW1.81.16.5 | HART 1 | RealIO |  |  |
| IW1.81.16.6 | UnitStatus | HwStatus |  |  |

Table 163. Controller\_3.1.81.16 AI930\* (AI4H A 1H) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.81.16.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_2\_A.IND\_2\_IO | 0.0 | 100.0 | l | 2 | false |
| IW1.81.16.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_2\_B.IND\_2\_IO | 0.0 | 100.0 | l | 2 | false |
| IW1.81.16.3 | Cikloprpil\_D6\_laktol.U\_14C46\_00.IND\_2A.IND\_2\_IO | 0.0 | 100.0 | l | 2 | false |
| IW1.81.16.4 | Ellato\_3.U\_04C91\_00.IND\_1ax3B.IND\_1\_IO | -20.0 | 350.0 | °C | 2 | false |
| IW1.81.16.5 |  | 0.0 | 100.0 |  | 1 | false |

Table 164. Controller\_3.1.81.16 AI930\* (AI4H A 1H) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.81.16.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_2\_A.IND\_2\_IO |
| IW1.81.16.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_2\_B.IND\_2\_IO |
| IW1.81.16.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.IND\_2A.IND\_2\_IO |
| IW1.81.16.4 |  |  |  | Ellato\_3.U\_04C91\_00.IND\_1ax3B.IND\_1\_IO |
| IW1.81.16.5 |  |  |  |  |

Table 165. Controller\_3.1.81.16 AI930\* (AI4H A 1H) (Status)

#### 82 S900

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.82.0 | UnitStatus | HwStatus |  |  |

Table 166. Controller\_3.1.82 S900 (Connections)

##### 0 CI920\* (CIPB)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Grid Frequency | 50 Hz | enum |  |  |  |
| Analog Status | on | enum |  |  |  |
| Internal Bus Redundancy | false | bool |  |  |  |
| CIPB Redundancy | false | bool |  |  |  |
| Power Supply Redundancy | false | bool |  |  |  |

Table 167. Controller\_3 (172.16.96.153).1.82.0 CI920\* (CIPB) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.82.0.1 | UnitStatus | HwStatus |  |  |

Table 168. Controller\_3.1.82.0 CI920\* (CIPB) (Connections)

##### 1 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 169. Controller\_3 (172.16.96.153).1.82.1 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.82.1.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM12A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04636 |
| QX1.82.1.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04641 |
| QX1.82.1.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM10A\_1.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04651 |
| QX1.82.1.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM10A\_2.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04652 |
| QW1.82.1.5 | All outputs | DwordIO |  |  |
| IW1.82.1.6 | UnitStatus | HwStatus |  |  |

Table 170. Controller\_3.1.82.1 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.82.1.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM12A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.1.2 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.1.3 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM10A\_1.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.1.4 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM10A\_2.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.82.1.5 |  |  |  |  |  |  |

Table 171. Controller\_3.1.82.1 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.82.1.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM12A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.82.1.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.82.1.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM10A\_1.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.82.1.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM10A\_2.GCS\_1A.GCS\_1\_IO.Cmd |
| QW1.82.1.5 |  |  |  |  |

Table 172. Controller\_3.1.82.1 DO910\* (DO4) (Status)

##### 2 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 173. Controller\_3 (172.16.96.153).1.82.2 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.82.2.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd | KSVG04662 |
| QX1.82.2.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.GCS\_1B.GCS\_1\_IO.Cmd | KSVG04663 |
| QX1.82.2.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM04A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04664 |
| QX1.82.2.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1B.GCS\_1\_IO.Cmd | KSVG04666 |
| QW1.82.2.5 | All outputs | DwordIO |  |  |
| IW1.82.2.6 | UnitStatus | HwStatus |  |  |

Table 174. Controller\_3.1.82.2 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.82.2.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.2.2 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.2.3 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM04A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.2.4 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.82.2.5 |  |  |  |  |  |  |

Table 175. Controller\_3.1.82.2 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.82.2.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |
| QX1.82.2.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.GCS\_1B.GCS\_1\_IO.Cmd |
| QX1.82.2.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM04A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.82.2.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1B.GCS\_1\_IO.Cmd |
| QW1.82.2.5 |  |  |  |  |

Table 176. Controller\_3.1.82.2 DO910\* (DO4) (Status)

##### 3 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 177. Controller\_3 (172.16.96.153).1.82.3 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.82.3.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM03A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04668 |
| QX1.82.3.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM03B.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04669 |
| QX1.82.3.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1C.GCS\_1\_IO.Cmd | KSVG04671 |
| QX1.82.3.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04673 |
| QW1.82.3.5 | All outputs | DwordIO |  |  |
| IW1.82.3.6 | UnitStatus | HwStatus |  |  |

Table 178. Controller\_3.1.82.3 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.82.3.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM03A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.3.2 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM03B.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.3.3 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.3.4 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.82.3.5 |  |  |  |  |  |  |

Table 179. Controller\_3.1.82.3 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.82.3.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM03A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.82.3.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM03B.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.82.3.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1C.GCS\_1\_IO.Cmd |
| QX1.82.3.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02A.GCS\_1A.GCS\_1\_IO.Cmd |
| QW1.82.3.5 |  |  |  |  |

Table 180. Controller\_3.1.82.3 DO910\* (DO4) (Status)

##### 4 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 181. Controller\_3 (172.16.96.153).1.82.4 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.82.4.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04681 |
| QX1.82.4.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM27.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd | KSVG04675 |
| QX1.82.4.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1B.GCS\_1\_IO.Cmd | KSVG04683 |
| QX1.82.4.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1C.GCS\_1\_IO.Cmd | KSVG04674 |
| QW1.82.4.5 | All outputs | DwordIO |  |  |
| IW1.82.4.6 | UnitStatus | HwStatus |  |  |

Table 182. Controller\_3.1.82.4 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.82.4.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.4.2 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM27.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.4.3 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.4.4 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.82.4.5 |  |  |  |  |  |  |

Table 183. Controller\_3.1.82.4 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.82.4.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.82.4.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM27.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd |
| QX1.82.4.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1B.GCS\_1\_IO.Cmd |
| QX1.82.4.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1C.GCS\_1\_IO.Cmd |
| QW1.82.4.5 |  |  |  |  |

Table 184. Controller\_3.1.82.4 DO910\* (DO4) (Status)

##### 5 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 185. Controller\_3 (172.16.96.153).1.82.5 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.82.5.1 | Output 1 | BoolIO | Ellato\_3.KSVG06889\_Cmd | KSVG06889 |
| QX1.82.5.2 | Output 2 | BoolIO |  |  |
| QX1.82.5.3 | Output 3 | BoolIO |  |  |
| QX1.82.5.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1D.GCS\_1\_IO.Cmd | KSVG04691 |
| QW1.82.5.5 | All outputs | DwordIO |  |  |
| IW1.82.5.6 | UnitStatus | HwStatus |  |  |

Table 186. Controller\_3.1.82.5 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.82.5.1 | Ellato\_3.KSVG06889\_Cmd |  |  |  |  | false |
| QX1.82.5.2 |  |  |  |  |  | false |
| QX1.82.5.3 |  |  |  |  |  | false |
| QX1.82.5.4 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1D.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.82.5.5 |  |  |  |  |  |  |

Table 187. Controller\_3.1.82.5 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.82.5.1 |  |  |  | Ellato\_3.KSVG06889\_Cmd |
| QX1.82.5.2 |  |  |  |  |
| QX1.82.5.3 |  |  |  |  |
| QX1.82.5.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1D.GCS\_1\_IO.Cmd |
| QW1.82.5.5 |  |  |  |  |

Table 188. Controller\_3.1.82.5 DO910\* (DO4) (Status)

##### 6 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 189. Controller\_3 (172.16.96.153).1.82.6 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.82.6.1 | Output 1 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM12Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG02436 |
| QX1.82.6.2 | Output 2 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM02Bax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG04241 |
| QX1.82.6.3 | Output 3 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd | KSVG04262 |
| QX1.82.6.4 | Output 4 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM01Aax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG04263 |
| QW1.82.6.5 | All outputs | DwordIO |  |  |
| IW1.82.6.6 | UnitStatus | HwStatus |  |  |

Table 190. Controller\_3.1.82.6 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.82.6.1 | Ellato\_3.U\_14C42\_Tart.EqM12Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.6.2 | Ellato\_3.U\_14C42\_Tart.EqM02Bax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.6.3 | Ellato\_3.U\_14C42\_Tart.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.6.4 | Ellato\_3.U\_14C42\_Tart.EqM01Aax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.82.6.5 |  |  |  |  |  |  |

Table 191. Controller\_3.1.82.6 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.82.6.1 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM12Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.82.6.2 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM02Bax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX1.82.6.3 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |
| QX1.82.6.4 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM01Aax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QW1.82.6.5 |  |  |  |  |

Table 192. Controller\_3.1.82.6 DO910\* (DO4) (Status)

##### 7 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 193. Controller\_3 (172.16.96.153).1.82.7 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.82.7.1 | Output 1 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04264 |
| QX1.82.7.2 | Output 2 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM03Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04268 |
| QX1.82.7.3 | Output 3 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM03Bax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04269 |
| QX1.82.7.4 | Output 4 | BoolIO | Ellato\_3.KSVG35170\_Cmd | KSVG35170 |
| QW1.82.7.5 | All outputs | DwordIO |  |  |
| IW1.82.7.6 | UnitStatus | HwStatus |  |  |

Table 194. Controller\_3.1.82.7 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.82.7.1 | Ellato\_3.U\_14C42\_Tart.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.7.2 | Ellato\_3.U\_14C42\_Tart.EqM03Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.7.3 | Ellato\_3.U\_14C42\_Tart.EqM03Bax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.7.4 | Ellato\_3.KSVG35170\_Cmd |  |  |  |  | false |
| QW1.82.7.5 |  |  |  |  |  |  |

Table 195. Controller\_3.1.82.7 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.82.7.1 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.82.7.2 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM03Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.82.7.3 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM03Bax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.82.7.4 |  |  |  | Ellato\_3.KSVG35170\_Cmd |
| QW1.82.7.5 |  |  |  |  |

Table 196. Controller\_3.1.82.7 DO910\* (DO4) (Status)

##### 8 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 197. Controller\_3 (172.16.96.153).1.82.8 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.82.8.1 | Output 1 | BoolIO | Ellato\_3.KSVG06788\_Cmd | KSVG06788 |
| QX1.82.8.2 | Output 2 | BoolIO | Ellato\_3.KSVG06891\_Cmd | KSVG06891 |
| QX1.82.8.3 | Output 3 | BoolIO |  |  |
| QX1.82.8.4 | Output 4 | BoolIO |  |  |
| QW1.82.8.5 | All outputs | DwordIO |  |  |
| IW1.82.8.6 | UnitStatus | HwStatus |  |  |

Table 198. Controller\_3.1.82.8 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.82.8.1 | Ellato\_3.KSVG06788\_Cmd |  |  |  |  | false |
| QX1.82.8.2 | Ellato\_3.KSVG06891\_Cmd |  |  |  |  | false |
| QX1.82.8.3 |  |  |  |  |  | false |
| QX1.82.8.4 |  |  |  |  |  | false |
| QW1.82.8.5 |  |  |  |  |  |  |

Table 199. Controller\_3.1.82.8 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.82.8.1 |  |  |  | Ellato\_3.KSVG06788\_Cmd |
| QX1.82.8.2 |  |  |  | Ellato\_3.KSVG06891\_Cmd |
| QX1.82.8.3 |  |  |  |  |
| QX1.82.8.4 |  |  |  |  |
| QW1.82.8.5 |  |  |  |  |

Table 200. Controller\_3.1.82.8 DO910\* (DO4) (Status)

##### 9 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 201. Controller\_3 (172.16.96.153).1.82.9 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.82.9.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Eax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG02484 |
| QX1.82.9.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Eax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG02494 |
| QX1.82.9.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1E.GCS\_1\_IO.Cmd | KSVG04692 |
| QX1.82.9.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1F.GCS\_1\_IO.Cmd | KSVG04693 |
| QW1.82.9.5 | All outputs | DwordIO |  |  |
| IW1.82.9.6 | UnitStatus | HwStatus |  |  |

Table 202. Controller\_3.1.82.9 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.82.9.1 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Eax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.9.2 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Eax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.9.3 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1E.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.9.4 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1F.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.82.9.5 |  |  |  |  |  |  |

Table 203. Controller\_3.1.82.9 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.82.9.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Eax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.82.9.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Eax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX1.82.9.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1E.GCS\_1\_IO.Cmd |
| QX1.82.9.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1F.GCS\_1\_IO.Cmd |
| QW1.82.9.5 |  |  |  |  |

Table 204. Controller\_3.1.82.9 DO910\* (DO4) (Status)

##### 10 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 205. Controller\_3 (172.16.96.153).1.82.10 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.82.10.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1G.GCS\_1\_IO.Cmd | KSVG04694 |
| QX1.82.10.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1B.GCS\_1\_IO.Cmd | KSVG04689 |
| QX1.82.10.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1I.GCS\_1\_IO.Cmd | KSVG04696 |
| QX1.82.10.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1C.GCS\_1\_IO.Cmd | KSVG04690 |
| QW1.82.10.5 | All outputs | DwordIO |  |  |
| IW1.82.10.6 | UnitStatus | HwStatus |  |  |

Table 206. Controller\_3.1.82.10 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.82.10.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1G.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.10.2 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.10.3 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1I.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.82.10.4 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.82.10.5 |  |  |  |  |  |  |

Table 207. Controller\_3.1.82.10 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.82.10.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1G.GCS\_1\_IO.Cmd |
| QX1.82.10.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1B.GCS\_1\_IO.Cmd |
| QX1.82.10.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1I.GCS\_1\_IO.Cmd |
| QX1.82.10.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1C.GCS\_1\_IO.Cmd |
| QW1.82.10.5 |  |  |  |  |

Table 208. Controller\_3.1.82.10 DO910\* (DO4) (Status)

##### 11 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 209. Controller\_3 (172.16.96.153).1.82.11 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.82.11.1 | Input 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM12A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04636 |
| IX1.82.11.2 | Input 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04641 |
| IX1.82.11.3 | Input 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM10A\_1.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04651 |
| IX1.82.11.4 | Input 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM10A\_2.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04652 |
| IX1.82.11.5 | Input 5 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 | KSVG04662 |
| IX1.82.11.6 | Input 6 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.GCS\_1B.GCS\_1\_IO.FB0 | KSVG04663 |
| IX1.82.11.7 | Input 7 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM04A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04664 |
| IX1.82.11.8 | Input 8 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1B.GCS\_1\_IO.FB0 | KSVG04666 |
| IW1.82.11.9 | All inputs | DwordIO |  |  |
| QX1.82.11.10 | Output 1 | BoolIO |  |  |
| QX1.82.11.11 | Output 2 | BoolIO |  |  |
| QX1.82.11.12 | Output 3 | BoolIO |  |  |
| QX1.82.11.13 | Output 4 | BoolIO |  |  |
| QX1.82.11.14 | Output 5 | BoolIO |  |  |
| QX1.82.11.15 | Output 6 | BoolIO |  |  |
| QX1.82.11.16 | Output 7 | BoolIO |  |  |
| QX1.82.11.17 | Output 8 | BoolIO |  |  |
| QW1.82.11.18 | All outputs | DwordIO |  |  |
| IX1.82.11.19 | Status Input 1 | BoolIO |  |  |
| IX1.82.11.20 | Status Input 2 | BoolIO |  |  |
| IX1.82.11.21 | Status Input 3 | BoolIO |  |  |
| IX1.82.11.22 | Status Input 4 | BoolIO |  |  |
| IX1.82.11.23 | Status Input 5 | BoolIO |  |  |
| IX1.82.11.24 | Status Input 6 | BoolIO |  |  |
| IX1.82.11.25 | Status Input 7 | BoolIO |  |  |
| IX1.82.11.26 | Status Input 8 | BoolIO |  |  |
| IW1.82.11.27 | UnitStatus | HwStatus |  |  |

Table 210. Controller\_3.1.82.11 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.82.11.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM12A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.11.2 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.11.3 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM10A\_1.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.11.4 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM10A\_2.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.11.5 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.11.6 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.11.7 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM04A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.11.8 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW1.82.11.9 |  |  |  |  |  |  |
| QX1.82.11.10 |  |  |  |  |  | false |
| QX1.82.11.11 |  |  |  |  |  | false |
| QX1.82.11.12 |  |  |  |  |  | false |
| QX1.82.11.13 |  |  |  |  |  | false |
| QX1.82.11.14 |  |  |  |  |  | false |
| QX1.82.11.15 |  |  |  |  |  | false |
| QX1.82.11.16 |  |  |  |  |  | false |
| QX1.82.11.17 |  |  |  |  |  | false |
| QW1.82.11.18 |  |  |  |  |  |  |
| IX1.82.11.19 |  |  |  |  |  | false |
| IX1.82.11.20 |  |  |  |  |  | false |
| IX1.82.11.21 |  |  |  |  |  | false |
| IX1.82.11.22 |  |  |  |  |  | false |
| IX1.82.11.23 |  |  |  |  |  | false |
| IX1.82.11.24 |  |  |  |  |  | false |
| IX1.82.11.25 |  |  |  |  |  | false |
| IX1.82.11.26 |  |  |  |  |  | false |

Table 211. Controller\_3.1.82.11 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.82.11.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM12A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.82.11.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.82.11.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM10A\_1.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.82.11.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM10A\_2.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.82.11.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |
| IX1.82.11.6 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM01A.GCS\_1B.GCS\_1\_IO.FB0 |
| IX1.82.11.7 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM04A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.82.11.8 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1B.GCS\_1\_IO.FB0 |
| IW1.82.11.9 |  |  |  |  |
| QX1.82.11.10 |  |  |  |  |
| QX1.82.11.11 |  |  |  |  |
| QX1.82.11.12 |  |  |  |  |
| QX1.82.11.13 |  |  |  |  |
| QX1.82.11.14 |  |  |  |  |
| QX1.82.11.15 |  |  |  |  |
| QX1.82.11.16 |  |  |  |  |
| QX1.82.11.17 |  |  |  |  |
| QW1.82.11.18 |  |  |  |  |
| IX1.82.11.19 |  |  |  |  |
| IX1.82.11.20 |  |  |  |  |
| IX1.82.11.21 |  |  |  |  |
| IX1.82.11.22 |  |  |  |  |
| IX1.82.11.23 |  |  |  |  |
| IX1.82.11.24 |  |  |  |  |
| IX1.82.11.25 |  |  |  |  |
| IX1.82.11.26 |  |  |  |  |

Table 212. Controller\_3.1.82.11 DX910\* (DIO8 S) (Status)

##### 12 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 213. Controller\_3 (172.16.96.153).1.82.12 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.82.12.1 | Input 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM03A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04668 |
| IX1.82.12.2 | Input 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM03B.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04669 |
| IX1.82.12.3 | Input 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1C.GCS\_1\_IO.FB0 | KSVG04671 |
| IX1.82.12.4 | Input 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04673 |
| IX1.82.12.5 | Input 5 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04674 |
| IX1.82.12.6 | Input 6 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM27.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 | KSVG04675 |
| IX1.82.12.7 | Input 7 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1B.GCS\_1\_IO.FB0 | KSVG04681 |
| IX1.82.12.8 | Input 8 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1C.GCS\_1\_IO.FB0 | KSVG04683 |
| IW1.82.12.9 | All inputs | DwordIO |  |  |
| QX1.82.12.10 | Output 1 | BoolIO |  |  |
| QX1.82.12.11 | Output 2 | BoolIO |  |  |
| QX1.82.12.12 | Output 3 | BoolIO |  |  |
| QX1.82.12.13 | Output 4 | BoolIO |  |  |
| QX1.82.12.14 | Output 5 | BoolIO |  |  |
| QX1.82.12.15 | Output 6 | BoolIO |  |  |
| QX1.82.12.16 | Output 7 | BoolIO |  |  |
| QX1.82.12.17 | Output 8 | BoolIO |  |  |
| QW1.82.12.18 | All outputs | DwordIO |  |  |
| IX1.82.12.19 | Status Input 1 | BoolIO |  |  |
| IX1.82.12.20 | Status Input 2 | BoolIO |  |  |
| IX1.82.12.21 | Status Input 3 | BoolIO |  |  |
| IX1.82.12.22 | Status Input 4 | BoolIO |  |  |
| IX1.82.12.23 | Status Input 5 | BoolIO |  |  |
| IX1.82.12.24 | Status Input 6 | BoolIO |  |  |
| IX1.82.12.25 | Status Input 7 | BoolIO |  |  |
| IX1.82.12.26 | Status Input 8 | BoolIO |  |  |
| IW1.82.12.27 | UnitStatus | HwStatus |  |  |

Table 214. Controller\_3.1.82.12 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.82.12.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM03A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.12.2 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM03B.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.12.3 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.12.4 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.12.5 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.12.6 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM27.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.12.7 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.12.8 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW1.82.12.9 |  |  |  |  |  |  |
| QX1.82.12.10 |  |  |  |  |  | false |
| QX1.82.12.11 |  |  |  |  |  | false |
| QX1.82.12.12 |  |  |  |  |  | false |
| QX1.82.12.13 |  |  |  |  |  | false |
| QX1.82.12.14 |  |  |  |  |  | false |
| QX1.82.12.15 |  |  |  |  |  | false |
| QX1.82.12.16 |  |  |  |  |  | false |
| QX1.82.12.17 |  |  |  |  |  | false |
| QW1.82.12.18 |  |  |  |  |  |  |
| IX1.82.12.19 |  |  |  |  |  | false |
| IX1.82.12.20 |  |  |  |  |  | false |
| IX1.82.12.21 |  |  |  |  |  | false |
| IX1.82.12.22 |  |  |  |  |  | false |
| IX1.82.12.23 |  |  |  |  |  | false |
| IX1.82.12.24 |  |  |  |  |  | false |
| IX1.82.12.25 |  |  |  |  |  | false |
| IX1.82.12.26 |  |  |  |  |  | false |

Table 215. Controller\_3.1.82.12 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.82.12.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM03A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.82.12.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM03B.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.82.12.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1C.GCS\_1\_IO.FB0 |
| IX1.82.12.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.82.12.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.82.12.6 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM27.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 |
| IX1.82.12.7 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1B.GCS\_1\_IO.FB0 |
| IX1.82.12.8 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM22B.GCS\_1C.GCS\_1\_IO.FB0 |
| IW1.82.12.9 |  |  |  |  |
| QX1.82.12.10 |  |  |  |  |
| QX1.82.12.11 |  |  |  |  |
| QX1.82.12.12 |  |  |  |  |
| QX1.82.12.13 |  |  |  |  |
| QX1.82.12.14 |  |  |  |  |
| QX1.82.12.15 |  |  |  |  |
| QX1.82.12.16 |  |  |  |  |
| QX1.82.12.17 |  |  |  |  |
| QW1.82.12.18 |  |  |  |  |
| IX1.82.12.19 |  |  |  |  |
| IX1.82.12.20 |  |  |  |  |
| IX1.82.12.21 |  |  |  |  |
| IX1.82.12.22 |  |  |  |  |
| IX1.82.12.23 |  |  |  |  |
| IX1.82.12.24 |  |  |  |  |
| IX1.82.12.25 |  |  |  |  |
| IX1.82.12.26 |  |  |  |  |

Table 216. Controller\_3.1.82.12 DX910\* (DIO8 S) (Status)

##### 13 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | true | bool |  |  |  |
| ch. 3: deactivate | true | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 217. Controller\_3 (172.16.96.153).1.82.13 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.82.13.1 | Input 1 | BoolIO | Ellato\_3.KSVG06889\_FB0 | KSVG06889 |
| IX1.82.13.2 | Input 2 | BoolIO |  |  |
| IX1.82.13.3 | Input 3 | BoolIO |  |  |
| IX1.82.13.4 | Input 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1D.GCS\_1\_IO.FB0 | KSVG04691 |
| IX1.82.13.5 | Input 5 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM12Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG02436 |
| IX1.82.13.6 | Input 6 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM02Bax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG04241 |
| IX1.82.13.7 | Input 7 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 | KSVG04262 |
| IX1.82.13.8 | Input 8 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM01Aax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG04263 |
| IW1.82.13.9 | All inputs | DwordIO |  |  |
| QX1.82.13.10 | Output 1 | BoolIO |  |  |
| QX1.82.13.11 | Output 2 | BoolIO |  |  |
| QX1.82.13.12 | Output 3 | BoolIO |  |  |
| QX1.82.13.13 | Output 4 | BoolIO |  |  |
| QX1.82.13.14 | Output 5 | BoolIO |  |  |
| QX1.82.13.15 | Output 6 | BoolIO |  |  |
| QX1.82.13.16 | Output 7 | BoolIO |  |  |
| QX1.82.13.17 | Output 8 | BoolIO |  |  |
| QW1.82.13.18 | All outputs | DwordIO |  |  |
| IX1.82.13.19 | Status Input 1 | BoolIO |  |  |
| IX1.82.13.20 | Status Input 2 | BoolIO |  |  |
| IX1.82.13.21 | Status Input 3 | BoolIO |  |  |
| IX1.82.13.22 | Status Input 4 | BoolIO |  |  |
| IX1.82.13.23 | Status Input 5 | BoolIO |  |  |
| IX1.82.13.24 | Status Input 6 | BoolIO |  |  |
| IX1.82.13.25 | Status Input 7 | BoolIO |  |  |
| IX1.82.13.26 | Status Input 8 | BoolIO |  |  |
| IW1.82.13.27 | UnitStatus | HwStatus |  |  |

Table 218. Controller\_3.1.82.13 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.82.13.1 | Ellato\_3.KSVG06889\_FB0 |  |  |  |  | false |
| IX1.82.13.2 |  |  |  |  |  | false |
| IX1.82.13.3 |  |  |  |  |  | false |
| IX1.82.13.4 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1D.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.13.5 | Ellato\_3.U\_14C42\_Tart.EqM12Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.13.6 | Ellato\_3.U\_14C42\_Tart.EqM02Bax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.13.7 | Ellato\_3.U\_14C42\_Tart.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.13.8 | Ellato\_3.U\_14C42\_Tart.EqM01Aax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW1.82.13.9 |  |  |  |  |  |  |
| QX1.82.13.10 |  |  |  |  |  | false |
| QX1.82.13.11 |  |  |  |  |  | false |
| QX1.82.13.12 |  |  |  |  |  | false |
| QX1.82.13.13 |  |  |  |  |  | false |
| QX1.82.13.14 |  |  |  |  |  | false |
| QX1.82.13.15 |  |  |  |  |  | false |
| QX1.82.13.16 |  |  |  |  |  | false |
| QX1.82.13.17 |  |  |  |  |  | false |
| QW1.82.13.18 |  |  |  |  |  |  |
| IX1.82.13.19 |  |  |  |  |  | false |
| IX1.82.13.20 |  |  |  |  |  | false |
| IX1.82.13.21 |  |  |  |  |  | false |
| IX1.82.13.22 |  |  |  |  |  | false |
| IX1.82.13.23 |  |  |  |  |  | false |
| IX1.82.13.24 |  |  |  |  |  | false |
| IX1.82.13.25 |  |  |  |  |  | false |
| IX1.82.13.26 |  |  |  |  |  | false |

Table 219. Controller\_3.1.82.13 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.82.13.1 |  |  |  | Ellato\_3.KSVG06889\_FB0 |
| IX1.82.13.2 |  |  |  |  |
| IX1.82.13.3 |  |  |  |  |
| IX1.82.13.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1D.GCS\_1\_IO.FB0 |
| IX1.82.13.5 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM12Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.82.13.6 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM02Bax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IX1.82.13.7 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |
| IX1.82.13.8 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM01Aax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IW1.82.13.9 |  |  |  |  |
| QX1.82.13.10 |  |  |  |  |
| QX1.82.13.11 |  |  |  |  |
| QX1.82.13.12 |  |  |  |  |
| QX1.82.13.13 |  |  |  |  |
| QX1.82.13.14 |  |  |  |  |
| QX1.82.13.15 |  |  |  |  |
| QX1.82.13.16 |  |  |  |  |
| QX1.82.13.17 |  |  |  |  |
| QW1.82.13.18 |  |  |  |  |
| IX1.82.13.19 |  |  |  |  |
| IX1.82.13.20 |  |  |  |  |
| IX1.82.13.21 |  |  |  |  |
| IX1.82.13.22 |  |  |  |  |
| IX1.82.13.23 |  |  |  |  |
| IX1.82.13.24 |  |  |  |  |
| IX1.82.13.25 |  |  |  |  |
| IX1.82.13.26 |  |  |  |  |

Table 220. Controller\_3.1.82.13 DX910\* (DIO8 S) (Status)

##### 14 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | true | bool |  |  |  |
| ch. 7: deactivate | true | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 221. Controller\_3 (172.16.96.153).1.82.14 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.82.14.1 | Input 1 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04264 |
| IX1.82.14.2 | Input 2 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM03Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04268 |
| IX1.82.14.3 | Input 3 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM03Bax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04269 |
| IX1.82.14.4 | Input 4 | BoolIO | Ellato\_3.KSVG06788\_FB0 | KSVG06788 |
| IX1.82.14.5 | Input 5 | BoolIO | Ellato\_3.KSVG06891\_FB0 | KSVG06891 |
| IX1.82.14.6 | Input 6 | BoolIO |  |  |
| IX1.82.14.7 | Input 7 | BoolIO |  |  |
| IX1.82.14.8 | Input 8 | BoolIO | Ellato\_3.KSVG35170\_FB0 | KSVG35170 |
| IW1.82.14.9 | All inputs | DwordIO |  |  |
| QX1.82.14.10 | Output 1 | BoolIO |  |  |
| QX1.82.14.11 | Output 2 | BoolIO |  |  |
| QX1.82.14.12 | Output 3 | BoolIO |  |  |
| QX1.82.14.13 | Output 4 | BoolIO |  |  |
| QX1.82.14.14 | Output 5 | BoolIO |  |  |
| QX1.82.14.15 | Output 6 | BoolIO |  |  |
| QX1.82.14.16 | Output 7 | BoolIO |  |  |
| QX1.82.14.17 | Output 8 | BoolIO |  |  |
| QW1.82.14.18 | All outputs | DwordIO |  |  |
| IX1.82.14.19 | Status Input 1 | BoolIO |  |  |
| IX1.82.14.20 | Status Input 2 | BoolIO |  |  |
| IX1.82.14.21 | Status Input 3 | BoolIO |  |  |
| IX1.82.14.22 | Status Input 4 | BoolIO |  |  |
| IX1.82.14.23 | Status Input 5 | BoolIO |  |  |
| IX1.82.14.24 | Status Input 6 | BoolIO |  |  |
| IX1.82.14.25 | Status Input 7 | BoolIO |  |  |
| IX1.82.14.26 | Status Input 8 | BoolIO |  |  |
| IW1.82.14.27 | UnitStatus | HwStatus |  |  |

Table 222. Controller\_3.1.82.14 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.82.14.1 | Ellato\_3.U\_14C42\_Tart.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.14.2 | Ellato\_3.U\_14C42\_Tart.EqM03Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.14.3 | Ellato\_3.U\_14C42\_Tart.EqM03Bax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.14.4 | Ellato\_3.KSVG06788\_FB0 |  |  |  |  | false |
| IX1.82.14.5 | Ellato\_3.KSVG06891\_FB0 |  |  |  |  | false |
| IX1.82.14.6 |  |  |  |  |  | false |
| IX1.82.14.7 |  |  |  |  |  | false |
| IX1.82.14.8 | Ellato\_3.KSVG35170\_FB0 |  |  |  |  | false |
| QW1.82.14.9 |  |  |  |  |  |  |
| QX1.82.14.10 |  |  |  |  |  | false |
| QX1.82.14.11 |  |  |  |  |  | false |
| QX1.82.14.12 |  |  |  |  |  | false |
| QX1.82.14.13 |  |  |  |  |  | false |
| QX1.82.14.14 |  |  |  |  |  | false |
| QX1.82.14.15 |  |  |  |  |  | false |
| QX1.82.14.16 |  |  |  |  |  | false |
| QX1.82.14.17 |  |  |  |  |  | false |
| QW1.82.14.18 |  |  |  |  |  |  |
| IX1.82.14.19 |  |  |  |  |  | false |
| IX1.82.14.20 |  |  |  |  |  | false |
| IX1.82.14.21 |  |  |  |  |  | false |
| IX1.82.14.22 |  |  |  |  |  | false |
| IX1.82.14.23 |  |  |  |  |  | false |
| IX1.82.14.24 |  |  |  |  |  | false |
| IX1.82.14.25 |  |  |  |  |  | false |
| IX1.82.14.26 |  |  |  |  |  | false |

Table 223. Controller\_3.1.82.14 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.82.14.1 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.82.14.2 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM03Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.82.14.3 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM03Bax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.82.14.4 |  |  |  | Ellato\_3.KSVG06788\_FB0 |
| IX1.82.14.5 |  |  |  | Ellato\_3.KSVG06891\_FB0 |
| IX1.82.14.6 |  |  |  |  |
| IX1.82.14.7 |  |  |  |  |
| IX1.82.14.8 |  |  |  | Ellato\_3.KSVG35170\_FB0 |
| IW1.82.14.9 |  |  |  |  |
| QX1.82.14.10 |  |  |  |  |
| QX1.82.14.11 |  |  |  |  |
| QX1.82.14.12 |  |  |  |  |
| QX1.82.14.13 |  |  |  |  |
| QX1.82.14.14 |  |  |  |  |
| QX1.82.14.15 |  |  |  |  |
| QX1.82.14.16 |  |  |  |  |
| QX1.82.14.17 |  |  |  |  |
| QW1.82.14.18 |  |  |  |  |
| IX1.82.14.19 |  |  |  |  |
| IX1.82.14.20 |  |  |  |  |
| IX1.82.14.21 |  |  |  |  |
| IX1.82.14.22 |  |  |  |  |
| IX1.82.14.23 |  |  |  |  |
| IX1.82.14.24 |  |  |  |  |
| IX1.82.14.25 |  |  |  |  |
| IX1.82.14.26 |  |  |  |  |

Table 224. Controller\_3.1.82.14 DX910\* (DIO8 S) (Status)

##### 15 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 225. Controller\_3 (172.16.96.153).1.82.15 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.82.15.1 | Input 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Eax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG02484 |
| IX1.82.15.2 | Input 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Eax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG02494 |
| IX1.82.15.3 | Input 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1E.GCS\_1\_IO.FB0 | KSVG04692 |
| IX1.82.15.4 | Input 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1F.GCS\_1\_IO.FB0 | KSVG04693 |
| IX1.82.15.5 | Input 5 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1G.GCS\_1\_IO.FB0 | KSVG04694 |
| IX1.82.15.6 | Input 6 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1B.GCS\_1\_IO.FB0 | KSVG04689 |
| IX1.82.15.7 | Input 7 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1I.GCS\_1\_IO.FB0 | KSVG04696 |
| IX1.82.15.8 | Input 8 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1C.GCS\_1\_IO.FB0 | KSVG04690 |
| IW1.82.15.9 | All inputs | DwordIO |  |  |
| QX1.82.15.10 | Output 1 | BoolIO |  |  |
| QX1.82.15.11 | Output 2 | BoolIO |  |  |
| QX1.82.15.12 | Output 3 | BoolIO |  |  |
| QX1.82.15.13 | Output 4 | BoolIO |  |  |
| QX1.82.15.14 | Output 5 | BoolIO |  |  |
| QX1.82.15.15 | Output 6 | BoolIO |  |  |
| QX1.82.15.16 | Output 7 | BoolIO |  |  |
| QX1.82.15.17 | Output 8 | BoolIO |  |  |
| QW1.82.15.18 | All outputs | DwordIO |  |  |
| IX1.82.15.19 | Status Input 1 | BoolIO |  |  |
| IX1.82.15.20 | Status Input 2 | BoolIO |  |  |
| IX1.82.15.21 | Status Input 3 | BoolIO |  |  |
| IX1.82.15.22 | Status Input 4 | BoolIO |  |  |
| IX1.82.15.23 | Status Input 5 | BoolIO |  |  |
| IX1.82.15.24 | Status Input 6 | BoolIO |  |  |
| IX1.82.15.25 | Status Input 7 | BoolIO |  |  |
| IX1.82.15.26 | Status Input 8 | BoolIO |  |  |
| IW1.82.15.27 | UnitStatus | HwStatus |  |  |

Table 226. Controller\_3.1.82.15 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.82.15.1 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Eax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.15.2 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Eax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.15.3 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1E.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.15.4 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1F.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.15.5 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1G.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.15.6 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.15.7 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1I.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.82.15.8 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW1.82.15.9 |  |  |  |  |  |  |
| QX1.82.15.10 |  |  |  |  |  | false |
| QX1.82.15.11 |  |  |  |  |  | false |
| QX1.82.15.12 |  |  |  |  |  | false |
| QX1.82.15.13 |  |  |  |  |  | false |
| QX1.82.15.14 |  |  |  |  |  | false |
| QX1.82.15.15 |  |  |  |  |  | false |
| QX1.82.15.16 |  |  |  |  |  | false |
| QX1.82.15.17 |  |  |  |  |  | false |
| QW1.82.15.18 |  |  |  |  |  |  |
| IX1.82.15.19 |  |  |  |  |  | false |
| IX1.82.15.20 |  |  |  |  |  | false |
| IX1.82.15.21 |  |  |  |  |  | false |
| IX1.82.15.22 |  |  |  |  |  | false |
| IX1.82.15.23 |  |  |  |  |  | false |
| IX1.82.15.24 |  |  |  |  |  | false |
| IX1.82.15.25 |  |  |  |  |  | false |
| IX1.82.15.26 |  |  |  |  |  | false |

Table 227. Controller\_3.1.82.15 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.82.15.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Eax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.82.15.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Eax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IX1.82.15.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1E.GCS\_1\_IO.FB0 |
| IX1.82.15.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1F.GCS\_1\_IO.FB0 |
| IX1.82.15.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1G.GCS\_1\_IO.FB0 |
| IX1.82.15.6 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1B.GCS\_1\_IO.FB0 |
| IX1.82.15.7 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1I.GCS\_1\_IO.FB0 |
| IX1.82.15.8 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.GCS\_1C.GCS\_1\_IO.FB0 |
| IW1.82.15.9 |  |  |  |  |
| QX1.82.15.10 |  |  |  |  |
| QX1.82.15.11 |  |  |  |  |
| QX1.82.15.12 |  |  |  |  |
| QX1.82.15.13 |  |  |  |  |
| QX1.82.15.14 |  |  |  |  |
| QX1.82.15.15 |  |  |  |  |
| QX1.82.15.16 |  |  |  |  |
| QX1.82.15.17 |  |  |  |  |
| QW1.82.15.18 |  |  |  |  |
| IX1.82.15.19 |  |  |  |  |
| IX1.82.15.20 |  |  |  |  |
| IX1.82.15.21 |  |  |  |  |
| IX1.82.15.22 |  |  |  |  |
| IX1.82.15.23 |  |  |  |  |
| IX1.82.15.24 |  |  |  |  |
| IX1.82.15.25 |  |  |  |  |
| IX1.82.15.26 |  |  |  |  |

Table 228. Controller\_3.1.82.15 DX910\* (DIO8 S) (Status)

##### 16 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | true | bool |  |  |  |
| ch. 4: deactivate | true | bool |  |  |  |
| ch. 5: deactivate | true | bool |  |  |  |
| ch. 6: deactivate | true | bool |  |  |  |
| ch. 7: deactivate | true | bool |  |  |  |
| ch. 8: deactivate | true | bool |  |  |  |

Table 229. Controller\_3 (172.16.96.153).1.82.16 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.82.16.1 | Input 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM15C.FS\_IO | FS04633 |
| IX1.82.16.2 | Input 2 | BoolIO | Ellato\_3.U\_PL\_C3.EqMPLA.PL\_1.PL\_1\_IO | PL 14C31 |
| IX1.82.16.3 | Input 3 | BoolIO |  |  |
| IX1.82.16.4 | Input 4 | BoolIO |  |  |
| IX1.82.16.5 | Input 5 | BoolIO |  |  |
| IX1.82.16.6 | Input 6 | BoolIO |  |  |
| IX1.82.16.7 | Input 7 | BoolIO |  |  |
| IX1.82.16.8 | Input 8 | BoolIO |  |  |
| IW1.82.16.9 | All inputs | DwordIO |  |  |
| QX1.82.16.10 | Output 1 | BoolIO |  |  |
| QX1.82.16.11 | Output 2 | BoolIO |  |  |
| QX1.82.16.12 | Output 3 | BoolIO |  |  |
| QX1.82.16.13 | Output 4 | BoolIO |  |  |
| QX1.82.16.14 | Output 5 | BoolIO |  |  |
| QX1.82.16.15 | Output 6 | BoolIO |  |  |
| QX1.82.16.16 | Output 7 | BoolIO |  |  |
| QX1.82.16.17 | Output 8 | BoolIO |  |  |
| QW1.82.16.18 | All outputs | DwordIO |  |  |
| IX1.82.16.19 | Status Input 1 | BoolIO |  |  |
| IX1.82.16.20 | Status Input 2 | BoolIO |  |  |
| IX1.82.16.21 | Status Input 3 | BoolIO |  |  |
| IX1.82.16.22 | Status Input 4 | BoolIO |  |  |
| IX1.82.16.23 | Status Input 5 | BoolIO |  |  |
| IX1.82.16.24 | Status Input 6 | BoolIO |  |  |
| IX1.82.16.25 | Status Input 7 | BoolIO |  |  |
| IX1.82.16.26 | Status Input 8 | BoolIO |  |  |
| IW1.82.16.27 | UnitStatus | HwStatus |  |  |

Table 230. Controller\_3.1.82.16 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.82.16.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM15C.FS\_IO |  |  |  |  | false |
| IX1.82.16.2 | Ellato\_3.U\_PL\_C3.EqMPLA.PL\_1.PL\_1\_IO |  |  |  |  | false |
| IX1.82.16.3 |  |  |  |  |  | false |
| IX1.82.16.4 |  |  |  |  |  | false |
| IX1.82.16.5 |  |  |  |  |  | false |
| IX1.82.16.6 |  |  |  |  |  | false |
| IX1.82.16.7 |  |  |  |  |  | false |
| IX1.82.16.8 |  |  |  |  |  | false |
| QW1.82.16.9 |  |  |  |  |  |  |
| QX1.82.16.10 |  |  |  |  |  | false |
| QX1.82.16.11 |  |  |  |  |  | false |
| QX1.82.16.12 |  |  |  |  |  | false |
| QX1.82.16.13 |  |  |  |  |  | false |
| QX1.82.16.14 |  |  |  |  |  | false |
| QX1.82.16.15 |  |  |  |  |  | false |
| QX1.82.16.16 |  |  |  |  |  | false |
| QX1.82.16.17 |  |  |  |  |  | false |
| QW1.82.16.18 |  |  |  |  |  |  |
| IX1.82.16.19 |  |  |  |  |  | false |
| IX1.82.16.20 |  |  |  |  |  | false |
| IX1.82.16.21 |  |  |  |  |  | false |
| IX1.82.16.22 |  |  |  |  |  | false |
| IX1.82.16.23 |  |  |  |  |  | false |
| IX1.82.16.24 |  |  |  |  |  | false |
| IX1.82.16.25 |  |  |  |  |  | false |
| IX1.82.16.26 |  |  |  |  |  | false |

Table 231. Controller\_3.1.82.16 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.82.16.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM15C.FS\_IO |
| IX1.82.16.2 |  |  |  | Ellato\_3.U\_PL\_C3.EqMPLA.PL\_1.PL\_1\_IO |
| IX1.82.16.3 |  |  |  |  |
| IX1.82.16.4 |  |  |  |  |
| IX1.82.16.5 |  |  |  |  |
| IX1.82.16.6 |  |  |  |  |
| IX1.82.16.7 |  |  |  |  |
| IX1.82.16.8 |  |  |  |  |
| IW1.82.16.9 |  |  |  |  |
| QX1.82.16.10 |  |  |  |  |
| QX1.82.16.11 |  |  |  |  |
| QX1.82.16.12 |  |  |  |  |
| QX1.82.16.13 |  |  |  |  |
| QX1.82.16.14 |  |  |  |  |
| QX1.82.16.15 |  |  |  |  |
| QX1.82.16.16 |  |  |  |  |
| QX1.82.16.17 |  |  |  |  |
| QW1.82.16.18 |  |  |  |  |
| IX1.82.16.19 |  |  |  |  |
| IX1.82.16.20 |  |  |  |  |
| IX1.82.16.21 |  |  |  |  |
| IX1.82.16.22 |  |  |  |  |
| IX1.82.16.23 |  |  |  |  |
| IX1.82.16.24 |  |  |  |  |
| IX1.82.16.25 |  |  |  |  |
| IX1.82.16.26 |  |  |  |  |

Table 232. Controller\_3.1.82.16 DX910\* (DIO8 S) (Status)

#### 83 S900

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.83.0 | UnitStatus | HwStatus |  |  |

Table 233. Controller\_3.1.83 S900 (Connections)

##### 0 CI920\* (CIPB)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Grid Frequency | 50 Hz | enum |  |  |  |
| Analog Status | on | enum |  |  |  |
| Internal Bus Redundancy | false | bool |  |  |  |
| CIPB Redundancy | false | bool |  |  |  |
| Power Supply Redundancy | false | bool |  |  |  |

Table 234. Controller\_3 (172.16.96.153).1.83.0 CI920\* (CIPB) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.83.0.1 | UnitStatus | HwStatus |  |  |

Table 235. Controller\_3.1.83.0 CI920\* (CIPB) (Connections)

##### 1 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 236. Controller\_3 (172.16.96.153).1.83.1 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.83.1.1 | Output 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG01068 |
| QX1.83.1.2 | Output 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM10Cax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG01051 |
| QX1.83.1.3 | Output 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM10Cax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG01052 |
| QX1.83.1.4 | Output 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG01064 |
| QW1.83.1.5 | All outputs | DwordIO |  |  |
| IW1.83.1.6 | UnitStatus | HwStatus |  |  |

Table 237. Controller\_3.1.83.1 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.83.1.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.1.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM10Cax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.1.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM10Cax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.1.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.83.1.5 |  |  |  |  |  |  |

Table 238. Controller\_3.1.83.1 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.83.1.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.83.1.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM10Cax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.83.1.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM10Cax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX1.83.1.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QW1.83.1.5 |  |  |  |  |

Table 239. Controller\_3.1.83.1 DO910\* (DO4) (Status)

##### 2 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 240. Controller\_3 (172.16.96.153).1.83.2 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.83.2.1 | Output 1 | BoolIO | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG09181 |
| QX1.83.2.2 | Output 2 | BoolIO | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1C.GCS\_1\_IO.Cmd | KSVG09182 |
| QX1.83.2.3 | Output 3 | BoolIO | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG09171 |
| QX1.83.2.4 | Output 4 | BoolIO | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1D.GCS\_1\_IO.Cmd | KSVG09191 |
| QW1.83.2.5 | All outputs | DwordIO |  |  |
| IW1.83.2.6 | UnitStatus | HwStatus |  |  |

Table 241. Controller\_3.1.83.2 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.83.2.1 | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.2.2 | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.2.3 | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.2.4 | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1D.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.83.2.5 |  |  |  |  |  |  |

Table 242. Controller\_3.1.83.2 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.83.2.1 |  |  |  | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX1.83.2.2 |  |  |  | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1C.GCS\_1\_IO.Cmd |
| QX1.83.2.3 |  |  |  | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.83.2.4 |  |  |  | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1D.GCS\_1\_IO.Cmd |
| QW1.83.2.5 |  |  |  |  |

Table 243. Controller\_3.1.83.2 DO910\* (DO4) (Status)

##### 3 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 244. Controller\_3 (172.16.96.153).1.83.3 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.83.3.1 | Output 1 | BoolIO | Ellato\_3.U\_04C91\_00.GCS\_1B.GCS\_1\_IO.Cmd | KSVG09175 |
| QX1.83.3.2 | Output 2 | BoolIO | Ellato\_3.U\_04C91\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG09164 |
| QX1.83.3.3 | Output 3 | BoolIO | Ellato\_3.U\_04C91\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd | KSVG09162 |
| QX1.83.3.4 | Output 4 | BoolIO | Ellato\_3.KSVG06792\_Cmd | KSVG06792 |
| QW1.83.3.5 | All outputs | DwordIO |  |  |
| IW1.83.3.6 | UnitStatus | HwStatus |  |  |

Table 245. Controller\_3.1.83.3 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.83.3.1 | Ellato\_3.U\_04C91\_00.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.3.2 | Ellato\_3.U\_04C91\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.3.3 | Ellato\_3.U\_04C91\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.3.4 | Ellato\_3.KSVG06792\_Cmd |  |  |  |  | false |
| QW1.83.3.5 |  |  |  |  |  |  |

Table 246. Controller\_3.1.83.3 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.83.3.1 |  |  |  | Ellato\_3.U\_04C91\_00.GCS\_1B.GCS\_1\_IO.Cmd |
| QX1.83.3.2 |  |  |  | Ellato\_3.U\_04C91\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.83.3.3 |  |  |  | Ellato\_3.U\_04C91\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |
| QX1.83.3.4 |  |  |  | Ellato\_3.KSVG06792\_Cmd |
| QW1.83.3.5 |  |  |  |  |

Table 247. Controller\_3.1.83.3 DO910\* (DO4) (Status)

##### 4 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 248. Controller\_3 (172.16.96.153).1.83.4 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.83.4.1 | Output 1 | BoolIO | Ellato\_3.U\_04C91\_00.GCS\_1A.GCS\_1\_IO.Cmd | KSVG09167 |
| QX1.83.4.2 | Output 2 | BoolIO | Ellato\_3.U\_04C91\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG09163 |
| QX1.83.4.3 | Output 3 | BoolIO | Ellato\_3.KSVG00137\_Cmd | KSVG00137 |
| QX1.83.4.4 | Output 4 | BoolIO | Ellato\_3.KSVG00138\_Cmd | KSVG00138 |
| QW1.83.4.5 | All outputs | DwordIO |  |  |
| IW1.83.4.6 | UnitStatus | HwStatus |  |  |

Table 249. Controller\_3.1.83.4 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.83.4.1 | Ellato\_3.U\_04C91\_00.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.4.2 | Ellato\_3.U\_04C91\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.4.3 | Ellato\_3.KSVG00137\_Cmd |  |  |  |  | false |
| QX1.83.4.4 | Ellato\_3.KSVG00138\_Cmd |  |  |  |  | false |
| QW1.83.4.5 |  |  |  |  |  |  |

Table 250. Controller\_3.1.83.4 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.83.4.1 |  |  |  | Ellato\_3.U\_04C91\_00.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.83.4.2 |  |  |  | Ellato\_3.U\_04C91\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX1.83.4.3 |  |  |  | Ellato\_3.KSVG00137\_Cmd |
| QX1.83.4.4 |  |  |  | Ellato\_3.KSVG00138\_Cmd |
| QW1.83.4.5 |  |  |  |  |

Table 251. Controller\_3.1.83.4 DO910\* (DO4) (Status)

##### 5 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 252. Controller\_3 (172.16.96.153).1.83.5 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.83.5.1 | Output 1 | BoolIO | Ellato\_3.KSVG00165\_Cmd | KSVG00165 |
| QX1.83.5.2 | Output 2 | BoolIO | Ellato\_3.KSVG06863\_Cmd | KSVG06863 |
| QX1.83.5.3 | Output 3 | BoolIO | Ellato\_3.KSVG06759\_Cmd | KSVG06759 |
| QX1.83.5.4 | Output 4 | BoolIO | Ellato\_3.KSVG06798\_Cmd | KSVG06798 |
| QW1.83.5.5 | All outputs | DwordIO |  |  |
| IW1.83.5.6 | UnitStatus | HwStatus |  |  |

Table 253. Controller\_3.1.83.5 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.83.5.1 | Ellato\_3.KSVG00165\_Cmd |  |  |  |  | false |
| QX1.83.5.2 | Ellato\_3.KSVG06863\_Cmd |  |  |  |  | false |
| QX1.83.5.3 | Ellato\_3.KSVG06759\_Cmd |  |  |  |  | false |
| QX1.83.5.4 | Ellato\_3.KSVG06798\_Cmd |  |  |  |  | false |
| QW1.83.5.5 |  |  |  |  |  |  |

Table 254. Controller\_3.1.83.5 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.83.5.1 |  |  |  | Ellato\_3.KSVG00165\_Cmd |
| QX1.83.5.2 |  |  |  | Ellato\_3.KSVG06863\_Cmd |
| QX1.83.5.3 |  |  |  | Ellato\_3.KSVG06759\_Cmd |
| QX1.83.5.4 |  |  |  | Ellato\_3.KSVG06798\_Cmd |
| QW1.83.5.5 |  |  |  |  |

Table 255. Controller\_3.1.83.5 DO910\* (DO4) (Status)

##### 6 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 256. Controller\_3 (172.16.96.153).1.83.6 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.83.6.1 | Output 1 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM04D.GCS\_1B.GCS\_1\_IO.Cmd | KSVG00664 |
| QX1.83.6.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1E.GCS\_1\_IO.Cmd | KSVG04670 |
| QX1.83.6.3 | Output 3 | BoolIO | Ellato\_3.BEM\_3\_AX3.EqM02A\_1.GCS\_1A.GCS\_1\_IO.Cmd | KSVG60777 |
| QX1.83.6.4 | Output 4 | BoolIO | Ellato\_3.BEM\_3\_AX3.EqM02A\_2.GCS\_1A.GCS\_1\_IO.Cmd | KSVG60977 |
| QW1.83.6.5 | All outputs | DwordIO |  |  |
| IW1.83.6.6 | UnitStatus | HwStatus |  |  |

Table 257. Controller\_3.1.83.6 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.83.6.1 | Ellato\_3.U\_02C06\_Tart.EqM04D.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.6.2 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1E.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.6.3 | Ellato\_3.BEM\_3\_AX3.EqM02A\_1.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.6.4 | Ellato\_3.BEM\_3\_AX3.EqM02A\_2.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.83.6.5 |  |  |  |  |  |  |

Table 258. Controller\_3.1.83.6 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.83.6.1 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM04D.GCS\_1B.GCS\_1\_IO.Cmd |
| QX1.83.6.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1E.GCS\_1\_IO.Cmd |
| QX1.83.6.3 |  |  |  | Ellato\_3.BEM\_3\_AX3.EqM02A\_1.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.83.6.4 |  |  |  | Ellato\_3.BEM\_3\_AX3.EqM02A\_2.GCS\_1A.GCS\_1\_IO.Cmd |
| QW1.83.6.5 |  |  |  |  |

Table 259. Controller\_3.1.83.6 DO910\* (DO4) (Status)

##### 7 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 260. Controller\_3 (172.16.96.153).1.83.7 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.83.7.1 | Output 1 | BoolIO | Ellato\_3.U\_04C354\_00.EqM04A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG35474 |
| QX1.83.7.2 | Output 2 | BoolIO | Ellato\_3.U\_04C354\_00.EqM01Bax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG35476 |
| QX1.83.7.3 | Output 3 | BoolIO | Ellato\_3.U\_04C354\_00.EqM01Bax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG35477 |
| QX1.83.7.4 | Output 4 | BoolIO | Ellato\_3.U\_04C354\_00.EqM01Dax3\_1.GCS\_1A.GCS\_1\_IO.Cmd | KSVG35478 |
| QW1.83.7.5 | All outputs | DwordIO |  |  |
| IW1.83.7.6 | UnitStatus | HwStatus |  |  |

Table 261. Controller\_3.1.83.7 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.83.7.1 | Ellato\_3.U\_04C354\_00.EqM04A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.7.2 | Ellato\_3.U\_04C354\_00.EqM01Bax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.7.3 | Ellato\_3.U\_04C354\_00.EqM01Bax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.7.4 | Ellato\_3.U\_04C354\_00.EqM01Dax3\_1.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW1.83.7.5 |  |  |  |  |  |  |

Table 262. Controller\_3.1.83.7 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.83.7.1 |  |  |  | Ellato\_3.U\_04C354\_00.EqM04A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.83.7.2 |  |  |  | Ellato\_3.U\_04C354\_00.EqM01Bax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.83.7.3 |  |  |  | Ellato\_3.U\_04C354\_00.EqM01Bax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX1.83.7.4 |  |  |  | Ellato\_3.U\_04C354\_00.EqM01Dax3\_1.GCS\_1A.GCS\_1\_IO.Cmd |
| QW1.83.7.5 |  |  |  |  |

Table 263. Controller\_3.1.83.7 DO910\* (DO4) (Status)

##### 8 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 264. Controller\_3 (172.16.96.153).1.83.8 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX1.83.8.1 | Output 1 | BoolIO | Ellato\_3.U\_04C354\_00.EqM01Dax3\_2.GCS\_1A.GCS\_1\_IO.Cmd | KSVG35479 |
| QX1.83.8.2 | Output 2 | BoolIO | Ellato\_3.U\_04C354\_00.EqM01Dax3\_3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG35480 |
| QX1.83.8.3 | Output 3 | BoolIO | Ellato\_3.U\_04C354\_00.EqM04A\_2.GCS\_1A.GCS\_1\_IO.Cmd | KSVG35494 |
| QX1.83.8.4 | Output 4 | BoolIO |  |  |
| QW1.83.8.5 | All outputs | DwordIO |  |  |
| IW1.83.8.6 | UnitStatus | HwStatus |  |  |

Table 265. Controller\_3.1.83.8 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX1.83.8.1 | Ellato\_3.U\_04C354\_00.EqM01Dax3\_2.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.8.2 | Ellato\_3.U\_04C354\_00.EqM01Dax3\_3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.8.3 | Ellato\_3.U\_04C354\_00.EqM04A\_2.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX1.83.8.4 |  |  |  |  |  | false |
| QW1.83.8.5 |  |  |  |  |  |  |

Table 266. Controller\_3.1.83.8 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX1.83.8.1 |  |  |  | Ellato\_3.U\_04C354\_00.EqM01Dax3\_2.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.83.8.2 |  |  |  | Ellato\_3.U\_04C354\_00.EqM01Dax3\_3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.83.8.3 |  |  |  | Ellato\_3.U\_04C354\_00.EqM04A\_2.GCS\_1A.GCS\_1\_IO.Cmd |
| QX1.83.8.4 |  |  |  |  |
| QW1.83.8.5 |  |  |  |  |

Table 267. Controller\_3.1.83.8 DO910\* (DO4) (Status)

##### 9 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 268. Controller\_3 (172.16.96.153).1.83.9 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.83.9.1 | Input 1 | BoolIO | Ellato\_3.U\_04C354\_00.EqM04A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG35474 |
| IX1.83.9.2 | Input 2 | BoolIO | Ellato\_3.U\_04C354\_00.EqM01Bax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG35476 |
| IX1.83.9.3 | Input 3 | BoolIO | Ellato\_3.U\_04C354\_00.EqM01Bax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG35477 |
| IX1.83.9.4 | Input 4 | BoolIO | Ellato\_3.U\_04C354\_00.EqM01Dax3\_1.GCS\_1A.GCS\_1\_IO.FB0 | KSVG35478 |
| IX1.83.9.5 | Input 5 | BoolIO | Ellato\_3.U\_04C354\_00.EqM01Dax3\_2.GCS\_1A.GCS\_1\_IO.FB0 | KSVG35479 |
| IX1.83.9.6 | Input 6 | BoolIO | Ellato\_3.U\_04C354\_00.EqM01Dax3\_3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG35480 |
| IX1.83.9.7 | Input 7 | BoolIO | Ellato\_3.U\_04C354\_00.EqM04A\_2.GCS\_1A.GCS\_1\_IO.FB0 | KSVG35494 |
| IX1.83.9.8 | Input 8 | BoolIO | Ellato\_3.U\_04C354\_00.Level\_H.LevelH\_IO | LSH35424 |
| IW1.83.9.9 | All inputs | DwordIO |  |  |
| QX1.83.9.10 | Output 1 | BoolIO |  |  |
| QX1.83.9.11 | Output 2 | BoolIO |  |  |
| QX1.83.9.12 | Output 3 | BoolIO |  |  |
| QX1.83.9.13 | Output 4 | BoolIO |  |  |
| QX1.83.9.14 | Output 5 | BoolIO |  |  |
| QX1.83.9.15 | Output 6 | BoolIO |  |  |
| QX1.83.9.16 | Output 7 | BoolIO |  |  |
| QX1.83.9.17 | Output 8 | BoolIO |  |  |
| QW1.83.9.18 | All outputs | DwordIO |  |  |
| IX1.83.9.19 | Status Input 1 | BoolIO |  |  |
| IX1.83.9.20 | Status Input 2 | BoolIO |  |  |
| IX1.83.9.21 | Status Input 3 | BoolIO |  |  |
| IX1.83.9.22 | Status Input 4 | BoolIO |  |  |
| IX1.83.9.23 | Status Input 5 | BoolIO |  |  |
| IX1.83.9.24 | Status Input 6 | BoolIO |  |  |
| IX1.83.9.25 | Status Input 7 | BoolIO |  |  |
| IX1.83.9.26 | Status Input 8 | BoolIO |  |  |
| IW1.83.9.27 | UnitStatus | HwStatus |  |  |

Table 269. Controller\_3.1.83.9 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.83.9.1 | Ellato\_3.U\_04C354\_00.EqM04A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.9.2 | Ellato\_3.U\_04C354\_00.EqM01Bax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.9.3 | Ellato\_3.U\_04C354\_00.EqM01Bax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.9.4 | Ellato\_3.U\_04C354\_00.EqM01Dax3\_1.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.9.5 | Ellato\_3.U\_04C354\_00.EqM01Dax3\_2.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.9.6 | Ellato\_3.U\_04C354\_00.EqM01Dax3\_3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.9.7 | Ellato\_3.U\_04C354\_00.EqM04A\_2.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.9.8 | Ellato\_3.U\_04C354\_00.Level\_H.LevelH\_IO |  |  |  |  | true |
| QW1.83.9.9 |  |  |  |  |  |  |
| QX1.83.9.10 |  |  |  |  |  | false |
| QX1.83.9.11 |  |  |  |  |  | false |
| QX1.83.9.12 |  |  |  |  |  | false |
| QX1.83.9.13 |  |  |  |  |  | false |
| QX1.83.9.14 |  |  |  |  |  | false |
| QX1.83.9.15 |  |  |  |  |  | false |
| QX1.83.9.16 |  |  |  |  |  | false |
| QX1.83.9.17 |  |  |  |  |  | false |
| QW1.83.9.18 |  |  |  |  |  |  |
| IX1.83.9.19 |  |  |  |  |  | false |
| IX1.83.9.20 |  |  |  |  |  | false |
| IX1.83.9.21 |  |  |  |  |  | false |
| IX1.83.9.22 |  |  |  |  |  | false |
| IX1.83.9.23 |  |  |  |  |  | false |
| IX1.83.9.24 |  |  |  |  |  | false |
| IX1.83.9.25 |  |  |  |  |  | false |
| IX1.83.9.26 |  |  |  |  |  | false |

Table 270. Controller\_3.1.83.9 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.83.9.1 |  |  |  | Ellato\_3.U\_04C354\_00.EqM04A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.83.9.2 |  |  |  | Ellato\_3.U\_04C354\_00.EqM01Bax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.83.9.3 |  |  |  | Ellato\_3.U\_04C354\_00.EqM01Bax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IX1.83.9.4 |  |  |  | Ellato\_3.U\_04C354\_00.EqM01Dax3\_1.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.83.9.5 |  |  |  | Ellato\_3.U\_04C354\_00.EqM01Dax3\_2.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.83.9.6 |  |  |  | Ellato\_3.U\_04C354\_00.EqM01Dax3\_3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.83.9.7 |  |  |  | Ellato\_3.U\_04C354\_00.EqM04A\_2.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.83.9.8 |  |  |  | Ellato\_3.U\_04C354\_00.Level\_H.LevelH\_IO |
| IW1.83.9.9 |  |  |  |  |
| QX1.83.9.10 |  |  |  |  |
| QX1.83.9.11 |  |  |  |  |
| QX1.83.9.12 |  |  |  |  |
| QX1.83.9.13 |  |  |  |  |
| QX1.83.9.14 |  |  |  |  |
| QX1.83.9.15 |  |  |  |  |
| QX1.83.9.16 |  |  |  |  |
| QX1.83.9.17 |  |  |  |  |
| QW1.83.9.18 |  |  |  |  |
| IX1.83.9.19 |  |  |  |  |
| IX1.83.9.20 |  |  |  |  |
| IX1.83.9.21 |  |  |  |  |
| IX1.83.9.22 |  |  |  |  |
| IX1.83.9.23 |  |  |  |  |
| IX1.83.9.24 |  |  |  |  |
| IX1.83.9.25 |  |  |  |  |
| IX1.83.9.26 |  |  |  |  |

Table 271. Controller\_3.1.83.9 DX910\* (DIO8 S) (Status)

##### 10 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 272. Controller\_3 (172.16.96.153).1.83.10 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.83.10.1 | Input 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG01068 |
| IX1.83.10.2 | Input 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM10Cax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG01051 |
| IX1.83.10.3 | Input 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM10Cax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG01052 |
| IX1.83.10.4 | Input 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG01064 |
| IX1.83.10.5 | Input 5 | BoolIO | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG09181 |
| IX1.83.10.6 | Input 6 | BoolIO | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1C.GCS\_1\_IO.FB0 | KSVG09182 |
| IX1.83.10.7 | Input 7 | BoolIO | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG09171 |
| IX1.83.10.8 | Input 8 | BoolIO | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1D.GCS\_1\_IO.FB0 | KSVG09191 |
| IW1.83.10.9 | All inputs | DwordIO |  |  |
| QX1.83.10.10 | Output 1 | BoolIO |  |  |
| QX1.83.10.11 | Output 2 | BoolIO |  |  |
| QX1.83.10.12 | Output 3 | BoolIO |  |  |
| QX1.83.10.13 | Output 4 | BoolIO |  |  |
| QX1.83.10.14 | Output 5 | BoolIO |  |  |
| QX1.83.10.15 | Output 6 | BoolIO |  |  |
| QX1.83.10.16 | Output 7 | BoolIO |  |  |
| QX1.83.10.17 | Output 8 | BoolIO |  |  |
| QW1.83.10.18 | All outputs | DwordIO |  |  |
| IX1.83.10.19 | Status Input 1 | BoolIO |  |  |
| IX1.83.10.20 | Status Input 2 | BoolIO |  |  |
| IX1.83.10.21 | Status Input 3 | BoolIO |  |  |
| IX1.83.10.22 | Status Input 4 | BoolIO |  |  |
| IX1.83.10.23 | Status Input 5 | BoolIO |  |  |
| IX1.83.10.24 | Status Input 6 | BoolIO |  |  |
| IX1.83.10.25 | Status Input 7 | BoolIO |  |  |
| IX1.83.10.26 | Status Input 8 | BoolIO |  |  |
| IW1.83.10.27 | UnitStatus | HwStatus |  |  |

Table 273. Controller\_3.1.83.10 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.83.10.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.10.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM10Cax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.10.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM10Cax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.10.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.10.5 | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.10.6 | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.10.7 | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.10.8 | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1D.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW1.83.10.9 |  |  |  |  |  |  |
| QX1.83.10.10 |  |  |  |  |  | false |
| QX1.83.10.11 |  |  |  |  |  | false |
| QX1.83.10.12 |  |  |  |  |  | false |
| QX1.83.10.13 |  |  |  |  |  | false |
| QX1.83.10.14 |  |  |  |  |  | false |
| QX1.83.10.15 |  |  |  |  |  | false |
| QX1.83.10.16 |  |  |  |  |  | false |
| QX1.83.10.17 |  |  |  |  |  | false |
| QW1.83.10.18 |  |  |  |  |  |  |
| IX1.83.10.19 |  |  |  |  |  | false |
| IX1.83.10.20 |  |  |  |  |  | false |
| IX1.83.10.21 |  |  |  |  |  | false |
| IX1.83.10.22 |  |  |  |  |  | false |
| IX1.83.10.23 |  |  |  |  |  | false |
| IX1.83.10.24 |  |  |  |  |  | false |
| IX1.83.10.25 |  |  |  |  |  | false |
| IX1.83.10.26 |  |  |  |  |  | false |

Table 274. Controller\_3.1.83.10 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.83.10.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.83.10.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM10Cax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.83.10.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM10Cax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IX1.83.10.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.83.10.5 |  |  |  | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IX1.83.10.6 |  |  |  | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1C.GCS\_1\_IO.FB0 |
| IX1.83.10.7 |  |  |  | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.83.10.8 |  |  |  | Ellato\_3.U\_04C91\_00.EqM02Dax3.GCS\_1D.GCS\_1\_IO.FB0 |
| IW1.83.10.9 |  |  |  |  |
| QX1.83.10.10 |  |  |  |  |
| QX1.83.10.11 |  |  |  |  |
| QX1.83.10.12 |  |  |  |  |
| QX1.83.10.13 |  |  |  |  |
| QX1.83.10.14 |  |  |  |  |
| QX1.83.10.15 |  |  |  |  |
| QX1.83.10.16 |  |  |  |  |
| QX1.83.10.17 |  |  |  |  |
| QW1.83.10.18 |  |  |  |  |
| IX1.83.10.19 |  |  |  |  |
| IX1.83.10.20 |  |  |  |  |
| IX1.83.10.21 |  |  |  |  |
| IX1.83.10.22 |  |  |  |  |
| IX1.83.10.23 |  |  |  |  |
| IX1.83.10.24 |  |  |  |  |
| IX1.83.10.25 |  |  |  |  |
| IX1.83.10.26 |  |  |  |  |

Table 275. Controller\_3.1.83.10 DX910\* (DIO8 S) (Status)

##### 11 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 276. Controller\_3 (172.16.96.153).1.83.11 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.83.11.1 | Input 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_B.FS\_IO | FS01033 |
| IX1.83.11.2 | Input 2 | BoolIO | Ellato\_3.U\_04C91\_00.GCS\_1B.GCS\_1\_IO.FB0 | KSVG09175 |
| IX1.83.11.3 | Input 3 | BoolIO | Ellato\_3.U\_04C91\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG09164 |
| IX1.83.11.4 | Input 4 | BoolIO | Ellato\_3.U\_04C91\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 | KSVG09162 |
| IX1.83.11.5 | Input 5 | BoolIO | Ellato\_3.KSVG06863\_FB0 | KSVG06863 |
| IX1.83.11.6 | Input 6 | BoolIO | Ellato\_3.U\_04C91\_00.GCS\_1A.GCS\_1\_IO.FB0 | KSVG09167 |
| IX1.83.11.7 | Input 7 | BoolIO | Ellato\_3.U\_04C91\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG09163 |
| IX1.83.11.8 | Input 8 | BoolIO | Ellato\_3.U\_04C91\_00.EqM15Iax3.FS\_IO | FS09139 |
| IW1.83.11.9 | All inputs | DwordIO |  |  |
| QX1.83.11.10 | Output 1 | BoolIO |  |  |
| QX1.83.11.11 | Output 2 | BoolIO |  |  |
| QX1.83.11.12 | Output 3 | BoolIO |  |  |
| QX1.83.11.13 | Output 4 | BoolIO |  |  |
| QX1.83.11.14 | Output 5 | BoolIO |  |  |
| QX1.83.11.15 | Output 6 | BoolIO |  |  |
| QX1.83.11.16 | Output 7 | BoolIO |  |  |
| QX1.83.11.17 | Output 8 | BoolIO |  |  |
| QW1.83.11.18 | All outputs | DwordIO |  |  |
| IX1.83.11.19 | Status Input 1 | BoolIO |  |  |
| IX1.83.11.20 | Status Input 2 | BoolIO |  |  |
| IX1.83.11.21 | Status Input 3 | BoolIO |  |  |
| IX1.83.11.22 | Status Input 4 | BoolIO |  |  |
| IX1.83.11.23 | Status Input 5 | BoolIO |  |  |
| IX1.83.11.24 | Status Input 6 | BoolIO |  |  |
| IX1.83.11.25 | Status Input 7 | BoolIO |  |  |
| IX1.83.11.26 | Status Input 8 | BoolIO |  |  |
| IW1.83.11.27 | UnitStatus | HwStatus |  |  |

Table 277. Controller\_3.1.83.11 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.83.11.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_B.FS\_IO |  |  |  |  | false |
| IX1.83.11.2 | Ellato\_3.U\_04C91\_00.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.11.3 | Ellato\_3.U\_04C91\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.11.4 | Ellato\_3.U\_04C91\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.11.5 | Ellato\_3.KSVG06863\_FB0 |  |  |  |  | false |
| IX1.83.11.6 | Ellato\_3.U\_04C91\_00.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.11.7 | Ellato\_3.U\_04C91\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.11.8 | Ellato\_3.U\_04C91\_00.EqM15Iax3.FS\_IO |  |  |  |  | false |
| QW1.83.11.9 |  |  |  |  |  |  |
| QX1.83.11.10 |  |  |  |  |  | false |
| QX1.83.11.11 |  |  |  |  |  | false |
| QX1.83.11.12 |  |  |  |  |  | false |
| QX1.83.11.13 |  |  |  |  |  | false |
| QX1.83.11.14 |  |  |  |  |  | false |
| QX1.83.11.15 |  |  |  |  |  | false |
| QX1.83.11.16 |  |  |  |  |  | false |
| QX1.83.11.17 |  |  |  |  |  | false |
| QW1.83.11.18 |  |  |  |  |  |  |
| IX1.83.11.19 |  |  |  |  |  | false |
| IX1.83.11.20 |  |  |  |  |  | false |
| IX1.83.11.21 |  |  |  |  |  | false |
| IX1.83.11.22 |  |  |  |  |  | false |
| IX1.83.11.23 |  |  |  |  |  | false |
| IX1.83.11.24 |  |  |  |  |  | false |
| IX1.83.11.25 |  |  |  |  |  | false |
| IX1.83.11.26 |  |  |  |  |  | false |

Table 278. Controller\_3.1.83.11 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.83.11.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_B.FS\_IO |
| IX1.83.11.2 |  |  |  | Ellato\_3.U\_04C91\_00.GCS\_1B.GCS\_1\_IO.FB0 |
| IX1.83.11.3 |  |  |  | Ellato\_3.U\_04C91\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.83.11.4 |  |  |  | Ellato\_3.U\_04C91\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |
| IX1.83.11.5 |  |  |  | Ellato\_3.KSVG06863\_FB0 |
| IX1.83.11.6 |  |  |  | Ellato\_3.U\_04C91\_00.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.83.11.7 |  |  |  | Ellato\_3.U\_04C91\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IX1.83.11.8 |  |  |  | Ellato\_3.U\_04C91\_00.EqM15Iax3.FS\_IO |
| IW1.83.11.9 |  |  |  |  |
| QX1.83.11.10 |  |  |  |  |
| QX1.83.11.11 |  |  |  |  |
| QX1.83.11.12 |  |  |  |  |
| QX1.83.11.13 |  |  |  |  |
| QX1.83.11.14 |  |  |  |  |
| QX1.83.11.15 |  |  |  |  |
| QX1.83.11.16 |  |  |  |  |
| QX1.83.11.17 |  |  |  |  |
| QW1.83.11.18 |  |  |  |  |
| IX1.83.11.19 |  |  |  |  |
| IX1.83.11.20 |  |  |  |  |
| IX1.83.11.21 |  |  |  |  |
| IX1.83.11.22 |  |  |  |  |
| IX1.83.11.23 |  |  |  |  |
| IX1.83.11.24 |  |  |  |  |
| IX1.83.11.25 |  |  |  |  |
| IX1.83.11.26 |  |  |  |  |

Table 279. Controller\_3.1.83.11 DX910\* (DIO8 S) (Status)

##### 12 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 280. Controller\_3 (172.16.96.153).1.83.12 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.83.12.1 | Input 1 | BoolIO | Ellato\_3.KSVG00137\_FB0 | KSVG00137 |
| IX1.83.12.2 | Input 2 | BoolIO | Ellato\_3.KSVG00138\_FB0 | KSVG00138 |
| IX1.83.12.3 | Input 3 | BoolIO | Ellato\_3.KSVG00165\_FB0 | KSVG00165 |
| IX1.83.12.4 | Input 4 | BoolIO | Ellato\_3.KSVG06792\_FB0 | KSVG06792 |
| IX1.83.12.5 | Input 5 | BoolIO | Ellato\_3.KSVG06759\_FB0 | KSVG06759 |
| IX1.83.12.6 | Input 6 | BoolIO | Ellato\_3.KSVG06798\_FB0 | KSVG06798 |
| IX1.83.12.7 | Input 7 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM04D.GCS\_1B.GCS\_1\_IO.FB0 | KSVG00664 |
| IX1.83.12.8 | Input 8 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1E.GCS\_1\_IO.FB0 | KSVG04670 |
| IW1.83.12.9 | All inputs | DwordIO |  |  |
| QX1.83.12.10 | Output 1 | BoolIO |  |  |
| QX1.83.12.11 | Output 2 | BoolIO |  |  |
| QX1.83.12.12 | Output 3 | BoolIO |  |  |
| QX1.83.12.13 | Output 4 | BoolIO |  |  |
| QX1.83.12.14 | Output 5 | BoolIO |  |  |
| QX1.83.12.15 | Output 6 | BoolIO |  |  |
| QX1.83.12.16 | Output 7 | BoolIO |  |  |
| QX1.83.12.17 | Output 8 | BoolIO |  |  |
| QW1.83.12.18 | All outputs | DwordIO |  |  |
| IX1.83.12.19 | Status Input 1 | BoolIO |  |  |
| IX1.83.12.20 | Status Input 2 | BoolIO |  |  |
| IX1.83.12.21 | Status Input 3 | BoolIO |  |  |
| IX1.83.12.22 | Status Input 4 | BoolIO |  |  |
| IX1.83.12.23 | Status Input 5 | BoolIO |  |  |
| IX1.83.12.24 | Status Input 6 | BoolIO |  |  |
| IX1.83.12.25 | Status Input 7 | BoolIO |  |  |
| IX1.83.12.26 | Status Input 8 | BoolIO |  |  |
| IW1.83.12.27 | UnitStatus | HwStatus |  |  |

Table 281. Controller\_3.1.83.12 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.83.12.1 | Ellato\_3.KSVG00137\_FB0 |  |  |  |  | false |
| IX1.83.12.2 | Ellato\_3.KSVG00138\_FB0 |  |  |  |  | false |
| IX1.83.12.3 | Ellato\_3.KSVG00165\_FB0 |  |  |  |  | false |
| IX1.83.12.4 | Ellato\_3.KSVG06792\_FB0 |  |  |  |  | false |
| IX1.83.12.5 | Ellato\_3.KSVG06759\_FB0 |  |  |  |  | false |
| IX1.83.12.6 | Ellato\_3.KSVG06798\_FB0 |  |  |  |  | false |
| IX1.83.12.7 | Ellato\_3.U\_02C06\_Tart.EqM04D.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.12.8 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1E.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW1.83.12.9 |  |  |  |  |  |  |
| QX1.83.12.10 |  |  |  |  |  | false |
| QX1.83.12.11 |  |  |  |  |  | false |
| QX1.83.12.12 |  |  |  |  |  | false |
| QX1.83.12.13 |  |  |  |  |  | false |
| QX1.83.12.14 |  |  |  |  |  | false |
| QX1.83.12.15 |  |  |  |  |  | false |
| QX1.83.12.16 |  |  |  |  |  | false |
| QX1.83.12.17 |  |  |  |  |  | false |
| QW1.83.12.18 |  |  |  |  |  |  |
| IX1.83.12.19 |  |  |  |  |  | false |
| IX1.83.12.20 |  |  |  |  |  | false |
| IX1.83.12.21 |  |  |  |  |  | false |
| IX1.83.12.22 |  |  |  |  |  | false |
| IX1.83.12.23 |  |  |  |  |  | false |
| IX1.83.12.24 |  |  |  |  |  | false |
| IX1.83.12.25 |  |  |  |  |  | false |
| IX1.83.12.26 |  |  |  |  |  | false |

Table 282. Controller\_3.1.83.12 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.83.12.1 |  |  |  | Ellato\_3.KSVG00137\_FB0 |
| IX1.83.12.2 |  |  |  | Ellato\_3.KSVG00138\_FB0 |
| IX1.83.12.3 |  |  |  | Ellato\_3.KSVG00165\_FB0 |
| IX1.83.12.4 |  |  |  | Ellato\_3.KSVG06792\_FB0 |
| IX1.83.12.5 |  |  |  | Ellato\_3.KSVG06759\_FB0 |
| IX1.83.12.6 |  |  |  | Ellato\_3.KSVG06798\_FB0 |
| IX1.83.12.7 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM04D.GCS\_1B.GCS\_1\_IO.FB0 |
| IX1.83.12.8 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1E.GCS\_1\_IO.FB0 |
| IW1.83.12.9 |  |  |  |  |
| QX1.83.12.10 |  |  |  |  |
| QX1.83.12.11 |  |  |  |  |
| QX1.83.12.12 |  |  |  |  |
| QX1.83.12.13 |  |  |  |  |
| QX1.83.12.14 |  |  |  |  |
| QX1.83.12.15 |  |  |  |  |
| QX1.83.12.16 |  |  |  |  |
| QX1.83.12.17 |  |  |  |  |
| QW1.83.12.18 |  |  |  |  |
| IX1.83.12.19 |  |  |  |  |
| IX1.83.12.20 |  |  |  |  |
| IX1.83.12.21 |  |  |  |  |
| IX1.83.12.22 |  |  |  |  |
| IX1.83.12.23 |  |  |  |  |
| IX1.83.12.24 |  |  |  |  |
| IX1.83.12.25 |  |  |  |  |
| IX1.83.12.26 |  |  |  |  |

Table 283. Controller\_3.1.83.12 DX910\* (DIO8 S) (Status)

##### 13 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | true | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | true | bool |  |  |  |
| ch. 6: deactivate | true | bool |  |  |  |
| ch. 7: deactivate | true | bool |  |  |  |
| ch. 8: deactivate | true | bool |  |  |  |

Table 284. Controller\_3 (172.16.96.153).1.83.13 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX1.83.13.1 | Input 1 | BoolIO |  |  |
| IX1.83.13.2 | Input 2 | BoolIO | Ellato\_3.U\_PL\_C3.EqMPLB.PL\_1.PL\_1\_IO | PL 02C32 |
| IX1.83.13.3 | Input 3 | BoolIO | Ellato\_3.BEM\_3\_AX3.EqM02A\_1.GCS\_1A.GCS\_1\_IO.FB0 | KSVG60777 |
| IX1.83.13.4 | Input 4 | BoolIO | Ellato\_3.BEM\_3\_AX3.EqM02A\_2.GCS\_1A.GCS\_1\_IO.FB0 | KSVG60977 |
| IX1.83.13.5 | Input 5 | BoolIO |  |  |
| IX1.83.13.6 | Input 6 | BoolIO |  |  |
| IX1.83.13.7 | Input 7 | BoolIO |  |  |
| IX1.83.13.8 | Input 8 | BoolIO |  |  |
| IW1.83.13.9 | All inputs | DwordIO |  |  |
| QX1.83.13.10 | Output 1 | BoolIO |  |  |
| QX1.83.13.11 | Output 2 | BoolIO |  |  |
| QX1.83.13.12 | Output 3 | BoolIO |  |  |
| QX1.83.13.13 | Output 4 | BoolIO |  |  |
| QX1.83.13.14 | Output 5 | BoolIO |  |  |
| QX1.83.13.15 | Output 6 | BoolIO |  |  |
| QX1.83.13.16 | Output 7 | BoolIO |  |  |
| QX1.83.13.17 | Output 8 | BoolIO |  |  |
| QW1.83.13.18 | All outputs | DwordIO |  |  |
| IX1.83.13.19 | Status Input 1 | BoolIO |  |  |
| IX1.83.13.20 | Status Input 2 | BoolIO |  |  |
| IX1.83.13.21 | Status Input 3 | BoolIO |  |  |
| IX1.83.13.22 | Status Input 4 | BoolIO |  |  |
| IX1.83.13.23 | Status Input 5 | BoolIO |  |  |
| IX1.83.13.24 | Status Input 6 | BoolIO |  |  |
| IX1.83.13.25 | Status Input 7 | BoolIO |  |  |
| IX1.83.13.26 | Status Input 8 | BoolIO |  |  |
| IW1.83.13.27 | UnitStatus | HwStatus |  |  |

Table 285. Controller\_3.1.83.13 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX1.83.13.1 |  |  |  |  |  | false |
| IX1.83.13.2 | Ellato\_3.U\_PL\_C3.EqMPLB.PL\_1.PL\_1\_IO |  |  |  |  | false |
| IX1.83.13.3 | Ellato\_3.BEM\_3\_AX3.EqM02A\_1.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.13.4 | Ellato\_3.BEM\_3\_AX3.EqM02A\_2.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX1.83.13.5 |  |  |  |  |  | false |
| IX1.83.13.6 |  |  |  |  |  | false |
| IX1.83.13.7 |  |  |  |  |  | false |
| IX1.83.13.8 |  |  |  |  |  | false |
| QW1.83.13.9 |  |  |  |  |  |  |
| QX1.83.13.10 |  |  |  |  |  | false |
| QX1.83.13.11 |  |  |  |  |  | false |
| QX1.83.13.12 |  |  |  |  |  | false |
| QX1.83.13.13 |  |  |  |  |  | false |
| QX1.83.13.14 |  |  |  |  |  | false |
| QX1.83.13.15 |  |  |  |  |  | false |
| QX1.83.13.16 |  |  |  |  |  | false |
| QX1.83.13.17 |  |  |  |  |  | false |
| QW1.83.13.18 |  |  |  |  |  |  |
| IX1.83.13.19 |  |  |  |  |  | false |
| IX1.83.13.20 |  |  |  |  |  | false |
| IX1.83.13.21 |  |  |  |  |  | false |
| IX1.83.13.22 |  |  |  |  |  | false |
| IX1.83.13.23 |  |  |  |  |  | false |
| IX1.83.13.24 |  |  |  |  |  | false |
| IX1.83.13.25 |  |  |  |  |  | false |
| IX1.83.13.26 |  |  |  |  |  | false |

Table 286. Controller\_3.1.83.13 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX1.83.13.1 |  |  |  |  |
| IX1.83.13.2 |  |  |  | Ellato\_3.U\_PL\_C3.EqMPLB.PL\_1.PL\_1\_IO |
| IX1.83.13.3 |  |  |  | Ellato\_3.BEM\_3\_AX3.EqM02A\_1.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.83.13.4 |  |  |  | Ellato\_3.BEM\_3\_AX3.EqM02A\_2.GCS\_1A.GCS\_1\_IO.FB0 |
| IX1.83.13.5 |  |  |  |  |
| IX1.83.13.6 |  |  |  |  |
| IX1.83.13.7 |  |  |  |  |
| IX1.83.13.8 |  |  |  |  |
| IW1.83.13.9 |  |  |  |  |
| QX1.83.13.10 |  |  |  |  |
| QX1.83.13.11 |  |  |  |  |
| QX1.83.13.12 |  |  |  |  |
| QX1.83.13.13 |  |  |  |  |
| QX1.83.13.14 |  |  |  |  |
| QX1.83.13.15 |  |  |  |  |
| QX1.83.13.16 |  |  |  |  |
| QX1.83.13.17 |  |  |  |  |
| QW1.83.13.18 |  |  |  |  |
| IX1.83.13.19 |  |  |  |  |
| IX1.83.13.20 |  |  |  |  |
| IX1.83.13.21 |  |  |  |  |
| IX1.83.13.22 |  |  |  |  |
| IX1.83.13.23 |  |  |  |  |
| IX1.83.13.24 |  |  |  |  |
| IX1.83.13.25 |  |  |  |  |
| IX1.83.13.26 |  |  |  |  |

Table 287. Controller\_3.1.83.13 DX910\* (DIO8 S) (Status)

##### 14 AO910\* (AO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: range | 4..20mA | enum |  |  |  |
| ch. 2: range | 4..20mA | enum |  |  |  |
| ch. 3: range | 4..20mA | enum |  |  |  |
| ch. 4: range | 4..20mA | enum |  |  |  |

Table 288. Controller\_3 (172.16.96.153).1.83.14 AO910\* (AO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QW1.83.14.1 | Output 1 | RealIO | Ellato\_3.U\_04C354\_00.CNT\_4Bax3\_CB.CNTValve\_IO.SP | KCV35473 |
| QW1.83.14.2 | Output 2 | RealIO |  |  |
| QW1.83.14.3 | Output 3 | RealIO |  |  |
| QW1.83.14.4 | Output 4 | RealIO |  |  |
| IW1.83.14.5 | UnitStatus | HwStatus |  |  |

Table 289. Controller\_3.1.83.14 AO910\* (AO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QW1.83.14.1 | Ellato\_3.U\_04C354\_00.CNT\_4Bax3\_CB.CNTValve\_IO.SP | 0.0 | 100.0 |  | 1 | false |
| QW1.83.14.2 |  | 0.0 | 100.0 |  | 1 | false |
| QW1.83.14.3 |  | 0.0 | 100.0 |  | 1 | false |
| QW1.83.14.4 |  | 0.0 | 100.0 |  | 1 | false |

Table 290. Controller\_3.1.83.14 AO910\* (AO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QW1.83.14.1 |  |  |  | Ellato\_3.U\_04C354\_00.CNT\_4Bax3\_CB.CNTValve\_IO.SP |
| QW1.83.14.2 |  |  |  |  |
| QW1.83.14.3 |  |  |  |  |
| QW1.83.14.4 |  |  |  |  |

Table 291. Controller\_3.1.83.14 AO910\* (AO4) (Status)

##### 15 AI930\* (AI4H A)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: HART status / range | off / 4..20mA | enum |  |  |  |
| ch. 2: HART status / range | off / 4..20mA | enum |  |  |  |
| ch. 3: HART status / range | off / 4..20mA | enum |  |  |  |
| ch. 4: HART status / range | off / 4..20mA | enum |  |  |  |
| ch. 1: filter (PT1) | off | enum |  |  |  |
| ch. 2: filter (PT1) | off | enum |  |  |  |
| ch. 3: filter (PT1) | off | enum |  |  |  |
| ch. 4: filter (PT1) | off | enum |  |  |  |

Table 292. Controller\_3 (172.16.96.153).1.83.15 AI930\* (AI4H A) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.83.15.1 | Input 1 | RealIO | Ellato\_3.U\_04C354\_00.IND\_1\_B.IND\_1\_IO | TIT35404 |
| IW1.83.15.2 | Input 2 | RealIO | Ellato\_3.U\_04C354\_00.IND\_1.IND\_1\_IO | TIT35405 |
| IW1.83.15.3 | Input 3 | RealIO | Ellato\_3.U\_04C354\_00.IND\_5ax3.IND\_5\_IO | PIT35413 |
| IW1.83.15.4 | Input 4 | RealIO | Ellato\_3.U\_04C354\_00.CNT\_4Bax3\_CB.IND\_5\_Cerabar.IND\_5\_IO | PIT35412 |
| IW1.83.15.5 | UnitStatus | HwStatus |  |  |

Table 293. Controller\_3.1.83.15 AI930\* (AI4H A) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.83.15.1 | Ellato\_3.U\_04C354\_00.IND\_1\_B.IND\_1\_IO | 0.0 | 100.0 | °C | 2 | false |
| IW1.83.15.2 | Ellato\_3.U\_04C354\_00.IND\_1.IND\_1\_IO | 0.0 | 100.0 | °C | 2 | false |
| IW1.83.15.3 | Ellato\_3.U\_04C354\_00.IND\_5ax3.IND\_5\_IO | 1.1 | 3.1 | bar | 2 | false |
| IW1.83.15.4 | Ellato\_3.U\_04C354\_00.CNT\_4Bax3\_CB.IND\_5\_Cerabar.IND\_5\_IO | 0.0 | 1.6 | bar | 2 | false |

Table 294. Controller\_3.1.83.15 AI930\* (AI4H A) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.83.15.1 |  |  |  | Ellato\_3.U\_04C354\_00.IND\_1\_B.IND\_1\_IO |
| IW1.83.15.2 |  |  |  | Ellato\_3.U\_04C354\_00.IND\_1.IND\_1\_IO |
| IW1.83.15.3 |  |  |  | Ellato\_3.U\_04C354\_00.IND\_5ax3.IND\_5\_IO |
| IW1.83.15.4 |  |  |  | Ellato\_3.U\_04C354\_00.CNT\_4Bax3\_CB.IND\_5\_Cerabar.IND\_5\_IO |

Table 295. Controller\_3.1.83.15 AI930\* (AI4H A) (Status)

##### 16 AI930\* (AI4H A)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | off | enum |  |  |  |
| ch. 3: open line detect | off | enum |  |  |  |
| ch. 4: open line detect | off | enum |  |  |  |
| ch. 1: HART status / range | off / 4..20mA | enum |  |  |  |
| ch. 2: HART status / range | off / 0..20mA | enum |  |  |  |
| ch. 3: HART status / range | off / 0..20mA | enum |  |  |  |
| ch. 4: HART status / range | off / 0..20mA | enum |  |  |  |
| ch. 1: filter (PT1) | off | enum |  |  |  |
| ch. 2: filter (PT1) | off | enum |  |  |  |
| ch. 3: filter (PT1) | off | enum |  |  |  |
| ch. 4: filter (PT1) | off | enum |  |  |  |

Table 296. Controller\_3 (172.16.96.153).1.83.16 AI930\* (AI4H A) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW1.83.16.1 | Input 1 | RealIO | Ellato\_3.U\_04C91\_00.IND\_2ax3.IND\_2\_IO | LITSAH09121 |
| IW1.83.16.2 | Input 2 | RealIO |  |  |
| IW1.83.16.3 | Input 3 | RealIO |  |  |
| IW1.83.16.4 | Input 4 | RealIO |  |  |
| IW1.83.16.5 | UnitStatus | HwStatus |  |  |

Table 297. Controller\_3.1.83.16 AI930\* (AI4H A) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW1.83.16.1 | Ellato\_3.U\_04C91\_00.IND\_2ax3.IND\_2\_IO | 0.0 | 100.0 |  | 1 | false |
| IW1.83.16.2 |  | 0.0 | 100.0 |  | 1 | false |
| IW1.83.16.3 |  | 0.0 | 100.0 |  | 1 | false |
| IW1.83.16.4 |  | 0.0 | 100.0 |  | 1 | false |

Table 298. Controller\_3.1.83.16 AI930\* (AI4H A) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW1.83.16.1 |  |  |  | Ellato\_3.U\_04C91\_00.IND\_2ax3.IND\_2\_IO |
| IW1.83.16.2 |  |  |  |  |
| IW1.83.16.3 |  |  |  |  |
| IW1.83.16.4 |  |  |  |  |

Table 299. Controller\_3.1.83.16 AI930\* (AI4H A) (Status)

### 2 (8) CI854

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Node address (FDL\_Add) | 1 | dint |  | 1 | 125 |
| Baudrate | 1.5M | enum | bit/s |  |  |
| Slot time (TSL) | 300 | dint | tbit | 37 | 16383 |
| Min station delay (min TSDR) | 11 | dint | tbit | 11 | 1023 |
| Max station delay (max TSDR) | 150 | dint | tbit | 37 | 65535 |
| Quiet time (TQui) | 0 | dint | tbit | 0 | 127 |
| Setup time (TSet) | 20 | dint | tbit | 1 | 255 |
| Target rotation time (TTR) | 37662 | dint | tbit | 255 | 999999 |
| Gap factor (G) | 1 | dint |  | 1 | 100 |
| Highest station address (HSA) | 126 | dint |  | 1 | 126 |
| Max retry limit | 3 | dint |  | 0 | 7 |
| BP flag | Auto Clear OFF | enum |  |  |  |
| Min slave interval | 20 | dint | 100us | 1 | 65535 |
| Poll time out | 50 | dint | ms | 1 | 65535 |
| Data control time | 250 | dint | 10ms | 1 | 65535 |
| Watchdog time | 1250 | dint | ms | 10 | 650250 |
| Line redundancy | Disabled | enum |  |  |  |
| Delta TTR | 0 | dint | % | 0 | 100 |
| Calculation method | Actual value based | enum |  |  |  |

Table 300. Controller\_3 (172.16.96.153).2 CI854 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.0 | UnitStatus | HwStatus |  |  |

Table 301. Controller\_3.2 CI854 (Connections)

#### 11 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.11.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 302. Controller\_3.2.11 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.11.0.0 | Temp1 Value | RealIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO | TIT03901 |
| IW2.11.0.1 | Temp1 Status | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO\_Status |  |
| IW2.11.0.2 | UnitStatus | HwStatus |  |  |

Table 303. Controller\_3.2.11.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.11.0.0 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW2.11.0.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO\_Status |  |  |  |  |  |

Table 304. Controller\_3.2.11.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.11.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO |
| IW2.11.0.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO\_Status |

Table 305. Controller\_3.2.11.0 Temperature 1 (Status)

#### 12 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.12.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 306. Controller\_3.2.12 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.12.0.0 | Temp1 Value | RealIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_A.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO | TIT03905 |
| IW2.12.0.1 | Temp1 Status | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_A.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |  |
| IW2.12.0.2 | UnitStatus | HwStatus |  |  |

Table 307. Controller\_3.2.12.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.12.0.0 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_A.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW2.12.0.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_A.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |  |  |  |  |  |

Table 308. Controller\_3.2.12.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.12.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_A.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO |
| IW2.12.0.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_A.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |

Table 309. Controller\_3.2.12.0 Temperature 1 (Status)

#### 13 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.13.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 310. Controller\_3.2.13 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.13.0.0 | Temp1 Value | RealIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_B.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO | TIT03905 |
| IW2.13.0.1 | Temp1 Status | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_B.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |  |
| IW2.13.0.2 | UnitStatus | HwStatus |  |  |

Table 311. Controller\_3.2.13.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.13.0.0 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_B.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW2.13.0.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_B.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |  |  |  |  |  |

Table 312. Controller\_3.2.13.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.13.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_B.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO |
| IW2.13.0.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_B.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |

Table 313. Controller\_3.2.13.0 Temperature 1 (Status)

#### 14 ABB\_TTX\_300\_PA

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Status format | condensed | enum |  |  |  |
| Parameterization | DPV1 only | enum |  |  |  |
| Measurement Type (PV =) | SV1 (Sensor 1) | enum |  |  |  |
| PV unit | degF | enum |  |  |  |
| Signal Characteristic | medium | enum |  |  |  |
| Sensor 1 (LIN\_TYPE) | Pt100 (IEC751) | enum |  |  |  |
| Sensor Connect. 1 / RJ Type 1 | 3 wire / internal reference | enum |  |  |  |
| Line R 1 / Fix. RJ Val. 1 (x100) | 0 | dint |  | -32000 | 32000 |
| Sensor 2 (LIN\_TYPE\_2) | Off | enum |  |  |  |
| Sensor Connect. 2 / RJ Type 2 | 3 wire / internal reference | enum |  |  |  |
| Line R 2 / Fix. RJ Val. 2 (x100) | 0 | dint |  | -32000 | 32000 |
| HMI Language | English | enum |  |  |  |
| HMI Local operation | Enable | enum |  |  |  |
| HMI Autoscroll | Disable | enum |  |  |  |
| HMI Contrast | 50 | dint |  | 0 | 100 |
| HMI Select View1 | Calculated Value | enum |  |  |  |
| HMI Select View2 Line1 | Sensor 1 | enum |  |  |  |
| HMI Select View2 Line2 | Sensor 2 | enum |  |  |  |
| DMask1: Warmstart | Disable | enum |  |  |  |
| DMask1: Coldstart | Disable | enum |  |  |  |
| DMask1: Sensor drift | Disable | enum |  |  |  |
| DMask1: S1 line res. too high | Disable | enum |  |  |  |
| DMask1: S1 short circuit | Disable | enum |  |  |  |
| DMask1: S1 wire break | Disable | enum |  |  |  |
| DMask1: S2 line res. too high | Disable | enum |  |  |  |
| DMask1: S2 short circuit | Disable | enum |  |  |  |
| DMask1: S2 wire break | Disable | enum |  |  |  |
| DMask1: Over meas. range S1 | Disable | enum |  |  |  |
| DMask1: Under meas. range S1 | Disable | enum |  |  |  |
| DMask1: Over meas. range S2 | Disable | enum |  |  |  |
| DMask1: Under meas. range S2 | Disable | enum |  |  |  |
| DMask1: Dev.Temp outside spec. | Disable | enum |  |  |  |
| DMask1: Hardware failure | Disable | enum |  |  |  |
| DMask1: Device not calibrated | Disable | enum |  |  |  |
| DMask1: Device is simulated | Disable | enum |  |  |  |
| DMask1: Configuration failure | Disable | enum |  |  |  |
| DMask1: Red. Sensor 1+2 failure | Disable | enum |  |  |  |
| DMask1: Red. S1 short circuit | Disable | enum |  |  |  |
| DMask1: Red. S1 wire break | Disable | enum |  |  |  |
| DMask1: Red. S2 short circuit | Disable | enum |  |  |  |
| DMask1: Red. S2 wire break | Disable | enum |  |  |  |
| DMask2: Warmstart | Disable | enum |  |  |  |
| DMask2: Coldstart | Disable | enum |  |  |  |
| DMask2: Sensor drift | Disable | enum |  |  |  |
| DMask2: S1 line res. too high | Disable | enum |  |  |  |
| DMask2: S1 short circuit | Disable | enum |  |  |  |
| DMask2: S1 wire break | Disable | enum |  |  |  |
| DMask2: S2 line res. too high | Disable | enum |  |  |  |
| DMask2: S2 short circuit | Disable | enum |  |  |  |
| DMask2: S2 wire break | Disable | enum |  |  |  |
| DMask2: Over meas. range S1 | Disable | enum |  |  |  |
| DMask2: Under meas. range S1 | Disable | enum |  |  |  |
| DMask2: Over meas. range S2 | Disable | enum |  |  |  |
| DMask2: Under meas. range S2 | Disable | enum |  |  |  |
| DMask2: Dev.Temp outside spec. | Disable | enum |  |  |  |
| DMask2: Hardware failure | Disable | enum |  |  |  |
| DMask2: Device not calibrated | Disable | enum |  |  |  |
| DMask2: Device is simulated | Disable | enum |  |  |  |
| DMask2: Configuration failure | Disable | enum |  |  |  |
| DMask2: Red. Sensor 1+2 failure | Disable | enum |  |  |  |
| DMask2: Red. S1 short circuit | Disable | enum |  |  |  |
| DMask2: Red. S1 wire break | Disable | enum |  |  |  |
| DMask2: Red. S2 short circuit | Disable | enum |  |  |  |
| DMask2: Red. S2 wire break | Disable | enum |  |  |  |

Table 314. Controller\_3 (172.16.96.153).2.14 ABB\_TTX\_300\_PA (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.14.0 | UnitStatus | HwStatus |  |  |

Table 315. Controller\_3.2.14 ABB\_TTX\_300\_PA (Connections)

##### 1 Analog Input (AI)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Scale START input (PVEu0 x10) | 0 | dint |  | -32000 | 32000 |
| Scale END input (PVEu100 x10) | 1000 | dint |  | -32000 | 32000 |
| Scale START output (OUTEu0 x10) | 0 | dint |  | -32000 | 32000 |
| Scale END output (OUTEu100 x10) | 1000 | dint |  | -32000 | 32000 |
| Unit | degC | enum |  |  |  |
| Damping (PVTIME) | no | enum |  |  |  |
| Failure strategy (FSAFE\_TYPE) | Use calculated value + BAD | enum |  |  |  |
| Failsafe value (FSAFE\_VALUE x10) | 32000 | dint |  | -32000 | 32000 |
| Upper lim. alarm (HI\_HI\_LIM x10) | 32001 | dint |  | -32001 | 32001 |
| Upper lim. warning (HI\_LIM x10) | 32001 | dint |  | -32001 | 32001 |
| Lower lim. warning (LO\_LIM x10) | -32001 | dint |  | -32001 | 32001 |
| Lower lim. alarm (LO\_LO\_LIM x10) | -32001 | dint |  | -32001 | 32001 |

Table 316. Controller\_3 (172.16.96.153).2.14.1 Analog Input (AI) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.14.1.0 | Analog Input | RealIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax3\_B.IND\_1\_IO | TIT03909 |
| IW2.14.1.1 | Analog Input Status | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax3\_B.IND\_1\_IO\_Status |  |
| IW2.14.1.2 | UnitStatus | HwStatus |  |  |

Table 317. Controller\_3.2.14.1 Analog Input (AI) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.14.1.0 | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax3\_B.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW2.14.1.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax3\_B.IND\_1\_IO\_Status |  |  |  |  |  |

Table 318. Controller\_3.2.14.1 Analog Input (AI) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.14.1.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax3\_B.IND\_1\_IO |
| IW2.14.1.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax3\_B.IND\_1\_IO\_Status |

Table 319. Controller\_3.2.14.1 Analog Input (AI) (Status)

#### 15 EH\_YP0 Cerabar M

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.15.0 | UnitStatus | HwStatus |  |  |

Table 320. Controller\_3.2.15 EH\_YP0 Cerabar M (Connections)

##### 0 Main Process Value

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.15.0.0 | Input 0 | RealIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_IO | PIT03911 |
| IW2.15.0.1 | Input 1 | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_Status |  |
| IW2.15.0.2 | UnitStatus | HwStatus |  |  |

Table 321. Controller\_3.2.15.0 Main Process Value (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.15.0.0 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_IO | 0.0 | 11.0 | bar | 2 | false |
| IW2.15.0.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_Status |  |  |  |  |  |

Table 322. Controller\_3.2.15.0 Main Process Value (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.15.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_IO |
| IW2.15.0.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_Status |

Table 323. Controller\_3.2.15.0 Main Process Value (Status)

#### 16 Pressure Transmitter (9700)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.16.0 | UnitStatus | HwStatus |  |  |

Table 324. Controller\_3.2.16 Pressure Transmitter (9700) (Connections)

##### 0 Analog Input (AI)short

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.16.0.0 | Value | RealIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.IND\_5.IND\_5\_IO | PIT03919 |
| IW2.16.0.1 | Status | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.IND\_5.IND\_5\_Status |  |
| IW2.16.0.2 | UnitStatus | HwStatus |  |  |

Table 325. Controller\_3.2.16.0 Analog Input (AI)short (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.16.0.0 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.IND\_5.IND\_5\_IO | 0.0 | 10.0 | bar | 2 | false |
| IW2.16.0.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.IND\_5.IND\_5\_Status |  |  |  |  |  |

Table 326. Controller\_3.2.16.0 Analog Input (AI)short (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.16.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.IND\_5.IND\_5\_IO |
| IW2.16.0.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.IND\_5.IND\_5\_Status |

Table 327. Controller\_3.2.16.0 Analog Input (AI)short (Status)

#### 19 ABB\_TZIDC\_x10\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.19.0 | UnitStatus V3\_0\_11 | HwStatus |  |  |

Table 328. Controller\_3.2.19 ABB\_TZIDC\_x10\_PA (Connections)

##### 0 SP+READBACK+POS\_D

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.19.0.0 | READBACK Value | RealIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.HeaterValve\_IO.FB | KCV03958 |
| IW2.19.0.1 | READBACK Status | DintIO |  |  |
| IW2.19.0.2 | POS\_D Value | DintIO |  |  |
| IW2.19.0.3 | POS\_D Status | DintIO |  |  |
| QW2.19.0.4 | SETPOINT Value | RealIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.HeaterValve\_IO.SP | KCV03958 |
| QW2.19.0.5 | SETPOINT Status | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Heater\_SP\_Control | KCV03958 |
| IW2.19.0.6 | UnitStatus | HwStatus |  |  |

Table 329. Controller\_3.2.19.0 SP+READBACK+POS\_D (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.19.0.0 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.HeaterValve\_IO.FB | 0.0 | 100.0 |  | 1 | false |
| IW2.19.0.1 |  |  |  |  |  |  |
| IW2.19.0.2 |  |  |  |  |  |  |
| IW2.19.0.3 |  |  |  |  |  |  |
| QW2.19.0.4 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.HeaterValve\_IO.SP | 0.0 | 100.0 |  | 1 | false |
| QW2.19.0.5 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Heater\_SP\_Control |  |  |  |  |  |

Table 330. Controller\_3.2.19.0 SP+READBACK+POS\_D (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.19.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.HeaterValve\_IO.FB |
| IW2.19.0.1 |  |  |  |  |
| IW2.19.0.2 |  |  |  |  |
| IW2.19.0.3 |  |  |  |  |
| QW2.19.0.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.HeaterValve\_IO.SP |
| QW2.19.0.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Heater\_SP\_Control |

Table 331. Controller\_3.2.19.0 SP+READBACK+POS\_D (Status)

#### 20 ABB\_TZIDC\_x10\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.20.0 | UnitStatus V3\_0\_11 | HwStatus |  |  |

Table 332. Controller\_3.2.20 ABB\_TZIDC\_x10\_PA (Connections)

##### 0 SP+READBACK+POS\_D

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.20.0.0 | READBACK Value | RealIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.CoolerValve\_IO.FB | KCV03959 |
| IW2.20.0.1 | READBACK Status | DintIO |  |  |
| IW2.20.0.2 | POS\_D Value | DintIO |  |  |
| IW2.20.0.3 | POS\_D Status | DintIO |  |  |
| QW2.20.0.4 | SETPOINT Value | RealIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.CoolerValve\_IO.SP | KCV03959 |
| QW2.20.0.5 | SETPOINT Status | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Cooler\_SP\_Control | KCV03959 |
| IW2.20.0.6 | UnitStatus | HwStatus |  |  |

Table 333. Controller\_3.2.20.0 SP+READBACK+POS\_D (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.20.0.0 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.CoolerValve\_IO.FB | 0.0 | 100.0 |  | 1 | false |
| IW2.20.0.1 |  |  |  |  |  |  |
| IW2.20.0.2 |  |  |  |  |  |  |
| IW2.20.0.3 |  |  |  |  |  |  |
| QW2.20.0.4 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.CoolerValve\_IO.SP | 0.0 | 100.0 |  | 1 | false |
| QW2.20.0.5 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Cooler\_SP\_Control |  |  |  |  |  |

Table 334. Controller\_3.2.20.0 SP+READBACK+POS\_D (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.20.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.CoolerValve\_IO.FB |
| IW2.20.0.1 |  |  |  |  |
| IW2.20.0.2 |  |  |  |  |
| IW2.20.0.3 |  |  |  |  |
| QW2.20.0.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.CoolerValve\_IO.SP |
| QW2.20.0.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Cooler\_SP\_Control |

Table 335. Controller\_3.2.20.0 SP+READBACK+POS\_D (Status)

#### 23 YP0 FXE4000

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.23.0 | UnitStatus | HwStatus |  |  |

Table 336. Controller\_3.2.23 YP0 FXE4000 (Connections)

##### 0 AI

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.23.0.0 | Flow | RealIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_4B.IND\_4\_Flow | FIQT00238 |
| IW2.23.0.1 | Flow\_Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_4B.IND\_4\_Flow\_Status |  |
| IW2.23.0.2 | UnitStatus | HwStatus |  |  |

Table 337. Controller\_3.2.23.0 AI (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.23.0.0 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_4B.IND\_4\_Flow | 0.0 | 100.0 | m3/h | 1 | false |
| IW2.23.0.1 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_4B.IND\_4\_Flow\_Status |  |  |  |  |  |

Table 338. Controller\_3.2.23.0 AI (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.23.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_4B.IND\_4\_Flow |
| IW2.23.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_4B.IND\_4\_Flow\_Status |

Table 339. Controller\_3.2.23.0 AI (Status)

##### 1 SETTOT\_MODETOT\_TOTAL

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.23.1.0 | Total | RealIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_4B.IND\_4\_Tot |  |
| IW2.23.1.1 | Total\_Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_4B.IND\_4\_Tot\_Status |  |
| QW2.23.1.2 | Reset\_Total | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_4B.Reset\_Out |  |
| QW2.23.1.3 | Mode\_Total | DintIO |  |  |
| IW2.23.1.4 | UnitStatus | HwStatus |  |  |

Table 340. Controller\_3.2.23.1 SETTOT\_MODETOT\_TOTAL (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.23.1.0 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_4B.IND\_4\_Tot | 0.0 | 100000.0 | m3 | 1 | false |
| IW2.23.1.1 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_4B.IND\_4\_Tot\_Status |  |  |  |  |  |
| QW2.23.1.2 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_4B.Reset\_Out |  |  |  |  |  |
| QW2.23.1.3 |  |  |  |  |  |  |

Table 341. Controller\_3.2.23.1 SETTOT\_MODETOT\_TOTAL (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.23.1.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_4B.IND\_4\_Tot |
| IW2.23.1.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_4B.IND\_4\_Tot\_Status |
| QW2.23.1.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_4B.Reset\_Out |
| QW2.23.1.3 |  |  |  |  |

Table 342. Controller\_3.2.23.1 SETTOT\_MODETOT\_TOTAL (Status)

#### 24 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.24.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 343. Controller\_3.2.24 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.24.0.0 | Temp1 Value | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO | TIT01001 |
| IW2.24.0.1 | Temp1 Status | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO\_Status |  |
| IW2.24.0.2 | UnitStatus | HwStatus |  |  |

Table 344. Controller\_3.2.24.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.24.0.0 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW2.24.0.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO\_Status |  |  |  |  |  |

Table 345. Controller\_3.2.24.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.24.0.0 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO |
| IW2.24.0.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO\_Status |

Table 346. Controller\_3.2.24.0 Temperature 1 (Status)

#### 25 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.25.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 347. Controller\_3.2.25 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.25.0.0 | Temp1 Value | RealIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO | TIT04608 |
| IW2.25.0.1 | Temp1 Status | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO\_Status |  |
| IW2.25.0.2 | UnitStatus | HwStatus |  |  |

Table 348. Controller\_3.2.25.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.25.0.0 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW2.25.0.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO\_Status |  |  |  |  |  |

Table 349. Controller\_3.2.25.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.25.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO |
| IW2.25.0.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO\_Status |

Table 350. Controller\_3.2.25.0 Temperature 1 (Status)

#### 26 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.26.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 351. Controller\_3.2.26 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.26.0.0 | Temp1 Value | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_C.IND\_1\_IO | TIT01009 |
| IW2.26.0.1 | Temp1 Status | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_C.IND\_1\_IO\_Status |  |
| IW2.26.0.2 | UnitStatus | HwStatus |  |  |

Table 352. Controller\_3.2.26.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.26.0.0 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_C.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW2.26.0.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_C.IND\_1\_IO\_Status |  |  |  |  |  |

Table 353. Controller\_3.2.26.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.26.0.0 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_C.IND\_1\_IO |
| IW2.26.0.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_C.IND\_1\_IO\_Status |

Table 354. Controller\_3.2.26.0 Temperature 1 (Status)

#### 27 Pressure Transmitter (9700)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.27.0 | UnitStatus | HwStatus |  |  |

Table 355. Controller\_3.2.27 Pressure Transmitter (9700) (Connections)

##### 0 Analog Input (AI)short

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.27.0.0 | Value | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_IO | PIT01011 |
| IW2.27.0.1 | Status | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_Status |  |
| IW2.27.0.2 | UnitStatus | HwStatus |  |  |

Table 356. Controller\_3.2.27.0 Analog Input (AI)short (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.27.0.0 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_IO | 0.0 | 10.0 | bar | 2 | false |
| IW2.27.0.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_Status |  |  |  |  |  |

Table 357. Controller\_3.2.27.0 Analog Input (AI)short (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.27.0.0 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_IO |
| IW2.27.0.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_Status |

Table 358. Controller\_3.2.27.0 Analog Input (AI)short (Status)

#### 28 Pressure Transmitter (9700)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.28.0 | UnitStatus | HwStatus |  |  |

Table 359. Controller\_3.2.28 Pressure Transmitter (9700) (Connections)

##### 0 Analog Input (AI)short

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.28.0.0 | Value | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.IND\_5.IND\_5\_IO | PT01019 |
| IW2.28.0.1 | Status | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.IND\_5.IND\_5\_Status |  |
| IW2.28.0.2 | UnitStatus | HwStatus |  |  |

Table 360. Controller\_3.2.28.0 Analog Input (AI)short (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.28.0.0 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.IND\_5.IND\_5\_IO | 0.0 | 10.0 | bar | 2 | false |
| IW2.28.0.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.IND\_5.IND\_5\_Status |  |  |  |  |  |

Table 361. Controller\_3.2.28.0 Analog Input (AI)short (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.28.0.0 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.IND\_5.IND\_5\_IO |
| IW2.28.0.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.IND\_5.IND\_5\_Status |

Table 362. Controller\_3.2.28.0 Analog Input (AI)short (Status)

#### 29 ABB\_TZIDC\_x10\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.29.0 | UnitStatus V3\_0\_11 | HwStatus |  |  |

Table 363. Controller\_3.2.29 ABB\_TZIDC\_x10\_PA (Connections)

##### 0 SP+READBACK+POS\_D

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.29.0.0 | READBACK Value | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.HeaterValve\_IO.FB | KCV01058 |
| IW2.29.0.1 | READBACK Status | DintIO |  |  |
| IW2.29.0.2 | POS\_D Value | DintIO |  |  |
| IW2.29.0.3 | POS\_D Status | DintIO |  |  |
| QW2.29.0.4 | SETPOINT Value | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.HeaterValve\_IO.SP | KCV01058 |
| QW2.29.0.5 | SETPOINT Status | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Heater\_SP\_Control | KCV01058 |
| IW2.29.0.6 | UnitStatus | HwStatus |  |  |

Table 364. Controller\_3.2.29.0 SP+READBACK+POS\_D (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.29.0.0 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.HeaterValve\_IO.FB | 0.0 | 100.0 | % | 1 | false |
| IW2.29.0.1 |  |  |  |  |  |  |
| IW2.29.0.2 |  |  |  |  |  |  |
| IW2.29.0.3 |  |  |  |  |  |  |
| QW2.29.0.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.HeaterValve\_IO.SP | 0.0 | 100.0 | % | 1 | false |
| QW2.29.0.5 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Heater\_SP\_Control |  |  |  |  |  |

Table 365. Controller\_3.2.29.0 SP+READBACK+POS\_D (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.29.0.0 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.HeaterValve\_IO.FB |
| IW2.29.0.1 |  |  |  |  |
| IW2.29.0.2 |  |  |  |  |
| IW2.29.0.3 |  |  |  |  |
| QW2.29.0.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.HeaterValve\_IO.SP |
| QW2.29.0.5 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Heater\_SP\_Control |

Table 366. Controller\_3.2.29.0 SP+READBACK+POS\_D (Status)

#### 30 ABB\_TZIDC\_x10\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.30.0 | UnitStatus V3\_0\_11 | HwStatus |  |  |

Table 367. Controller\_3.2.30 ABB\_TZIDC\_x10\_PA (Connections)

##### 0 SP+READBACK+POS\_D

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.30.0.0 | READBACK Value | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.CoolerValve\_IO.FB | KCV01059 |
| IW2.30.0.1 | READBACK Status | DintIO |  |  |
| IW2.30.0.2 | POS\_D Value | DintIO |  |  |
| IW2.30.0.3 | POS\_D Status | DintIO |  |  |
| QW2.30.0.4 | SETPOINT Value | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.CoolerValve\_IO.SP | KCV01059 |
| QW2.30.0.5 | SETPOINT Status | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Cooler\_SP\_Control | KCV01059 |
| IW2.30.0.6 | UnitStatus | HwStatus |  |  |

Table 368. Controller\_3.2.30.0 SP+READBACK+POS\_D (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.30.0.0 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.CoolerValve\_IO.FB | 0.0 | 100.0 | % | 1 | false |
| IW2.30.0.1 |  |  |  |  |  |  |
| IW2.30.0.2 |  |  |  |  |  |  |
| IW2.30.0.3 |  |  |  |  |  |  |
| QW2.30.0.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.CoolerValve\_IO.SP | 0.0 | 100.0 | % | 1 | false |
| QW2.30.0.5 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Cooler\_SP\_Control |  |  |  |  |  |

Table 369. Controller\_3.2.30.0 SP+READBACK+POS\_D (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.30.0.0 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.CoolerValve\_IO.FB |
| IW2.30.0.1 |  |  |  |  |
| IW2.30.0.2 |  |  |  |  |
| IW2.30.0.3 |  |  |  |  |
| QW2.30.0.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.CoolerValve\_IO.SP |
| QW2.30.0.5 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Cooler\_SP\_Control |

Table 370. Controller\_3.2.30.0 SP+READBACK+POS\_D (Status)

#### 31 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.31.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 371. Controller\_3.2.31 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.31.0.0 | Temp1 Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO | TIT04701 |
| IW2.31.0.1 | Temp1 Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO\_Status |  |
| IW2.31.0.2 | UnitStatus | HwStatus |  |  |

Table 372. Controller\_3.2.31.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.31.0.0 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW2.31.0.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO\_Status |  |  |  |  |  |

Table 373. Controller\_3.2.31.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.31.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO |
| IW2.31.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Transmitter\_InnerTemp.IND\_1\_IO\_Status |

Table 374. Controller\_3.2.31.0 Temperature 1 (Status)

#### 32 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.32.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 375. Controller\_3.2.32 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.32.0.0 | Temp1 Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_A.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO | TIT04704 |
| IW2.32.0.1 | Temp1 Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_A.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |  |
| IW2.32.0.2 | UnitStatus | HwStatus |  |  |

Table 376. Controller\_3.2.32.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.32.0.0 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_A.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW2.32.0.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_A.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |  |  |  |  |  |

Table 377. Controller\_3.2.32.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.32.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_A.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO |
| IW2.32.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_A.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |

Table 378. Controller\_3.2.32.0 Temperature 1 (Status)

#### 33 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.33.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 379. Controller\_3.2.33 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.33.0.0 | Temp1 Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_B.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO | TIT04705 |
| IW2.33.0.1 | Temp1 Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_B.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |  |
| IW2.33.0.2 | UnitStatus | HwStatus |  |  |

Table 380. Controller\_3.2.33.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.33.0.0 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_B.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW2.33.0.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_B.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |  |  |  |  |  |

Table 381. Controller\_3.2.33.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.33.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_B.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO |
| IW2.33.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_B.CNT\_1Tax3.IND\_1ax3.IND\_1\_IO\_Status |

Table 382. Controller\_3.2.33.0 Temperature 1 (Status)

#### 34 ABB\_TTX\_300\_PA

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Status format | condensed | enum |  |  |  |
| Parameterization | DPV1 only | enum |  |  |  |
| Measurement Type (PV =) | SV1 (Sensor 1) | enum |  |  |  |
| PV unit | degF | enum |  |  |  |
| Signal Characteristic | medium | enum |  |  |  |
| Sensor 1 (LIN\_TYPE) | Pt100 (IEC751) | enum |  |  |  |
| Sensor Connect. 1 / RJ Type 1 | 3 wire / internal reference | enum |  |  |  |
| Line R 1 / Fix. RJ Val. 1 (x100) | 0 | dint |  | -32000 | 32000 |
| Sensor 2 (LIN\_TYPE\_2) | Off | enum |  |  |  |
| Sensor Connect. 2 / RJ Type 2 | 3 wire / internal reference | enum |  |  |  |
| Line R 2 / Fix. RJ Val. 2 (x100) | 0 | dint |  | -32000 | 32000 |
| HMI Language | English | enum |  |  |  |
| HMI Local operation | Enable | enum |  |  |  |
| HMI Autoscroll | Disable | enum |  |  |  |
| HMI Contrast | 50 | dint |  | 0 | 100 |
| HMI Select View1 | Calculated Value | enum |  |  |  |
| HMI Select View2 Line1 | Sensor 1 | enum |  |  |  |
| HMI Select View2 Line2 | Sensor 2 | enum |  |  |  |
| DMask1: Warmstart | Disable | enum |  |  |  |
| DMask1: Coldstart | Disable | enum |  |  |  |
| DMask1: Sensor drift | Disable | enum |  |  |  |
| DMask1: S1 line res. too high | Disable | enum |  |  |  |
| DMask1: S1 short circuit | Disable | enum |  |  |  |
| DMask1: S1 wire break | Disable | enum |  |  |  |
| DMask1: S2 line res. too high | Disable | enum |  |  |  |
| DMask1: S2 short circuit | Disable | enum |  |  |  |
| DMask1: S2 wire break | Disable | enum |  |  |  |
| DMask1: Over meas. range S1 | Disable | enum |  |  |  |
| DMask1: Under meas. range S1 | Disable | enum |  |  |  |
| DMask1: Over meas. range S2 | Disable | enum |  |  |  |
| DMask1: Under meas. range S2 | Disable | enum |  |  |  |
| DMask1: Dev.Temp outside spec. | Disable | enum |  |  |  |
| DMask1: Hardware failure | Disable | enum |  |  |  |
| DMask1: Device not calibrated | Disable | enum |  |  |  |
| DMask1: Device is simulated | Disable | enum |  |  |  |
| DMask1: Configuration failure | Disable | enum |  |  |  |
| DMask1: Red. Sensor 1+2 failure | Disable | enum |  |  |  |
| DMask1: Red. S1 short circuit | Disable | enum |  |  |  |
| DMask1: Red. S1 wire break | Disable | enum |  |  |  |
| DMask1: Red. S2 short circuit | Disable | enum |  |  |  |
| DMask1: Red. S2 wire break | Disable | enum |  |  |  |
| DMask2: Warmstart | Disable | enum |  |  |  |
| DMask2: Coldstart | Disable | enum |  |  |  |
| DMask2: Sensor drift | Disable | enum |  |  |  |
| DMask2: S1 line res. too high | Disable | enum |  |  |  |
| DMask2: S1 short circuit | Disable | enum |  |  |  |
| DMask2: S1 wire break | Disable | enum |  |  |  |
| DMask2: S2 line res. too high | Disable | enum |  |  |  |
| DMask2: S2 short circuit | Disable | enum |  |  |  |
| DMask2: S2 wire break | Disable | enum |  |  |  |
| DMask2: Over meas. range S1 | Disable | enum |  |  |  |
| DMask2: Under meas. range S1 | Disable | enum |  |  |  |
| DMask2: Over meas. range S2 | Disable | enum |  |  |  |
| DMask2: Under meas. range S2 | Disable | enum |  |  |  |
| DMask2: Dev.Temp outside spec. | Disable | enum |  |  |  |
| DMask2: Hardware failure | Disable | enum |  |  |  |
| DMask2: Device not calibrated | Disable | enum |  |  |  |
| DMask2: Device is simulated | Disable | enum |  |  |  |
| DMask2: Configuration failure | Disable | enum |  |  |  |
| DMask2: Red. Sensor 1+2 failure | Disable | enum |  |  |  |
| DMask2: Red. S1 short circuit | Disable | enum |  |  |  |
| DMask2: Red. S1 wire break | Disable | enum |  |  |  |
| DMask2: Red. S2 short circuit | Disable | enum |  |  |  |
| DMask2: Red. S2 wire break | Disable | enum |  |  |  |

Table 383. Controller\_3 (172.16.96.153).2.34 ABB\_TTX\_300\_PA (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.34.0 | UnitStatus | HwStatus |  |  |

Table 384. Controller\_3.2.34 ABB\_TTX\_300\_PA (Connections)

##### 1 Analog Input (AI)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Scale START input (PVEu0 x10) | 0 | dint |  | -32000 | 32000 |
| Scale END input (PVEu100 x10) | 1000 | dint |  | -32000 | 32000 |
| Scale START output (OUTEu0 x10) | 0 | dint |  | -32000 | 32000 |
| Scale END output (OUTEu100 x10) | 1000 | dint |  | -32000 | 32000 |
| Unit | degC | enum |  |  |  |
| Damping (PVTIME) | no | enum |  |  |  |
| Failure strategy (FSAFE\_TYPE) | Use calculated value + BAD | enum |  |  |  |
| Failsafe value (FSAFE\_VALUE x10) | 32000 | dint |  | -32000 | 32000 |
| Upper lim. alarm (HI\_HI\_LIM x10) | 32001 | dint |  | -32001 | 32001 |
| Upper lim. warning (HI\_LIM x10) | 32001 | dint |  | -32001 | 32001 |
| Lower lim. warning (LO\_LIM x10) | -32001 | dint |  | -32001 | 32001 |
| Lower lim. alarm (LO\_LO\_LIM x10) | -32001 | dint |  | -32001 | 32001 |

Table 385. Controller\_3 (172.16.96.153).2.34.1 Analog Input (AI) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.34.1.0 | Analog Input | RealIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_1A.IND\_1\_IO | TIT04709 |
| IW2.34.1.1 | Analog Input Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_1A.IND\_1\_IO\_Status |  |
| IW2.34.1.2 | UnitStatus | HwStatus |  |  |

Table 386. Controller\_3.2.34.1 Analog Input (AI) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.34.1.0 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_1A.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW2.34.1.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_1A.IND\_1\_IO\_Status |  |  |  |  |  |

Table 387. Controller\_3.2.34.1 Analog Input (AI) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.34.1.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_1A.IND\_1\_IO |
| IW2.34.1.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_1A.IND\_1\_IO\_Status |

Table 388. Controller\_3.2.34.1 Analog Input (AI) (Status)

#### 35 Pressure Transmitter (9700)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.35.0 | UnitStatus | HwStatus |  |  |

Table 389. Controller\_3.2.35 Pressure Transmitter (9700) (Connections)

##### 0 Analog Input (AI)short

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.35.0.0 | Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.CNT\_1.IND\_5.IND\_5\_IO | PIT04711 |
| IW2.35.0.1 | Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.CNT\_1.IND\_5.IND\_5\_Status |  |
| IW2.35.0.2 | UnitStatus | HwStatus |  |  |

Table 390. Controller\_3.2.35.0 Analog Input (AI)short (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.35.0.0 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.CNT\_1.IND\_5.IND\_5\_IO | 0.0 | 10.0 | bar | 2 | false |
| IW2.35.0.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.CNT\_1.IND\_5.IND\_5\_Status |  |  |  |  |  |

Table 391. Controller\_3.2.35.0 Analog Input (AI)short (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.35.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.CNT\_1.IND\_5.IND\_5\_IO |
| IW2.35.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.CNT\_1.IND\_5.IND\_5\_Status |

Table 392. Controller\_3.2.35.0 Analog Input (AI)short (Status)

#### 36 Pressure Transmitter (9700)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.36.0 | UnitStatus | HwStatus |  |  |

Table 393. Controller\_3.2.36 Pressure Transmitter (9700) (Connections)

##### 0 Analog Input (AI)short

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.36.0.0 | Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.IND\_5.IND\_5\_IO | PT04719 |
| IW2.36.0.1 | Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.IND\_5.IND\_5\_Status |  |
| IW2.36.0.2 | UnitStatus | HwStatus |  |  |

Table 394. Controller\_3.2.36.0 Analog Input (AI)short (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.36.0.0 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.IND\_5.IND\_5\_IO | 0.0 | 10.0 | bar | 1 | false |
| IW2.36.0.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.IND\_5.IND\_5\_Status |  |  |  |  |  |

Table 395. Controller\_3.2.36.0 Analog Input (AI)short (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.36.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.IND\_5.IND\_5\_IO |
| IW2.36.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.IND\_5.IND\_5\_Status |

Table 396. Controller\_3.2.36.0 Analog Input (AI)short (Status)

#### 39 ABB\_TZIDC\_x10\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.39.0 | UnitStatus V3\_0\_11 | HwStatus |  |  |

Table 397. Controller\_3.2.39 ABB\_TZIDC\_x10\_PA (Connections)

##### 0 SP+READBACK+POS\_D

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.39.0.0 | READBACK Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.HeaterValve\_IO.FB | KCV04758 |
| IW2.39.0.1 | READBACK Status | DintIO |  |  |
| IW2.39.0.2 | POS\_D Value | DintIO |  |  |
| IW2.39.0.3 | POS\_D Status | DintIO |  |  |
| QW2.39.0.4 | SETPOINT Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.HeaterValve\_IO.SP | KCV04758 |
| QW2.39.0.5 | SETPOINT Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Heater\_SP\_Control | KCV04758 |
| IW2.39.0.6 | UnitStatus | HwStatus |  |  |

Table 398. Controller\_3.2.39.0 SP+READBACK+POS\_D (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.39.0.0 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.HeaterValve\_IO.FB | 0.0 | 100.0 | % | 1 | false |
| IW2.39.0.1 |  |  |  |  |  |  |
| IW2.39.0.2 |  |  |  |  |  |  |
| IW2.39.0.3 |  |  |  |  |  |  |
| QW2.39.0.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.HeaterValve\_IO.SP | 0.0 | 100.0 | % | 1 | false |
| QW2.39.0.5 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Heater\_SP\_Control |  |  |  |  |  |

Table 399. Controller\_3.2.39.0 SP+READBACK+POS\_D (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.39.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.HeaterValve\_IO.FB |
| IW2.39.0.1 |  |  |  |  |
| IW2.39.0.2 |  |  |  |  |
| IW2.39.0.3 |  |  |  |  |
| QW2.39.0.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.HeaterValve\_IO.SP |
| QW2.39.0.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Heater\_SP\_Control |

Table 400. Controller\_3.2.39.0 SP+READBACK+POS\_D (Status)

#### 40 ABB\_TZIDC\_x10\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.40.0 | UnitStatus V3\_0\_11 | HwStatus |  |  |

Table 401. Controller\_3.2.40 ABB\_TZIDC\_x10\_PA (Connections)

##### 0 SP+READBACK+POS\_D

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.40.0.0 | READBACK Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.CoolerValve\_IO.FB | KCV04759 |
| IW2.40.0.1 | READBACK Status | DintIO |  |  |
| IW2.40.0.2 | POS\_D Value | DintIO |  |  |
| IW2.40.0.3 | POS\_D Status | DintIO |  |  |
| QW2.40.0.4 | SETPOINT Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.CoolerValve\_IO.SP | KCV04759 |
| QW2.40.0.5 | SETPOINT Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Cooler\_SP\_Control | KCV04759 |
| IW2.40.0.6 | UnitStatus | HwStatus |  |  |

Table 402. Controller\_3.2.40.0 SP+READBACK+POS\_D (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.40.0.0 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.CoolerValve\_IO.FB | 0.0 | 100.0 | % | 1 | false |
| IW2.40.0.1 |  |  |  |  |  |  |
| IW2.40.0.2 |  |  |  |  |  |  |
| IW2.40.0.3 |  |  |  |  |  |  |
| QW2.40.0.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.CoolerValve\_IO.SP | 0.0 | 100.0 | % | 1 | false |
| QW2.40.0.5 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Cooler\_SP\_Control |  |  |  |  |  |

Table 403. Controller\_3.2.40.0 SP+READBACK+POS\_D (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.40.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.CoolerValve\_IO.FB |
| IW2.40.0.1 |  |  |  |  |
| IW2.40.0.2 |  |  |  |  |
| IW2.40.0.3 |  |  |  |  |
| QW2.40.0.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.CoolerValve\_IO.SP |
| QW2.40.0.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Cooler\_SP\_Control |

Table 404. Controller\_3.2.40.0 SP+READBACK+POS\_D (Status)

#### 41 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.41.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 405. Controller\_3.2.41 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.41.0.0 | Temp1 Value | RealIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.IND\_1ax3.IND\_1\_IO | TIT04605 |
| IW2.41.0.1 | Temp1 Status | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.IND\_1ax3.IND\_1\_IO\_Status |  |
| IW2.41.0.2 | UnitStatus | HwStatus |  |  |

Table 406. Controller\_3.2.41.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.41.0.0 | Cikloprpil\_D6\_laktol.U\_14C46\_00.IND\_1ax3.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW2.41.0.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.IND\_1ax3.IND\_1\_IO\_Status |  |  |  |  |  |

Table 407. Controller\_3.2.41.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.41.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.IND\_1ax3.IND\_1\_IO |
| IW2.41.0.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.IND\_1ax3.IND\_1\_IO\_Status |

Table 408. Controller\_3.2.41.0 Temperature 1 (Status)

#### 42 YP0 Micropilot M

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.42.0 | UnitStatus | HwStatus |  |  |

Table 409. Controller\_3.2.42 YP0 Micropilot M (Connections)

##### 0 Main Process Value

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.42.0.0 | Process Value | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_2Bax3.IND\_2\_IO | LIT01021 |
| IW2.42.0.1 | Status | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_2Bax3.IND\_2\_IO\_Status |  |
| IW2.42.0.2 | UnitStatus | HwStatus |  |  |

Table 410. Controller\_3.2.42.0 Main Process Value (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.42.0.0 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_2Bax3.IND\_2\_IO | 0.0 | 5000.0 | Liter | 1 | false |
| IW2.42.0.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_2Bax3.IND\_2\_IO\_Status |  |  |  |  |  |

Table 411. Controller\_3.2.42.0 Main Process Value (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.42.0.0 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_2Bax3.IND\_2\_IO |
| IW2.42.0.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_2Bax3.IND\_2\_IO\_Status |

Table 412. Controller\_3.2.42.0 Main Process Value (Status)

#### 43 YP0 Micropilot M

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.43.0 | UnitStatus | HwStatus |  |  |

Table 413. Controller\_3.2.43 YP0 Micropilot M (Connections)

##### 0 Main Process Value

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.43.0.0 | Process Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_2Bax3.IND\_2\_IO | LIT00221 |
| IW2.43.0.1 | Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_2Bax3.IND\_2\_IO\_Status |  |
| IW2.43.0.2 | UnitStatus | HwStatus |  |  |

Table 414. Controller\_3.2.43.0 Main Process Value (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.43.0.0 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_2Bax3.IND\_2\_IO | 0.0 | 3500.0 | l | 1 | false |
| IW2.43.0.1 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_2Bax3.IND\_2\_IO\_Status |  |  |  |  |  |

Table 415. Controller\_3.2.43.0 Main Process Value (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.43.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_2Bax3.IND\_2\_IO |
| IW2.43.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.IND\_2Bax3.IND\_2\_IO\_Status |

Table 416. Controller\_3.2.43.0 Main Process Value (Status)

#### 81 S900

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.81.0 | UnitStatus | HwStatus |  |  |

Table 417. Controller\_3.2.81 S900 (Connections)

##### 0 CI920\* (CIPB)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Grid Frequency | 50 Hz | enum |  |  |  |
| Analog Status | on | enum |  |  |  |
| Internal Bus Redundancy | false | bool |  |  |  |
| CIPB Redundancy | false | bool |  |  |  |
| Power Supply Redundancy | false | bool |  |  |  |

Table 418. Controller\_3 (172.16.96.153).2.81.0 CI920\* (CIPB) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.81.0.1 | UnitStatus | HwStatus |  |  |

Table 419. Controller\_3.2.81.0 CI920\* (CIPB) (Connections)

##### 1 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 420. Controller\_3 (172.16.96.153).2.81.1 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.81.1.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_B.GCS\_1B.GCS\_1\_IO.Cmd | KSVG03925 |
| QX2.81.1.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM12Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03936 |
| QX2.81.1.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Dax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03953 |
| QX2.81.1.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM21Aax3\_A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03956 |
| QW2.81.1.5 | All outputs | DwordIO |  |  |
| IW2.81.1.6 | UnitStatus | HwStatus |  |  |

Table 421. Controller\_3.2.81.1 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.81.1.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_B.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.1.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM12Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.1.3 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Dax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.1.4 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM21Aax3\_A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.81.1.5 |  |  |  |  |  |  |

Table 422. Controller\_3.2.81.1 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.81.1.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_B.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.81.1.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM12Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.81.1.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Dax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.81.1.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM21Aax3\_A.GCS\_1A.GCS\_1\_IO.Cmd |
| QW2.81.1.5 |  |  |  |  |

Table 423. Controller\_3.2.81.1 DO910\* (DO4) (Status)

##### 2 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 424. Controller\_3 (172.16.96.153).2.81.2 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.81.2.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM21Aax3\_B.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03957 |
| QX2.81.2.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd | KSVG03962 |
| QX2.81.2.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG03963 |
| QX2.81.2.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03965 |
| QW2.81.2.5 | All outputs | DwordIO |  |  |
| IW2.81.2.6 | UnitStatus | HwStatus |  |  |

Table 425. Controller\_3.2.81.2 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.81.2.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM21Aax3\_B.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.2.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.2.3 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.2.4 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.81.2.5 |  |  |  |  |  |  |

Table 426. Controller\_3.2.81.2 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.81.2.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM21Aax3\_B.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.81.2.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |
| QX2.81.2.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.81.2.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_A.GCS\_1A.GCS\_1\_IO.Cmd |
| QW2.81.2.5 |  |  |  |  |

Table 427. Controller\_3.2.81.2 DO910\* (DO4) (Status)

##### 3 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 428. Controller\_3 (172.16.96.153).2.81.3 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.81.3.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_B.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03966 |
| QX2.81.3.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_A.GCS\_1B.GCS\_1\_IO.Cmd | KSVG03967 |
| QX2.81.3.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03969 |
| QX2.81.3.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1B.GCS\_1\_IO.Cmd | KSVG03972 |
| QW2.81.3.5 | All outputs | DwordIO |  |  |
| IW2.81.3.6 | UnitStatus | HwStatus |  |  |

Table 429. Controller\_3.2.81.3 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.81.3.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_B.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.3.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_A.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.3.3 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.3.4 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.81.3.5 |  |  |  |  |  |  |

Table 430. Controller\_3.2.81.3 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.81.3.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_B.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.81.3.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_A.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.81.3.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.81.3.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1B.GCS\_1\_IO.Cmd |
| QW2.81.3.5 |  |  |  |  |

Table 431. Controller\_3.2.81.3 DO910\* (DO4) (Status)

##### 4 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 432. Controller\_3 (172.16.96.153).2.81.4 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.81.4.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03973 |
| QX2.81.4.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1C.GCS\_1\_IO.Cmd | KSVG03974 |
| QX2.81.4.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_A.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd | KSVG03975 |
| QX2.81.4.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03982 |
| QW2.81.4.5 | All outputs | DwordIO |  |  |
| IW2.81.4.6 | UnitStatus | HwStatus |  |  |

Table 433. Controller\_3.2.81.4 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.81.4.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.4.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.4.3 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_A.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.4.4 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.81.4.5 |  |  |  |  |  |  |

Table 434. Controller\_3.2.81.4 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.81.4.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.81.4.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1C.GCS\_1\_IO.Cmd |
| QX2.81.4.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_A.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd |
| QX2.81.4.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1A.GCS\_1\_IO.Cmd |
| QW2.81.4.5 |  |  |  |  |

Table 435. Controller\_3.2.81.4 DO910\* (DO4) (Status)

##### 5 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 436. Controller\_3 (172.16.96.153).2.81.5 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.81.5.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1B.GCS\_1\_IO.Cmd | KSVG03983 |
| QX2.81.5.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1C.GCS\_1\_IO.Cmd | KSVG03984 |
| QX2.81.5.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_B.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd | KSVG03985 |
| QX2.81.5.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03988 |
| QW2.81.5.5 | All outputs | DwordIO |  |  |
| IW2.81.5.6 | UnitStatus | HwStatus |  |  |

Table 437. Controller\_3.2.81.5 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.81.5.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.5.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.5.3 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_B.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.5.4 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.81.5.5 |  |  |  |  |  |  |

Table 438. Controller\_3.2.81.5 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.81.5.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.81.5.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1C.GCS\_1\_IO.Cmd |
| QX2.81.5.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_B.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd |
| QX2.81.5.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QW2.81.5.5 |  |  |  |  |

Table 439. Controller\_3.2.81.5 DO910\* (DO4) (Status)

##### 6 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 440. Controller\_3 (172.16.96.153).2.81.6 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.81.6.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG03989 |
| QX2.81.6.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1C.GCS\_1\_IO.Cmd | KSVG03990 |
| QX2.81.6.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1D.GCS\_1\_IO.Cmd | KSVG03991 |
| QX2.81.6.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1E.GCS\_1\_IO.Cmd | KSVG03992 |
| QW2.81.6.5 | All outputs | DwordIO |  |  |
| IW2.81.6.6 | UnitStatus | HwStatus |  |  |

Table 441. Controller\_3.2.81.6 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.81.6.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.6.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.6.3 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1D.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.6.4 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1E.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.81.6.5 |  |  |  |  |  |  |

Table 442. Controller\_3.2.81.6 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.81.6.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.81.6.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1C.GCS\_1\_IO.Cmd |
| QX2.81.6.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1D.GCS\_1\_IO.Cmd |
| QX2.81.6.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1E.GCS\_1\_IO.Cmd |
| QW2.81.6.5 |  |  |  |  |

Table 443. Controller\_3.2.81.6 DO910\* (DO4) (Status)

##### 7 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 444. Controller\_3 (172.16.96.153).2.81.7 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.81.7.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1F.GCS\_1\_IO.Cmd | KSVG03993 |
| QX2.81.7.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1G.GCS\_1\_IO.Cmd | KSVG03994 |
| QX2.81.7.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1H.GCS\_1\_IO.Cmd | KSVG03995 |
| QX2.81.7.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1I.GCS\_1\_IO.Cmd | KSVG03996 |
| QW2.81.7.5 | All outputs | DwordIO |  |  |
| IW2.81.7.6 | UnitStatus | HwStatus |  |  |

Table 445. Controller\_3.2.81.7 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.81.7.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1F.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.7.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1G.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.7.3 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1H.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.7.4 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1I.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.81.7.5 |  |  |  |  |  |  |

Table 446. Controller\_3.2.81.7 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.81.7.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1F.GCS\_1\_IO.Cmd |
| QX2.81.7.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1G.GCS\_1\_IO.Cmd |
| QX2.81.7.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1H.GCS\_1\_IO.Cmd |
| QX2.81.7.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1I.GCS\_1\_IO.Cmd |
| QW2.81.7.5 |  |  |  |  |

Table 447. Controller\_3.2.81.7 DO910\* (DO4) (Status)

##### 8 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 448. Controller\_3 (172.16.96.153).2.81.8 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.81.8.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1J.GCS\_1\_IO.Cmd | KSVG03997 |
| QX2.81.8.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1K.GCS\_1\_IO.Cmd | KSVG03998 |
| QX2.81.8.3 | Output 3 | BoolIO | Ellato\_3.KSVG06781\_Cmd | KSVG06781 |
| QX2.81.8.4 | Output 4 | BoolIO | Ellato\_3.KSVG06876\_Cmd | KSVG06876 |
| QW2.81.8.5 | All outputs | DwordIO |  |  |
| IW2.81.8.6 | UnitStatus | HwStatus |  |  |

Table 449. Controller\_3.2.81.8 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.81.8.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1J.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.8.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1K.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.8.3 | Ellato\_3.KSVG06781\_Cmd |  |  |  |  | false |
| QX2.81.8.4 | Ellato\_3.KSVG06876\_Cmd |  |  |  |  | false |
| QW2.81.8.5 |  |  |  |  |  |  |

Table 450. Controller\_3.2.81.8 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.81.8.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1J.GCS\_1\_IO.Cmd |
| QX2.81.8.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1K.GCS\_1\_IO.Cmd |
| QX2.81.8.3 |  |  |  | Ellato\_3.KSVG06781\_Cmd |
| QX2.81.8.4 |  |  |  | Ellato\_3.KSVG06876\_Cmd |
| QW2.81.8.5 |  |  |  |  |

Table 451. Controller\_3.2.81.8 DO910\* (DO4) (Status)

##### 9 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 452. Controller\_3 (172.16.96.153).2.81.9 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.81.9.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Cax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03951 |
| QX2.81.9.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Cax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG03952 |
| QX2.81.9.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Aax31.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03926 |
| QX2.81.9.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Aax32.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03950 |
| QW2.81.9.5 | All outputs | DwordIO |  |  |
| IW2.81.9.6 | UnitStatus | HwStatus |  |  |

Table 453. Controller\_3.2.81.9 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.81.9.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Cax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.9.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Cax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.9.3 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Aax31.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.81.9.4 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Aax32.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.81.9.5 |  |  |  |  |  |  |

Table 454. Controller\_3.2.81.9 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.81.9.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Cax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.81.9.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Cax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.81.9.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Aax31.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.81.9.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Aax32.GCS\_1A.GCS\_1\_IO.Cmd |
| QW2.81.9.5 |  |  |  |  |

Table 455. Controller\_3.2.81.9 DO910\* (DO4) (Status)

##### 11 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 456. Controller\_3 (172.16.96.153).2.81.11 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.81.11.1 | Input 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_B.GCS\_1B.GCS\_1\_IO.FB0 | KSVG03925 |
| IX2.81.11.2 | Input 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM12Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03936 |
| IX2.81.11.3 | Input 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Dax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03953 |
| IX2.81.11.4 | Input 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM21Aax3\_A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03956 |
| IX2.81.11.5 | Input 5 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM21Aax3\_B.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03957 |
| IX2.81.11.6 | Input 6 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 | KSVG03962 |
| IX2.81.11.7 | Input 7 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG03963 |
| IX2.81.11.8 | Input 8 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03965 |
| IW2.81.11.9 | All inputs | DwordIO |  |  |
| QX2.81.11.10 | Output 1 | BoolIO |  |  |
| QX2.81.11.11 | Output 2 | BoolIO |  |  |
| QX2.81.11.12 | Output 3 | BoolIO |  |  |
| QX2.81.11.13 | Output 4 | BoolIO |  |  |
| QX2.81.11.14 | Output 5 | BoolIO |  |  |
| QX2.81.11.15 | Output 6 | BoolIO |  |  |
| QX2.81.11.16 | Output 7 | BoolIO |  |  |
| QX2.81.11.17 | Output 8 | BoolIO |  |  |
| QW2.81.11.18 | All outputs | DwordIO |  |  |
| IX2.81.11.19 | Status Input 1 | BoolIO |  |  |
| IX2.81.11.20 | Status Input 2 | BoolIO |  |  |
| IX2.81.11.21 | Status Input 3 | BoolIO |  |  |
| IX2.81.11.22 | Status Input 4 | BoolIO |  |  |
| IX2.81.11.23 | Status Input 5 | BoolIO |  |  |
| IX2.81.11.24 | Status Input 6 | BoolIO |  |  |
| IX2.81.11.25 | Status Input 7 | BoolIO |  |  |
| IX2.81.11.26 | Status Input 8 | BoolIO |  |  |
| IW2.81.11.27 | UnitStatus | HwStatus |  |  |

Table 457. Controller\_3.2.81.11 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.81.11.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_B.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.11.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM12Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.11.3 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Dax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.11.4 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM21Aax3\_A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.11.5 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM21Aax3\_B.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.11.6 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.11.7 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.11.8 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW2.81.11.9 |  |  |  |  |  |  |
| QX2.81.11.10 |  |  |  |  |  | false |
| QX2.81.11.11 |  |  |  |  |  | false |
| QX2.81.11.12 |  |  |  |  |  | false |
| QX2.81.11.13 |  |  |  |  |  | false |
| QX2.81.11.14 |  |  |  |  |  | false |
| QX2.81.11.15 |  |  |  |  |  | false |
| QX2.81.11.16 |  |  |  |  |  | false |
| QX2.81.11.17 |  |  |  |  |  | false |
| QW2.81.11.18 |  |  |  |  |  |  |
| IX2.81.11.19 |  |  |  |  |  | false |
| IX2.81.11.20 |  |  |  |  |  | false |
| IX2.81.11.21 |  |  |  |  |  | false |
| IX2.81.11.22 |  |  |  |  |  | false |
| IX2.81.11.23 |  |  |  |  |  | false |
| IX2.81.11.24 |  |  |  |  |  | false |
| IX2.81.11.25 |  |  |  |  |  | false |
| IX2.81.11.26 |  |  |  |  |  | false |

Table 458. Controller\_3.2.81.11 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.81.11.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_B.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.81.11.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM12Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.81.11.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Dax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.81.11.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM21Aax3\_A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.81.11.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM21Aax3\_B.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.81.11.6 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |
| IX2.81.11.7 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.81.11.8 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_A.GCS\_1A.GCS\_1\_IO.FB0 |
| IW2.81.11.9 |  |  |  |  |
| QX2.81.11.10 |  |  |  |  |
| QX2.81.11.11 |  |  |  |  |
| QX2.81.11.12 |  |  |  |  |
| QX2.81.11.13 |  |  |  |  |
| QX2.81.11.14 |  |  |  |  |
| QX2.81.11.15 |  |  |  |  |
| QX2.81.11.16 |  |  |  |  |
| QX2.81.11.17 |  |  |  |  |
| QW2.81.11.18 |  |  |  |  |
| IX2.81.11.19 |  |  |  |  |
| IX2.81.11.20 |  |  |  |  |
| IX2.81.11.21 |  |  |  |  |
| IX2.81.11.22 |  |  |  |  |
| IX2.81.11.23 |  |  |  |  |
| IX2.81.11.24 |  |  |  |  |
| IX2.81.11.25 |  |  |  |  |
| IX2.81.11.26 |  |  |  |  |

Table 459. Controller\_3.2.81.11 DX910\* (DIO8 S) (Status)

##### 12 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 460. Controller\_3 (172.16.96.153).2.81.12 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.81.12.1 | Input 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_B.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03966 |
| IX2.81.12.2 | Input 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_A.GCS\_1B.GCS\_1\_IO.FB0 | KSVG03967 |
| IX2.81.12.3 | Input 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03969 |
| IX2.81.12.4 | Input 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1B.GCS\_1\_IO.FB0 | KSVG03972 |
| IX2.81.12.5 | Input 5 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03973 |
| IX2.81.12.6 | Input 6 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1C.GCS\_1\_IO.FB0 | KSVG03974 |
| IX2.81.12.7 | Input 7 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_A.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 | KSVG03975 |
| IX2.81.12.8 | Input 8 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03982 |
| IW2.81.12.9 | All inputs | DwordIO |  |  |
| QX2.81.12.10 | Output 1 | BoolIO |  |  |
| QX2.81.12.11 | Output 2 | BoolIO |  |  |
| QX2.81.12.12 | Output 3 | BoolIO |  |  |
| QX2.81.12.13 | Output 4 | BoolIO |  |  |
| QX2.81.12.14 | Output 5 | BoolIO |  |  |
| QX2.81.12.15 | Output 6 | BoolIO |  |  |
| QX2.81.12.16 | Output 7 | BoolIO |  |  |
| QX2.81.12.17 | Output 8 | BoolIO |  |  |
| QW2.81.12.18 | All outputs | DwordIO |  |  |
| IX2.81.12.19 | Status Input 1 | BoolIO |  |  |
| IX2.81.12.20 | Status Input 2 | BoolIO |  |  |
| IX2.81.12.21 | Status Input 3 | BoolIO |  |  |
| IX2.81.12.22 | Status Input 4 | BoolIO |  |  |
| IX2.81.12.23 | Status Input 5 | BoolIO |  |  |
| IX2.81.12.24 | Status Input 6 | BoolIO |  |  |
| IX2.81.12.25 | Status Input 7 | BoolIO |  |  |
| IX2.81.12.26 | Status Input 8 | BoolIO |  |  |
| IW2.81.12.27 | UnitStatus | HwStatus |  |  |

Table 461. Controller\_3.2.81.12 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.81.12.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_B.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.12.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_A.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.12.3 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.12.4 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.12.5 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.12.6 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.12.7 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_A.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.12.8 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW2.81.12.9 |  |  |  |  |  |  |
| QX2.81.12.10 |  |  |  |  |  | false |
| QX2.81.12.11 |  |  |  |  |  | false |
| QX2.81.12.12 |  |  |  |  |  | false |
| QX2.81.12.13 |  |  |  |  |  | false |
| QX2.81.12.14 |  |  |  |  |  | false |
| QX2.81.12.15 |  |  |  |  |  | false |
| QX2.81.12.16 |  |  |  |  |  | false |
| QX2.81.12.17 |  |  |  |  |  | false |
| QW2.81.12.18 |  |  |  |  |  |  |
| IX2.81.12.19 |  |  |  |  |  | false |
| IX2.81.12.20 |  |  |  |  |  | false |
| IX2.81.12.21 |  |  |  |  |  | false |
| IX2.81.12.22 |  |  |  |  |  | false |
| IX2.81.12.23 |  |  |  |  |  | false |
| IX2.81.12.24 |  |  |  |  |  | false |
| IX2.81.12.25 |  |  |  |  |  | false |
| IX2.81.12.26 |  |  |  |  |  | false |

Table 462. Controller\_3.2.81.12 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.81.12.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_B.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.81.12.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Bax3\_A.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.81.12.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.81.12.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.81.12.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.81.12.6 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_A.GCS\_1C.GCS\_1\_IO.FB0 |
| IX2.81.12.7 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_A.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 |
| IX2.81.12.8 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1A.GCS\_1\_IO.FB0 |
| IW2.81.12.9 |  |  |  |  |
| QX2.81.12.10 |  |  |  |  |
| QX2.81.12.11 |  |  |  |  |
| QX2.81.12.12 |  |  |  |  |
| QX2.81.12.13 |  |  |  |  |
| QX2.81.12.14 |  |  |  |  |
| QX2.81.12.15 |  |  |  |  |
| QX2.81.12.16 |  |  |  |  |
| QX2.81.12.17 |  |  |  |  |
| QW2.81.12.18 |  |  |  |  |
| IX2.81.12.19 |  |  |  |  |
| IX2.81.12.20 |  |  |  |  |
| IX2.81.12.21 |  |  |  |  |
| IX2.81.12.22 |  |  |  |  |
| IX2.81.12.23 |  |  |  |  |
| IX2.81.12.24 |  |  |  |  |
| IX2.81.12.25 |  |  |  |  |
| IX2.81.12.26 |  |  |  |  |

Table 463. Controller\_3.2.81.12 DX910\* (DIO8 S) (Status)

##### 13 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 464. Controller\_3 (172.16.96.153).2.81.13 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.81.13.1 | Input 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1B.GCS\_1\_IO.FB0 | KSVG03983 |
| IX2.81.13.2 | Input 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1C.GCS\_1\_IO.FB0 | KSVG03984 |
| IX2.81.13.3 | Input 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_B.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 | KSVG03985 |
| IX2.81.13.4 | Input 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03988 |
| IX2.81.13.5 | Input 5 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG03989 |
| IX2.81.13.6 | Input 6 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1C.GCS\_1\_IO.FB0 | KSVG03990 |
| IX2.81.13.7 | Input 7 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1D.GCS\_1\_IO.FB0 | KSVG03991 |
| IX2.81.13.8 | Input 8 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1E.GCS\_1\_IO.FB0 | KSVG03992 |
| IW2.81.13.9 | All inputs | DwordIO |  |  |
| QX2.81.13.10 | Output 1 | BoolIO |  |  |
| QX2.81.13.11 | Output 2 | BoolIO |  |  |
| QX2.81.13.12 | Output 3 | BoolIO |  |  |
| QX2.81.13.13 | Output 4 | BoolIO |  |  |
| QX2.81.13.14 | Output 5 | BoolIO |  |  |
| QX2.81.13.15 | Output 6 | BoolIO |  |  |
| QX2.81.13.16 | Output 7 | BoolIO |  |  |
| QX2.81.13.17 | Output 8 | BoolIO |  |  |
| QW2.81.13.18 | All outputs | DwordIO |  |  |
| IX2.81.13.19 | Status Input 1 | BoolIO |  |  |
| IX2.81.13.20 | Status Input 2 | BoolIO |  |  |
| IX2.81.13.21 | Status Input 3 | BoolIO |  |  |
| IX2.81.13.22 | Status Input 4 | BoolIO |  |  |
| IX2.81.13.23 | Status Input 5 | BoolIO |  |  |
| IX2.81.13.24 | Status Input 6 | BoolIO |  |  |
| IX2.81.13.25 | Status Input 7 | BoolIO |  |  |
| IX2.81.13.26 | Status Input 8 | BoolIO |  |  |
| IW2.81.13.27 | UnitStatus | HwStatus |  |  |

Table 465. Controller\_3.2.81.13 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.81.13.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.13.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.13.3 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_B.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.13.4 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.13.5 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.13.6 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.13.7 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1D.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.13.8 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1E.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW2.81.13.9 |  |  |  |  |  |  |
| QX2.81.13.10 |  |  |  |  |  | false |
| QX2.81.13.11 |  |  |  |  |  | false |
| QX2.81.13.12 |  |  |  |  |  | false |
| QX2.81.13.13 |  |  |  |  |  | false |
| QX2.81.13.14 |  |  |  |  |  | false |
| QX2.81.13.15 |  |  |  |  |  | false |
| QX2.81.13.16 |  |  |  |  |  | false |
| QX2.81.13.17 |  |  |  |  |  | false |
| QW2.81.13.18 |  |  |  |  |  |  |
| IX2.81.13.19 |  |  |  |  |  | false |
| IX2.81.13.20 |  |  |  |  |  | false |
| IX2.81.13.21 |  |  |  |  |  | false |
| IX2.81.13.22 |  |  |  |  |  | false |
| IX2.81.13.23 |  |  |  |  |  | false |
| IX2.81.13.24 |  |  |  |  |  | false |
| IX2.81.13.25 |  |  |  |  |  | false |
| IX2.81.13.26 |  |  |  |  |  | false |

Table 466. Controller\_3.2.81.13 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.81.13.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.81.13.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM22Bax3\_B.GCS\_1C.GCS\_1\_IO.FB0 |
| IX2.81.13.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM27ax3\_B.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 |
| IX2.81.13.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.81.13.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.81.13.6 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1C.GCS\_1\_IO.FB0 |
| IX2.81.13.7 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1D.GCS\_1\_IO.FB0 |
| IX2.81.13.8 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1E.GCS\_1\_IO.FB0 |
| IW2.81.13.9 |  |  |  |  |
| QX2.81.13.10 |  |  |  |  |
| QX2.81.13.11 |  |  |  |  |
| QX2.81.13.12 |  |  |  |  |
| QX2.81.13.13 |  |  |  |  |
| QX2.81.13.14 |  |  |  |  |
| QX2.81.13.15 |  |  |  |  |
| QX2.81.13.16 |  |  |  |  |
| QX2.81.13.17 |  |  |  |  |
| QW2.81.13.18 |  |  |  |  |
| IX2.81.13.19 |  |  |  |  |
| IX2.81.13.20 |  |  |  |  |
| IX2.81.13.21 |  |  |  |  |
| IX2.81.13.22 |  |  |  |  |
| IX2.81.13.23 |  |  |  |  |
| IX2.81.13.24 |  |  |  |  |
| IX2.81.13.25 |  |  |  |  |
| IX2.81.13.26 |  |  |  |  |

Table 467. Controller\_3.2.81.13 DX910\* (DIO8 S) (Status)

##### 14 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 468. Controller\_3 (172.16.96.153).2.81.14 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.81.14.1 | Input 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1F.GCS\_1\_IO.FB0 | KSVG03993 |
| IX2.81.14.2 | Input 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1G.GCS\_1\_IO.FB0 | KSVG03994 |
| IX2.81.14.3 | Input 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1H.GCS\_1\_IO.FB0 | KSVG03995 |
| IX2.81.14.4 | Input 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1I.GCS\_1\_IO.FB0 | KSVG03996 |
| IX2.81.14.5 | Input 5 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1J.GCS\_1\_IO.FB0 | KSVG03997 |
| IX2.81.14.6 | Input 6 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1K.GCS\_1\_IO.FB0 | KSVG03998 |
| IX2.81.14.7 | Input 7 | BoolIO | Ellato\_3.KSVG06781\_FB0 | KSVG06781 |
| IX2.81.14.8 | Input 8 | BoolIO | Ellato\_3.KSVG06876\_FB0 | KSVG06876 |
| IW2.81.14.9 | All inputs | DwordIO |  |  |
| QX2.81.14.10 | Output 1 | BoolIO |  |  |
| QX2.81.14.11 | Output 2 | BoolIO |  |  |
| QX2.81.14.12 | Output 3 | BoolIO |  |  |
| QX2.81.14.13 | Output 4 | BoolIO |  |  |
| QX2.81.14.14 | Output 5 | BoolIO |  |  |
| QX2.81.14.15 | Output 6 | BoolIO |  |  |
| QX2.81.14.16 | Output 7 | BoolIO |  |  |
| QX2.81.14.17 | Output 8 | BoolIO |  |  |
| QW2.81.14.18 | All outputs | DwordIO |  |  |
| IX2.81.14.19 | Status Input 1 | BoolIO |  |  |
| IX2.81.14.20 | Status Input 2 | BoolIO |  |  |
| IX2.81.14.21 | Status Input 3 | BoolIO |  |  |
| IX2.81.14.22 | Status Input 4 | BoolIO |  |  |
| IX2.81.14.23 | Status Input 5 | BoolIO |  |  |
| IX2.81.14.24 | Status Input 6 | BoolIO |  |  |
| IX2.81.14.25 | Status Input 7 | BoolIO |  |  |
| IX2.81.14.26 | Status Input 8 | BoolIO |  |  |
| IW2.81.14.27 | UnitStatus | HwStatus |  |  |

Table 469. Controller\_3.2.81.14 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.81.14.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1F.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.14.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1G.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.14.3 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1H.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.14.4 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1I.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.14.5 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1J.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.14.6 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1K.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.14.7 | Ellato\_3.KSVG06781\_FB0 |  |  |  |  | false |
| IX2.81.14.8 | Ellato\_3.KSVG06876\_FB0 |  |  |  |  | false |
| QW2.81.14.9 |  |  |  |  |  |  |
| QX2.81.14.10 |  |  |  |  |  | false |
| QX2.81.14.11 |  |  |  |  |  | false |
| QX2.81.14.12 |  |  |  |  |  | false |
| QX2.81.14.13 |  |  |  |  |  | false |
| QX2.81.14.14 |  |  |  |  |  | false |
| QX2.81.14.15 |  |  |  |  |  | false |
| QX2.81.14.16 |  |  |  |  |  | false |
| QX2.81.14.17 |  |  |  |  |  | false |
| QW2.81.14.18 |  |  |  |  |  |  |
| IX2.81.14.19 |  |  |  |  |  | false |
| IX2.81.14.20 |  |  |  |  |  | false |
| IX2.81.14.21 |  |  |  |  |  | false |
| IX2.81.14.22 |  |  |  |  |  | false |
| IX2.81.14.23 |  |  |  |  |  | false |
| IX2.81.14.24 |  |  |  |  |  | false |
| IX2.81.14.25 |  |  |  |  |  | false |
| IX2.81.14.26 |  |  |  |  |  | false |

Table 470. Controller\_3.2.81.14 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.81.14.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1F.GCS\_1\_IO.FB0 |
| IX2.81.14.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1G.GCS\_1\_IO.FB0 |
| IX2.81.14.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1H.GCS\_1\_IO.FB0 |
| IX2.81.14.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1I.GCS\_1\_IO.FB0 |
| IX2.81.14.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1J.GCS\_1\_IO.FB0 |
| IX2.81.14.6 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.GCS\_1K.GCS\_1\_IO.FB0 |
| IX2.81.14.7 |  |  |  | Ellato\_3.KSVG06781\_FB0 |
| IX2.81.14.8 |  |  |  | Ellato\_3.KSVG06876\_FB0 |
| IW2.81.14.9 |  |  |  |  |
| QX2.81.14.10 |  |  |  |  |
| QX2.81.14.11 |  |  |  |  |
| QX2.81.14.12 |  |  |  |  |
| QX2.81.14.13 |  |  |  |  |
| QX2.81.14.14 |  |  |  |  |
| QX2.81.14.15 |  |  |  |  |
| QX2.81.14.16 |  |  |  |  |
| QX2.81.14.17 |  |  |  |  |
| QW2.81.14.18 |  |  |  |  |
| IX2.81.14.19 |  |  |  |  |
| IX2.81.14.20 |  |  |  |  |
| IX2.81.14.21 |  |  |  |  |
| IX2.81.14.22 |  |  |  |  |
| IX2.81.14.23 |  |  |  |  |
| IX2.81.14.24 |  |  |  |  |
| IX2.81.14.25 |  |  |  |  |
| IX2.81.14.26 |  |  |  |  |

Table 471. Controller\_3.2.81.14 DX910\* (DIO8 S) (Status)

##### 15 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | true | bool |  |  |  |
| ch. 8: deactivate | true | bool |  |  |  |

Table 472. Controller\_3 (172.16.96.153).2.81.15 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.81.15.1 | Input 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Cax3.GCS\_1A.GCS\_1\_IO.FB0 | KCVG03951 |
| IX2.81.15.2 | Input 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Cax3.GCS\_1B.GCS\_1\_IO.FB0 | KCVG03952 |
| IX2.81.15.3 | Input 3 | BoolIO | Ellato\_3.U\_PL\_C3.EqMPLF.PL\_1.PL\_1\_IO | PL 12B31 |
| IX2.81.15.4 | Input 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM15Cax3.FS\_IO | FS03935 |
| IX2.81.15.5 | Input 5 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Aax31.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03926 |
| IX2.81.15.6 | Input 6 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Aax32.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03950 |
| IX2.81.15.7 | Input 7 | BoolIO |  |  |
| IX2.81.15.8 | Input 8 | BoolIO |  |  |
| IW2.81.15.9 | All inputs | DwordIO |  |  |
| QX2.81.15.10 | Output 1 | BoolIO |  |  |
| QX2.81.15.11 | Output 2 | BoolIO |  |  |
| QX2.81.15.12 | Output 3 | BoolIO |  |  |
| QX2.81.15.13 | Output 4 | BoolIO |  |  |
| QX2.81.15.14 | Output 5 | BoolIO |  |  |
| QX2.81.15.15 | Output 6 | BoolIO |  |  |
| QX2.81.15.16 | Output 7 | BoolIO |  |  |
| QX2.81.15.17 | Output 8 | BoolIO |  |  |
| QW2.81.15.18 | All outputs | DwordIO |  |  |
| IX2.81.15.19 | Status Input 1 | BoolIO |  |  |
| IX2.81.15.20 | Status Input 2 | BoolIO |  |  |
| IX2.81.15.21 | Status Input 3 | BoolIO |  |  |
| IX2.81.15.22 | Status Input 4 | BoolIO |  |  |
| IX2.81.15.23 | Status Input 5 | BoolIO |  |  |
| IX2.81.15.24 | Status Input 6 | BoolIO |  |  |
| IX2.81.15.25 | Status Input 7 | BoolIO |  |  |
| IX2.81.15.26 | Status Input 8 | BoolIO |  |  |
| IW2.81.15.27 | UnitStatus | HwStatus |  |  |

Table 473. Controller\_3.2.81.15 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.81.15.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Cax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.15.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Cax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.15.3 | Ellato\_3.U\_PL\_C3.EqMPLF.PL\_1.PL\_1\_IO |  |  |  |  | false |
| IX2.81.15.4 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM15Cax3.FS\_IO |  |  |  |  | false |
| IX2.81.15.5 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Aax31.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.15.6 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Aax32.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.81.15.7 |  |  |  |  |  | false |
| IX2.81.15.8 |  |  |  |  |  | false |
| QW2.81.15.9 |  |  |  |  |  |  |
| QX2.81.15.10 |  |  |  |  |  | false |
| QX2.81.15.11 |  |  |  |  |  | false |
| QX2.81.15.12 |  |  |  |  |  | false |
| QX2.81.15.13 |  |  |  |  |  | false |
| QX2.81.15.14 |  |  |  |  |  | false |
| QX2.81.15.15 |  |  |  |  |  | false |
| QX2.81.15.16 |  |  |  |  |  | false |
| QX2.81.15.17 |  |  |  |  |  | false |
| QW2.81.15.18 |  |  |  |  |  |  |
| IX2.81.15.19 |  |  |  |  |  | false |
| IX2.81.15.20 |  |  |  |  |  | false |
| IX2.81.15.21 |  |  |  |  |  | false |
| IX2.81.15.22 |  |  |  |  |  | false |
| IX2.81.15.23 |  |  |  |  |  | false |
| IX2.81.15.24 |  |  |  |  |  | false |
| IX2.81.15.25 |  |  |  |  |  | false |
| IX2.81.15.26 |  |  |  |  |  | false |

Table 474. Controller\_3.2.81.15 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.81.15.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Cax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.81.15.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM10Cax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.81.15.3 |  |  |  | Ellato\_3.U\_PL\_C3.EqMPLF.PL\_1.PL\_1\_IO |
| IX2.81.15.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM15Cax3.FS\_IO |
| IX2.81.15.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Aax31.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.81.15.6 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM02Aax32.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.81.15.7 |  |  |  |  |
| IX2.81.15.8 |  |  |  |  |
| IW2.81.15.9 |  |  |  |  |
| QX2.81.15.10 |  |  |  |  |
| QX2.81.15.11 |  |  |  |  |
| QX2.81.15.12 |  |  |  |  |
| QX2.81.15.13 |  |  |  |  |
| QX2.81.15.14 |  |  |  |  |
| QX2.81.15.15 |  |  |  |  |
| QX2.81.15.16 |  |  |  |  |
| QX2.81.15.17 |  |  |  |  |
| QW2.81.15.18 |  |  |  |  |
| IX2.81.15.19 |  |  |  |  |
| IX2.81.15.20 |  |  |  |  |
| IX2.81.15.21 |  |  |  |  |
| IX2.81.15.22 |  |  |  |  |
| IX2.81.15.23 |  |  |  |  |
| IX2.81.15.24 |  |  |  |  |
| IX2.81.15.25 |  |  |  |  |
| IX2.81.15.26 |  |  |  |  |

Table 475. Controller\_3.2.81.15 DX910\* (DIO8 S) (Status)

##### 16 AI930\* (AI4H A)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: HART status / range | off / 4..20mA | enum |  |  |  |
| ch. 2: HART status / range | off / 4..20mA | enum |  |  |  |
| ch. 3: HART status / range | off / 4..20mA | enum |  |  |  |
| ch. 4: HART status / range | off / 4..20mA | enum |  |  |  |
| ch. 1: filter (PT1) | off | enum |  |  |  |
| ch. 2: filter (PT1) | off | enum |  |  |  |
| ch. 3: filter (PT1) | off | enum |  |  |  |
| ch. 4: filter (PT1) | off | enum |  |  |  |

Table 476. Controller\_3 (172.16.96.153).2.81.16 AI930\* (AI4H A) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.81.16.1 | Input 1 | RealIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_2ax3\_A.IND\_2\_IO | LITSAH03923 |
| IW2.81.16.2 | Input 2 | RealIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_2ax3\_B.IND\_2\_IO | LITSAH03924 |
| IW2.81.16.3 | Input 3 | RealIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_2A.IND\_2\_IO | LIT04723 |
| IW2.81.16.4 | Input 4 | RealIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_2B.IND\_2\_IO | LIT04724 |
| IW2.81.16.5 | UnitStatus | HwStatus |  |  |

Table 477. Controller\_3.2.81.16 AI930\* (AI4H A) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW2.81.16.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_2ax3\_A.IND\_2\_IO | 0.0 | 100.0 |  | 1 | false |
| IW2.81.16.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_2ax3\_B.IND\_2\_IO | 0.0 | 100.0 |  | 1 | false |
| IW2.81.16.3 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_2A.IND\_2\_IO | 0.0 | 100.0 |  | 1 | false |
| IW2.81.16.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_2B.IND\_2\_IO | 0.0 | 100.0 |  | 1 | false |

Table 478. Controller\_3.2.81.16 AI930\* (AI4H A) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW2.81.16.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_2ax3\_A.IND\_2\_IO |
| IW2.81.16.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_2ax3\_B.IND\_2\_IO |
| IW2.81.16.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_2A.IND\_2\_IO |
| IW2.81.16.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_2B.IND\_2\_IO |

Table 479. Controller\_3.2.81.16 AI930\* (AI4H A) (Status)

#### 82 S900

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.82.0 | UnitStatus | HwStatus |  |  |

Table 480. Controller\_3.2.82 S900 (Connections)

##### 0 CI920\* (CIPB)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Grid Frequency | 50 Hz | enum |  |  |  |
| Analog Status | on | enum |  |  |  |
| Internal Bus Redundancy | false | bool |  |  |  |
| CIPB Redundancy | false | bool |  |  |  |
| Power Supply Redundancy | false | bool |  |  |  |

Table 481. Controller\_3 (172.16.96.153).2.82.0 CI920\* (CIPB) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.82.0.1 | UnitStatus | HwStatus |  |  |

Table 482. Controller\_3.2.82.0 CI920\* (CIPB) (Connections)

##### 1 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 483. Controller\_3 (172.16.96.153).2.82.1 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.82.1.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM05Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG00261 |
| QX2.82.1.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_B.GCS\_1A.GCS\_1\_IO.Cmd | KSVG00271 |
| QX2.82.1.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG00272 |
| QX2.82.1.4 | Output 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_A.GCS\_1B.GCS\_1\_IO.Cmd | KSVG00273 |
| QW2.82.1.5 | All outputs | DwordIO |  |  |
| IW2.82.1.6 | UnitStatus | HwStatus |  |  |

Table 484. Controller\_3.2.82.1 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.82.1.1 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM05Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.1.2 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_B.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.1.3 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.1.4 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_A.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.82.1.5 |  |  |  |  |  |  |

Table 485. Controller\_3.2.82.1 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.82.1.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM05Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.82.1.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_B.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.82.1.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.82.1.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_A.GCS\_1B.GCS\_1\_IO.Cmd |
| QW2.82.1.5 |  |  |  |  |

Table 486. Controller\_3.2.82.1 DO910\* (DO4) (Status)

##### 2 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 487. Controller\_3 (172.16.96.153).2.82.2 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.82.2.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_B.GCS\_1B.GCS\_1\_IO.Cmd | KSVG00274 |
| QX2.82.2.2 | Output 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3A.GCS\_1\_IO.Cmd | KSVG01020 |
| QX2.82.2.3 | Output 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM12A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG01036 |
| QX2.82.2.4 | Output 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Bax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG01047 |
| QW2.82.2.5 | All outputs | DwordIO |  |  |
| IW2.82.2.6 | UnitStatus | HwStatus |  |  |

Table 488. Controller\_3.2.82.2 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.82.2.1 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_B.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.2.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.2.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM12A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.2.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Bax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.82.2.5 |  |  |  |  |  |  |

Table 489. Controller\_3.2.82.2 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.82.2.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_B.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.82.2.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3A.GCS\_1\_IO.Cmd |
| QX2.82.2.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM12A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.82.2.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Bax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QW2.82.2.5 |  |  |  |  |

Table 490. Controller\_3.2.82.2 DO910\* (DO4) (Status)

##### 3 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 491. Controller\_3 (172.16.96.153).2.82.3 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.82.3.1 | Output 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3B.GCS\_1\_IO.Cmd | KSVG01050 |
| QX2.82.3.2 | Output 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3G.GCS\_1\_IO.Cmd | KSVG01059 |
| QX2.82.3.3 | Output 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3F.GCS\_1\_IO.Cmd | KSVG01058 |
| QX2.82.3.4 | Output 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG01053 |
| QW2.82.3.5 | All outputs | DwordIO |  |  |
| IW2.82.3.6 | UnitStatus | HwStatus |  |  |

Table 492. Controller\_3.2.82.3 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.82.3.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.3.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3G.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.3.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3F.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.3.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.82.3.5 |  |  |  |  |  |  |

Table 493. Controller\_3.2.82.3 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.82.3.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3B.GCS\_1\_IO.Cmd |
| QX2.82.3.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3G.GCS\_1\_IO.Cmd |
| QX2.82.3.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3F.GCS\_1\_IO.Cmd |
| QX2.82.3.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QW2.82.3.5 |  |  |  |  |

Table 494. Controller\_3.2.82.3 DO910\* (DO4) (Status)

##### 4 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 495. Controller\_3 (172.16.96.153).2.82.4 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.82.4.1 | Output 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd | KSVG01062 |
| QX2.82.4.2 | Output 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG01063 |
| QX2.82.4.3 | Output 3 | BoolIO |  |  |
| QX2.82.4.4 | Output 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3C.GCS\_1\_IO.Cmd | KSVG01065 |
| QW2.82.4.5 | All outputs | DwordIO |  |  |
| IW2.82.4.6 | UnitStatus | HwStatus |  |  |

Table 496. Controller\_3.2.82.4 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.82.4.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.4.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.4.3 |  |  |  |  |  | false |
| QX2.82.4.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.82.4.5 |  |  |  |  |  |  |

Table 497. Controller\_3.2.82.4 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.82.4.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |
| QX2.82.4.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.82.4.3 |  |  |  |  |
| QX2.82.4.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3C.GCS\_1\_IO.Cmd |
| QW2.82.4.5 |  |  |  |  |

Table 498. Controller\_3.2.82.4 DO910\* (DO4) (Status)

##### 5 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 499. Controller\_3 (172.16.96.153).2.82.5 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.82.5.1 | Output 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3D.GCS\_1\_IO.Cmd | KSVG01067 |
| QX2.82.5.2 | Output 2 | BoolIO |  |  |
| QX2.82.5.3 | Output 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG01077 |
| QX2.82.5.4 | Output 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Bax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG01078 |
| QW2.82.5.5 | All outputs | DwordIO |  |  |
| IW2.82.5.6 | UnitStatus | HwStatus |  |  |

Table 500. Controller\_3.2.82.5 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.82.5.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3D.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.5.2 |  |  |  |  |  | false |
| QX2.82.5.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.5.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Bax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.82.5.5 |  |  |  |  |  |  |

Table 501. Controller\_3.2.82.5 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.82.5.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3D.GCS\_1\_IO.Cmd |
| QX2.82.5.2 |  |  |  |  |
| QX2.82.5.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.82.5.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Bax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QW2.82.5.5 |  |  |  |  |

Table 502. Controller\_3.2.82.5 DO910\* (DO4) (Status)

##### 6 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 503. Controller\_3 (172.16.96.153).2.82.6 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.82.6.1 | Output 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3E.GCS\_1\_IO.Cmd | KSVG01079 |
| QX2.82.6.2 | Output 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG01088 |
| QX2.82.6.3 | Output 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG01089 |
| QX2.82.6.4 | Output 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1C.GCS\_1\_IO.Cmd | KSVG01090 |
| QW2.82.6.5 | All outputs | DwordIO |  |  |
| IW2.82.6.6 | UnitStatus | HwStatus |  |  |

Table 504. Controller\_3.2.82.6 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.82.6.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3E.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.6.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.6.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.6.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.82.6.5 |  |  |  |  |  |  |

Table 505. Controller\_3.2.82.6 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.82.6.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3E.GCS\_1\_IO.Cmd |
| QX2.82.6.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.82.6.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.82.6.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1C.GCS\_1\_IO.Cmd |
| QW2.82.6.5 |  |  |  |  |

Table 506. Controller\_3.2.82.6 DO910\* (DO4) (Status)

##### 7 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 507. Controller\_3 (172.16.96.153).2.82.7 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.82.7.1 | Output 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1D.GCS\_1\_IO.Cmd | KSVG01091 |
| QX2.82.7.2 | Output 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1E.GCS\_1\_IO.Cmd | KSVG01092 |
| QX2.82.7.3 | Output 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1F.GCS\_1\_IO.Cmd | KSVG01093 |
| QX2.82.7.4 | Output 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1G.GCS\_1\_IO.Cmd | KSVG01094 |
| QW2.82.7.5 | All outputs | DwordIO |  |  |
| IW2.82.7.6 | UnitStatus | HwStatus |  |  |

Table 508. Controller\_3.2.82.7 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.82.7.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1D.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.7.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1E.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.7.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1F.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.7.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1G.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.82.7.5 |  |  |  |  |  |  |

Table 509. Controller\_3.2.82.7 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.82.7.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1D.GCS\_1\_IO.Cmd |
| QX2.82.7.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1E.GCS\_1\_IO.Cmd |
| QX2.82.7.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1F.GCS\_1\_IO.Cmd |
| QX2.82.7.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1G.GCS\_1\_IO.Cmd |
| QW2.82.7.5 |  |  |  |  |

Table 510. Controller\_3.2.82.7 DO910\* (DO4) (Status)

##### 8 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 511. Controller\_3 (172.16.96.153).2.82.8 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.82.8.1 | Output 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1H.GCS\_1\_IO.Cmd | KSVG01095 |
| QX2.82.8.2 | Output 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1I.GCS\_1\_IO.Cmd | KSVG01096 |
| QX2.82.8.3 | Output 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1J.GCS\_1\_IO.Cmd | KSVG01097 |
| QX2.82.8.4 | Output 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1K.GCS\_1\_IO.Cmd | KSVG01098 |
| QW2.82.8.5 | All outputs | DwordIO |  |  |
| IW2.82.8.6 | UnitStatus | HwStatus |  |  |

Table 512. Controller\_3.2.82.8 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.82.8.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1H.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.8.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1I.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.8.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1J.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.82.8.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1K.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.82.8.5 |  |  |  |  |  |  |

Table 513. Controller\_3.2.82.8 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.82.8.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1H.GCS\_1\_IO.Cmd |
| QX2.82.8.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1I.GCS\_1\_IO.Cmd |
| QX2.82.8.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1J.GCS\_1\_IO.Cmd |
| QX2.82.8.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1K.GCS\_1\_IO.Cmd |
| QW2.82.8.5 |  |  |  |  |

Table 514. Controller\_3.2.82.8 DO910\* (DO4) (Status)

##### 9 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 515. Controller\_3 (172.16.96.153).2.82.9 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.82.9.1 | Output 1 | BoolIO | Ellato\_3.KSVG06880\_Cmd | KSVG06880 |
| QX2.82.9.2 | Output 2 | BoolIO |  |  |
| QX2.82.9.3 | Output 3 | BoolIO |  |  |
| QX2.82.9.4 | Output 4 | BoolIO |  |  |
| QW2.82.9.5 | All outputs | DwordIO |  |  |
| IW2.82.9.6 | UnitStatus | HwStatus |  |  |

Table 516. Controller\_3.2.82.9 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.82.9.1 | Ellato\_3.KSVG06880\_Cmd |  |  |  |  | false |
| QX2.82.9.2 |  |  |  |  |  | false |
| QX2.82.9.3 |  |  |  |  |  | false |
| QX2.82.9.4 |  |  |  |  |  | false |
| QW2.82.9.5 |  |  |  |  |  |  |

Table 517. Controller\_3.2.82.9 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.82.9.1 |  |  |  | Ellato\_3.KSVG06880\_Cmd |
| QX2.82.9.2 |  |  |  |  |
| QX2.82.9.3 |  |  |  |  |
| QX2.82.9.4 |  |  |  |  |
| QW2.82.9.5 |  |  |  |  |

Table 518. Controller\_3.2.82.9 DO910\* (DO4) (Status)

##### 11 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 519. Controller\_3 (172.16.96.153).2.82.11 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.82.11.1 | Input 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM15Cax3.FS\_IO | FS00238 |
| IX2.82.11.2 | Input 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM05Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG00261 |
| IX2.82.11.3 | Input 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_B.GCS\_1A.GCS\_1\_IO.FB0 | KSVG00271 |
| IX2.82.11.4 | Input 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG00272 |
| IX2.82.11.5 | Input 5 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_A.GCS\_1B.GCS\_1\_IO.FB0 | KSVG00273 |
| IX2.82.11.6 | Input 6 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_B.GCS\_1B.GCS\_1\_IO.FB0 | KSVG00274 |
| IX2.82.11.7 | Input 7 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3A.GCS\_1\_IO.FB0 | KSVG01020 |
| IX2.82.11.8 | Input 8 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM12A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG01036 |
| IW2.82.11.9 | All inputs | DwordIO |  |  |
| QX2.82.11.10 | Output 1 | BoolIO |  |  |
| QX2.82.11.11 | Output 2 | BoolIO |  |  |
| QX2.82.11.12 | Output 3 | BoolIO |  |  |
| QX2.82.11.13 | Output 4 | BoolIO |  |  |
| QX2.82.11.14 | Output 5 | BoolIO |  |  |
| QX2.82.11.15 | Output 6 | BoolIO |  |  |
| QX2.82.11.16 | Output 7 | BoolIO |  |  |
| QX2.82.11.17 | Output 8 | BoolIO |  |  |
| QW2.82.11.18 | All outputs | DwordIO |  |  |
| IX2.82.11.19 | Status Input 1 | BoolIO |  |  |
| IX2.82.11.20 | Status Input 2 | BoolIO |  |  |
| IX2.82.11.21 | Status Input 3 | BoolIO |  |  |
| IX2.82.11.22 | Status Input 4 | BoolIO |  |  |
| IX2.82.11.23 | Status Input 5 | BoolIO |  |  |
| IX2.82.11.24 | Status Input 6 | BoolIO |  |  |
| IX2.82.11.25 | Status Input 7 | BoolIO |  |  |
| IX2.82.11.26 | Status Input 8 | BoolIO |  |  |
| IW2.82.11.27 | UnitStatus | HwStatus |  |  |

Table 520. Controller\_3.2.82.11 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.82.11.1 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM15Cax3.FS\_IO |  |  |  |  | false |
| IX2.82.11.2 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM05Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.11.3 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_B.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.11.4 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.11.5 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_A.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.11.6 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_B.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.11.7 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.11.8 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM12A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW2.82.11.9 |  |  |  |  |  |  |
| QX2.82.11.10 |  |  |  |  |  | false |
| QX2.82.11.11 |  |  |  |  |  | false |
| QX2.82.11.12 |  |  |  |  |  | false |
| QX2.82.11.13 |  |  |  |  |  | false |
| QX2.82.11.14 |  |  |  |  |  | false |
| QX2.82.11.15 |  |  |  |  |  | false |
| QX2.82.11.16 |  |  |  |  |  | false |
| QX2.82.11.17 |  |  |  |  |  | false |
| QW2.82.11.18 |  |  |  |  |  |  |
| IX2.82.11.19 |  |  |  |  |  | false |
| IX2.82.11.20 |  |  |  |  |  | false |
| IX2.82.11.21 |  |  |  |  |  | false |
| IX2.82.11.22 |  |  |  |  |  | false |
| IX2.82.11.23 |  |  |  |  |  | false |
| IX2.82.11.24 |  |  |  |  |  | false |
| IX2.82.11.25 |  |  |  |  |  | false |
| IX2.82.11.26 |  |  |  |  |  | false |

Table 521. Controller\_3.2.82.11 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.82.11.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM15Cax3.FS\_IO |
| IX2.82.11.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM05Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.82.11.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_B.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.82.11.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.82.11.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_A.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.82.11.6 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM02B\_B.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.82.11.7 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3A.GCS\_1\_IO.FB0 |
| IX2.82.11.8 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM12A.GCS\_1A.GCS\_1\_IO.FB0 |
| IW2.82.11.9 |  |  |  |  |
| QX2.82.11.10 |  |  |  |  |
| QX2.82.11.11 |  |  |  |  |
| QX2.82.11.12 |  |  |  |  |
| QX2.82.11.13 |  |  |  |  |
| QX2.82.11.14 |  |  |  |  |
| QX2.82.11.15 |  |  |  |  |
| QX2.82.11.16 |  |  |  |  |
| QX2.82.11.17 |  |  |  |  |
| QW2.82.11.18 |  |  |  |  |
| IX2.82.11.19 |  |  |  |  |
| IX2.82.11.20 |  |  |  |  |
| IX2.82.11.21 |  |  |  |  |
| IX2.82.11.22 |  |  |  |  |
| IX2.82.11.23 |  |  |  |  |
| IX2.82.11.24 |  |  |  |  |
| IX2.82.11.25 |  |  |  |  |
| IX2.82.11.26 |  |  |  |  |

Table 522. Controller\_3.2.82.11 DX910\* (DIO8 S) (Status)

##### 12 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | true | bool |  |  |  |

Table 523. Controller\_3 (172.16.96.153).2.82.12 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.82.12.1 | Input 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Bax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG01047 |
| IX2.82.12.2 | Input 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3B.GCS\_1\_IO.FB0 | KSVG01050 |
| IX2.82.12.3 | Input 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3G.GCS\_1\_IO.FB0 | KSVG01059 |
| IX2.82.12.4 | Input 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3F.GCS\_1\_IO.FB0 | KSVG01058 |
| IX2.82.12.5 | Input 5 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG01053 |
| IX2.82.12.6 | Input 6 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 | KSVG01062 |
| IX2.82.12.7 | Input 7 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG01063 |
| IX2.82.12.8 | Input 8 | BoolIO |  |  |
| IW2.82.12.9 | All inputs | DwordIO |  |  |
| QX2.82.12.10 | Output 1 | BoolIO |  |  |
| QX2.82.12.11 | Output 2 | BoolIO |  |  |
| QX2.82.12.12 | Output 3 | BoolIO |  |  |
| QX2.82.12.13 | Output 4 | BoolIO |  |  |
| QX2.82.12.14 | Output 5 | BoolIO |  |  |
| QX2.82.12.15 | Output 6 | BoolIO |  |  |
| QX2.82.12.16 | Output 7 | BoolIO |  |  |
| QX2.82.12.17 | Output 8 | BoolIO |  |  |
| QW2.82.12.18 | All outputs | DwordIO |  |  |
| IX2.82.12.19 | Status Input 1 | BoolIO |  |  |
| IX2.82.12.20 | Status Input 2 | BoolIO |  |  |
| IX2.82.12.21 | Status Input 3 | BoolIO |  |  |
| IX2.82.12.22 | Status Input 4 | BoolIO |  |  |
| IX2.82.12.23 | Status Input 5 | BoolIO |  |  |
| IX2.82.12.24 | Status Input 6 | BoolIO |  |  |
| IX2.82.12.25 | Status Input 7 | BoolIO |  |  |
| IX2.82.12.26 | Status Input 8 | BoolIO |  |  |
| IW2.82.12.27 | UnitStatus | HwStatus |  |  |

Table 524. Controller\_3.2.82.12 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.82.12.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Bax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.12.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.12.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3G.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.12.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3F.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.12.5 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.12.6 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.12.7 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.12.8 |  |  |  |  |  | false |
| QW2.82.12.9 |  |  |  |  |  |  |
| QX2.82.12.10 |  |  |  |  |  | false |
| QX2.82.12.11 |  |  |  |  |  | false |
| QX2.82.12.12 |  |  |  |  |  | false |
| QX2.82.12.13 |  |  |  |  |  | false |
| QX2.82.12.14 |  |  |  |  |  | false |
| QX2.82.12.15 |  |  |  |  |  | false |
| QX2.82.12.16 |  |  |  |  |  | false |
| QX2.82.12.17 |  |  |  |  |  | false |
| QW2.82.12.18 |  |  |  |  |  |  |
| IX2.82.12.19 |  |  |  |  |  | false |
| IX2.82.12.20 |  |  |  |  |  | false |
| IX2.82.12.21 |  |  |  |  |  | false |
| IX2.82.12.22 |  |  |  |  |  | false |
| IX2.82.12.23 |  |  |  |  |  | false |
| IX2.82.12.24 |  |  |  |  |  | false |
| IX2.82.12.25 |  |  |  |  |  | false |
| IX2.82.12.26 |  |  |  |  |  | false |

Table 525. Controller\_3.2.82.12 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.82.12.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Bax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.82.12.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3B.GCS\_1\_IO.FB0 |
| IX2.82.12.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3G.GCS\_1\_IO.FB0 |
| IX2.82.12.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3F.GCS\_1\_IO.FB0 |
| IX2.82.12.5 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.82.12.6 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |
| IX2.82.12.7 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM01Aax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.82.12.8 |  |  |  |  |
| IW2.82.12.9 |  |  |  |  |
| QX2.82.12.10 |  |  |  |  |
| QX2.82.12.11 |  |  |  |  |
| QX2.82.12.12 |  |  |  |  |
| QX2.82.12.13 |  |  |  |  |
| QX2.82.12.14 |  |  |  |  |
| QX2.82.12.15 |  |  |  |  |
| QX2.82.12.16 |  |  |  |  |
| QX2.82.12.17 |  |  |  |  |
| QW2.82.12.18 |  |  |  |  |
| IX2.82.12.19 |  |  |  |  |
| IX2.82.12.20 |  |  |  |  |
| IX2.82.12.21 |  |  |  |  |
| IX2.82.12.22 |  |  |  |  |
| IX2.82.12.23 |  |  |  |  |
| IX2.82.12.24 |  |  |  |  |
| IX2.82.12.25 |  |  |  |  |
| IX2.82.12.26 |  |  |  |  |

Table 526. Controller\_3.2.82.12 DX910\* (DIO8 S) (Status)

##### 13 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | true | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | true | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 527. Controller\_3 (172.16.96.153).2.82.13 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.82.13.1 | Input 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3C.GCS\_1\_IO.FB0 | KSVG01065 |
| IX2.82.13.2 | Input 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3D.GCS\_1\_IO.FB0 | KSVG01067 |
| IX2.82.13.3 | Input 3 | BoolIO |  |  |
| IX2.82.13.4 | Input 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG01077 |
| IX2.82.13.5 | Input 5 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Bax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG01078 |
| IX2.82.13.6 | Input 6 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3E.GCS\_1\_IO.FB0 | KSVG01079 |
| IX2.82.13.7 | Input 7 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG01088 |
| IX2.82.13.8 | Input 8 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG01089 |
| IW2.82.13.9 | All inputs | DwordIO |  |  |
| QX2.82.13.10 | Output 1 | BoolIO |  |  |
| QX2.82.13.11 | Output 2 | BoolIO |  |  |
| QX2.82.13.12 | Output 3 | BoolIO |  |  |
| QX2.82.13.13 | Output 4 | BoolIO |  |  |
| QX2.82.13.14 | Output 5 | BoolIO |  |  |
| QX2.82.13.15 | Output 6 | BoolIO |  |  |
| QX2.82.13.16 | Output 7 | BoolIO |  |  |
| QX2.82.13.17 | Output 8 | BoolIO |  |  |
| QW2.82.13.18 | All outputs | DwordIO |  |  |
| IX2.82.13.19 | Status Input 1 | BoolIO |  |  |
| IX2.82.13.20 | Status Input 2 | BoolIO |  |  |
| IX2.82.13.21 | Status Input 3 | BoolIO |  |  |
| IX2.82.13.22 | Status Input 4 | BoolIO |  |  |
| IX2.82.13.23 | Status Input 5 | BoolIO |  |  |
| IX2.82.13.24 | Status Input 6 | BoolIO |  |  |
| IX2.82.13.25 | Status Input 7 | BoolIO |  |  |
| IX2.82.13.26 | Status Input 8 | BoolIO |  |  |
| IW2.82.13.27 | UnitStatus | HwStatus |  |  |

Table 528. Controller\_3.2.82.13 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.82.13.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.13.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3D.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.13.3 |  |  |  |  |  | false |
| IX2.82.13.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.13.5 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Bax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.13.6 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3E.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.13.7 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.13.8 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW2.82.13.9 |  |  |  |  |  |  |
| QX2.82.13.10 |  |  |  |  |  | false |
| QX2.82.13.11 |  |  |  |  |  | false |
| QX2.82.13.12 |  |  |  |  |  | false |
| QX2.82.13.13 |  |  |  |  |  | false |
| QX2.82.13.14 |  |  |  |  |  | false |
| QX2.82.13.15 |  |  |  |  |  | false |
| QX2.82.13.16 |  |  |  |  |  | false |
| QX2.82.13.17 |  |  |  |  |  | false |
| QW2.82.13.18 |  |  |  |  |  |  |
| IX2.82.13.19 |  |  |  |  |  | false |
| IX2.82.13.20 |  |  |  |  |  | false |
| IX2.82.13.21 |  |  |  |  |  | false |
| IX2.82.13.22 |  |  |  |  |  | false |
| IX2.82.13.23 |  |  |  |  |  | false |
| IX2.82.13.24 |  |  |  |  |  | false |
| IX2.82.13.25 |  |  |  |  |  | false |
| IX2.82.13.26 |  |  |  |  |  | false |

Table 529. Controller\_3.2.82.13 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.82.13.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3C.GCS\_1\_IO.FB0 |
| IX2.82.13.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3D.GCS\_1\_IO.FB0 |
| IX2.82.13.3 |  |  |  |  |
| IX2.82.13.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.82.13.5 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Bax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.82.13.6 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM02Iax3.GCS\_1ax3E.GCS\_1\_IO.FB0 |
| IX2.82.13.7 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.82.13.8 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IW2.82.13.9 |  |  |  |  |
| QX2.82.13.10 |  |  |  |  |
| QX2.82.13.11 |  |  |  |  |
| QX2.82.13.12 |  |  |  |  |
| QX2.82.13.13 |  |  |  |  |
| QX2.82.13.14 |  |  |  |  |
| QX2.82.13.15 |  |  |  |  |
| QX2.82.13.16 |  |  |  |  |
| QX2.82.13.17 |  |  |  |  |
| QW2.82.13.18 |  |  |  |  |
| IX2.82.13.19 |  |  |  |  |
| IX2.82.13.20 |  |  |  |  |
| IX2.82.13.21 |  |  |  |  |
| IX2.82.13.22 |  |  |  |  |
| IX2.82.13.23 |  |  |  |  |
| IX2.82.13.24 |  |  |  |  |
| IX2.82.13.25 |  |  |  |  |
| IX2.82.13.26 |  |  |  |  |

Table 530. Controller\_3.2.82.13 DX910\* (DIO8 S) (Status)

##### 14 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 531. Controller\_3 (172.16.96.153).2.82.14 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.82.14.1 | Input 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1C.GCS\_1\_IO.FB0 | KSVG01090 |
| IX2.82.14.2 | Input 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1D.GCS\_1\_IO.FB0 | KSVG01091 |
| IX2.82.14.3 | Input 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1E.GCS\_1\_IO.FB0 | KSVG01092 |
| IX2.82.14.4 | Input 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1F.GCS\_1\_IO.FB0 | KSVG01093 |
| IX2.82.14.5 | Input 5 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1G.GCS\_1\_IO.FB0 | KSVG01094 |
| IX2.82.14.6 | Input 6 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1H.GCS\_1\_IO.FB0 | KSVG01095 |
| IX2.82.14.7 | Input 7 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1I.GCS\_1\_IO.FB0 | KSVG01096 |
| IX2.82.14.8 | Input 8 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1J.GCS\_1\_IO.FB0 | KSVG01097 |
| IW2.82.14.9 | All inputs | DwordIO |  |  |
| QX2.82.14.10 | Output 1 | BoolIO |  |  |
| QX2.82.14.11 | Output 2 | BoolIO |  |  |
| QX2.82.14.12 | Output 3 | BoolIO |  |  |
| QX2.82.14.13 | Output 4 | BoolIO |  |  |
| QX2.82.14.14 | Output 5 | BoolIO |  |  |
| QX2.82.14.15 | Output 6 | BoolIO |  |  |
| QX2.82.14.16 | Output 7 | BoolIO |  |  |
| QX2.82.14.17 | Output 8 | BoolIO |  |  |
| QW2.82.14.18 | All outputs | DwordIO |  |  |
| IX2.82.14.19 | Status Input 1 | BoolIO |  |  |
| IX2.82.14.20 | Status Input 2 | BoolIO |  |  |
| IX2.82.14.21 | Status Input 3 | BoolIO |  |  |
| IX2.82.14.22 | Status Input 4 | BoolIO |  |  |
| IX2.82.14.23 | Status Input 5 | BoolIO |  |  |
| IX2.82.14.24 | Status Input 6 | BoolIO |  |  |
| IX2.82.14.25 | Status Input 7 | BoolIO |  |  |
| IX2.82.14.26 | Status Input 8 | BoolIO |  |  |
| IW2.82.14.27 | UnitStatus | HwStatus |  |  |

Table 532. Controller\_3.2.82.14 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.82.14.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.14.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1D.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.14.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1E.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.14.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1F.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.14.5 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1G.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.14.6 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1H.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.14.7 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1I.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.14.8 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1J.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW2.82.14.9 |  |  |  |  |  |  |
| QX2.82.14.10 |  |  |  |  |  | false |
| QX2.82.14.11 |  |  |  |  |  | false |
| QX2.82.14.12 |  |  |  |  |  | false |
| QX2.82.14.13 |  |  |  |  |  | false |
| QX2.82.14.14 |  |  |  |  |  | false |
| QX2.82.14.15 |  |  |  |  |  | false |
| QX2.82.14.16 |  |  |  |  |  | false |
| QX2.82.14.17 |  |  |  |  |  | false |
| QW2.82.14.18 |  |  |  |  |  |  |
| IX2.82.14.19 |  |  |  |  |  | false |
| IX2.82.14.20 |  |  |  |  |  | false |
| IX2.82.14.21 |  |  |  |  |  | false |
| IX2.82.14.22 |  |  |  |  |  | false |
| IX2.82.14.23 |  |  |  |  |  | false |
| IX2.82.14.24 |  |  |  |  |  | false |
| IX2.82.14.25 |  |  |  |  |  | false |
| IX2.82.14.26 |  |  |  |  |  | false |

Table 533. Controller\_3.2.82.14 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.82.14.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1C.GCS\_1\_IO.FB0 |
| IX2.82.14.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1D.GCS\_1\_IO.FB0 |
| IX2.82.14.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1E.GCS\_1\_IO.FB0 |
| IX2.82.14.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1F.GCS\_1\_IO.FB0 |
| IX2.82.14.5 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1G.GCS\_1\_IO.FB0 |
| IX2.82.14.6 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1H.GCS\_1\_IO.FB0 |
| IX2.82.14.7 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1I.GCS\_1\_IO.FB0 |
| IX2.82.14.8 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1J.GCS\_1\_IO.FB0 |
| IW2.82.14.9 |  |  |  |  |
| QX2.82.14.10 |  |  |  |  |
| QX2.82.14.11 |  |  |  |  |
| QX2.82.14.12 |  |  |  |  |
| QX2.82.14.13 |  |  |  |  |
| QX2.82.14.14 |  |  |  |  |
| QX2.82.14.15 |  |  |  |  |
| QX2.82.14.16 |  |  |  |  |
| QX2.82.14.17 |  |  |  |  |
| QW2.82.14.18 |  |  |  |  |
| IX2.82.14.19 |  |  |  |  |
| IX2.82.14.20 |  |  |  |  |
| IX2.82.14.21 |  |  |  |  |
| IX2.82.14.22 |  |  |  |  |
| IX2.82.14.23 |  |  |  |  |
| IX2.82.14.24 |  |  |  |  |
| IX2.82.14.25 |  |  |  |  |
| IX2.82.14.26 |  |  |  |  |

Table 534. Controller\_3.2.82.14 DX910\* (DIO8 S) (Status)

##### 15 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | true | bool |  |  |  |
| ch. 5: deactivate | true | bool |  |  |  |
| ch. 6: deactivate | true | bool |  |  |  |
| ch. 7: deactivate | true | bool |  |  |  |
| ch. 8: deactivate | true | bool |  |  |  |

Table 535. Controller\_3 (172.16.96.153).2.82.15 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.82.15.1 | Input 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1K.GCS\_1\_IO.FB0 | KSVG01098 |
| IX2.82.15.2 | Input 2 | BoolIO | Ellato\_3.KSVG06880\_FB0 | KSVG06880 |
| IX2.82.15.3 | Input 3 | BoolIO | Ellato\_3.U\_PL\_C3.EqMPLG.PL\_1.PL\_1\_IO | PL 02B31 |
| IX2.82.15.4 | Input 4 | BoolIO |  |  |
| IX2.82.15.5 | Input 5 | BoolIO |  |  |
| IX2.82.15.6 | Input 6 | BoolIO |  |  |
| IX2.82.15.7 | Input 7 | BoolIO |  |  |
| IX2.82.15.8 | Input 8 | BoolIO |  |  |
| IW2.82.15.9 | All inputs | DwordIO |  |  |
| QX2.82.15.10 | Output 1 | BoolIO |  |  |
| QX2.82.15.11 | Output 2 | BoolIO |  |  |
| QX2.82.15.12 | Output 3 | BoolIO |  |  |
| QX2.82.15.13 | Output 4 | BoolIO |  |  |
| QX2.82.15.14 | Output 5 | BoolIO |  |  |
| QX2.82.15.15 | Output 6 | BoolIO |  |  |
| QX2.82.15.16 | Output 7 | BoolIO |  |  |
| QX2.82.15.17 | Output 8 | BoolIO |  |  |
| QW2.82.15.18 | All outputs | DwordIO |  |  |
| IX2.82.15.19 | Status Input 1 | BoolIO |  |  |
| IX2.82.15.20 | Status Input 2 | BoolIO |  |  |
| IX2.82.15.21 | Status Input 3 | BoolIO |  |  |
| IX2.82.15.22 | Status Input 4 | BoolIO |  |  |
| IX2.82.15.23 | Status Input 5 | BoolIO |  |  |
| IX2.82.15.24 | Status Input 6 | BoolIO |  |  |
| IX2.82.15.25 | Status Input 7 | BoolIO |  |  |
| IX2.82.15.26 | Status Input 8 | BoolIO |  |  |
| IW2.82.15.27 | UnitStatus | HwStatus |  |  |

Table 536. Controller\_3.2.82.15 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.82.15.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1K.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.82.15.2 | Ellato\_3.KSVG06880\_FB0 |  |  |  |  | false |
| IX2.82.15.3 | Ellato\_3.U\_PL\_C3.EqMPLG.PL\_1.PL\_1\_IO |  |  |  |  | false |
| IX2.82.15.4 |  |  |  |  |  | false |
| IX2.82.15.5 |  |  |  |  |  | false |
| IX2.82.15.6 |  |  |  |  |  | false |
| IX2.82.15.7 |  |  |  |  |  | false |
| IX2.82.15.8 |  |  |  |  |  | false |
| QW2.82.15.9 |  |  |  |  |  |  |
| QX2.82.15.10 |  |  |  |  |  | false |
| QX2.82.15.11 |  |  |  |  |  | false |
| QX2.82.15.12 |  |  |  |  |  | false |
| QX2.82.15.13 |  |  |  |  |  | false |
| QX2.82.15.14 |  |  |  |  |  | false |
| QX2.82.15.15 |  |  |  |  |  | false |
| QX2.82.15.16 |  |  |  |  |  | false |
| QX2.82.15.17 |  |  |  |  |  | false |
| QW2.82.15.18 |  |  |  |  |  |  |
| IX2.82.15.19 |  |  |  |  |  | false |
| IX2.82.15.20 |  |  |  |  |  | false |
| IX2.82.15.21 |  |  |  |  |  | false |
| IX2.82.15.22 |  |  |  |  |  | false |
| IX2.82.15.23 |  |  |  |  |  | false |
| IX2.82.15.24 |  |  |  |  |  | false |
| IX2.82.15.25 |  |  |  |  |  | false |
| IX2.82.15.26 |  |  |  |  |  | false |

Table 537. Controller\_3.2.82.15 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.82.15.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.GCS\_1K.GCS\_1\_IO.FB0 |
| IX2.82.15.2 |  |  |  | Ellato\_3.KSVG06880\_FB0 |
| IX2.82.15.3 |  |  |  | Ellato\_3.U\_PL\_C3.EqMPLG.PL\_1.PL\_1\_IO |
| IX2.82.15.4 |  |  |  |  |
| IX2.82.15.5 |  |  |  |  |
| IX2.82.15.6 |  |  |  |  |
| IX2.82.15.7 |  |  |  |  |
| IX2.82.15.8 |  |  |  |  |
| IW2.82.15.9 |  |  |  |  |
| QX2.82.15.10 |  |  |  |  |
| QX2.82.15.11 |  |  |  |  |
| QX2.82.15.12 |  |  |  |  |
| QX2.82.15.13 |  |  |  |  |
| QX2.82.15.14 |  |  |  |  |
| QX2.82.15.15 |  |  |  |  |
| QX2.82.15.16 |  |  |  |  |
| QX2.82.15.17 |  |  |  |  |
| QW2.82.15.18 |  |  |  |  |
| IX2.82.15.19 |  |  |  |  |
| IX2.82.15.20 |  |  |  |  |
| IX2.82.15.21 |  |  |  |  |
| IX2.82.15.22 |  |  |  |  |
| IX2.82.15.23 |  |  |  |  |
| IX2.82.15.24 |  |  |  |  |
| IX2.82.15.25 |  |  |  |  |
| IX2.82.15.26 |  |  |  |  |

Table 538. Controller\_3.2.82.15 DX910\* (DIO8 S) (Status)

#### 83 S900

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.83.0 | UnitStatus | HwStatus |  |  |

Table 539. Controller\_3.2.83 S900 (Connections)

##### 0 CI920\* (CIPB)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Grid Frequency | 50 Hz | enum |  |  |  |
| Analog Status | on | enum |  |  |  |
| Internal Bus Redundancy | false | bool |  |  |  |
| CIPB Redundancy | false | bool |  |  |  |
| Power Supply Redundancy | false | bool |  |  |  |

Table 540. Controller\_3 (172.16.96.153).2.83.0 CI920\* (CIPB) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.83.0.1 | UnitStatus | HwStatus |  |  |

Table 541. Controller\_3.2.83.0 CI920\* (CIPB) (Connections)

##### 1 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 542. Controller\_3 (172.16.96.153).2.83.1 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.83.1.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04661 |
| QX2.83.1.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1B.GCS\_1\_IO.Cmd | KSVG04676 |
| QX2.83.1.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1C.GCS\_1\_IO.Cmd | KSVG04686 |
| QX2.83.1.4 | Output 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM12A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04736 |
| QW2.83.1.5 | All outputs | DwordIO |  |  |
| IW2.83.1.6 | UnitStatus | HwStatus |  |  |

Table 543. Controller\_3.2.83.1 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.83.1.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.1.2 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.1.3 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.1.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM12A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.83.1.5 |  |  |  |  |  |  |

Table 544. Controller\_3.2.83.1 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.83.1.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.83.1.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.83.1.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1C.GCS\_1\_IO.Cmd |
| QX2.83.1.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM12A.GCS\_1A.GCS\_1\_IO.Cmd |
| QW2.83.1.5 |  |  |  |  |

Table 545. Controller\_3.2.83.1 DO910\* (DO4) (Status)

##### 2 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 546. Controller\_3 (172.16.96.153).2.83.2 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.83.2.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM10C.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04751 |
| QX2.83.2.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM10C.GCS\_1B.GCS\_1\_IO.Cmd | KSVG04752 |
| QX2.83.2.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM21A\_A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04756 |
| QX2.83.2.4 | Output 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM21A\_B.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04757 |
| QW2.83.2.5 | All outputs | DwordIO |  |  |
| IW2.83.2.6 | UnitStatus | HwStatus |  |  |

Table 547. Controller\_3.2.83.2 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.83.2.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM10C.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.2.2 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM10C.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.2.3 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM21A\_A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.2.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM21A\_B.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.83.2.5 |  |  |  |  |  |  |

Table 548. Controller\_3.2.83.2 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.83.2.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM10C.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.83.2.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM10C.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.83.2.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM21A\_A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.83.2.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM21A\_B.GCS\_1A.GCS\_1\_IO.Cmd |
| QW2.83.2.5 |  |  |  |  |

Table 549. Controller\_3.2.83.2 DO910\* (DO4) (Status)

##### 3 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 550. Controller\_3 (172.16.96.153).2.83.3 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.83.3.1 | Output 1 | BoolIO | Ellato\_3.KSVG06757\_Cmd | KSVG06757 |
| QX2.83.3.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd | KSVG04762 |
| QX2.83.3.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.GCS\_1B.GCS\_1\_IO.Cmd | KSVG04763 |
| QX2.83.3.4 | Output 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM04A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04764 |
| QW2.83.3.5 | All outputs | DwordIO |  |  |
| IW2.83.3.6 | UnitStatus | HwStatus |  |  |

Table 551. Controller\_3.2.83.3 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.83.3.1 | Ellato\_3.KSVG06757\_Cmd |  |  |  |  | false |
| QX2.83.3.2 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.3.3 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.3.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM04A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.83.3.5 |  |  |  |  |  |  |

Table 552. Controller\_3.2.83.3 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.83.3.1 |  |  |  | Ellato\_3.KSVG06757\_Cmd |
| QX2.83.3.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.CNT\_1.GCS\_1.GCS\_1\_IO.Cmd |
| QX2.83.3.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.83.3.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM04A.GCS\_1A.GCS\_1\_IO.Cmd |
| QW2.83.3.5 |  |  |  |  |

Table 553. Controller\_3.2.83.3 DO910\* (DO4) (Status)

##### 4 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 554. Controller\_3 (172.16.96.153).2.83.4 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.83.4.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04766 |
| QX2.83.4.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1B.GCS\_1\_IO.Cmd | KSVG04767 |
| QX2.83.4.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM03A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04768 |
| QX2.83.4.4 | Output 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM03B.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04769 |
| QW2.83.4.5 | All outputs | DwordIO |  |  |
| IW2.83.4.6 | UnitStatus | HwStatus |  |  |

Table 555. Controller\_3.2.83.4 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.83.4.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.4.2 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.4.3 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM03A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.4.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM03B.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.83.4.5 |  |  |  |  |  |  |

Table 556. Controller\_3.2.83.4 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.83.4.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.83.4.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.83.4.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM03A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.83.4.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM03B.GCS\_1A.GCS\_1\_IO.Cmd |
| QW2.83.4.5 |  |  |  |  |

Table 557. Controller\_3.2.83.4 DO910\* (DO4) (Status)

##### 5 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 558. Controller\_3 (172.16.96.153).2.83.5 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.83.5.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1C.GCS\_1\_IO.Cmd | KSVG04771 |
| QX2.83.5.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1D.GCS\_1\_IO.Cmd | KSVG04772 |
| QX2.83.5.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1C.GCS\_1\_IO.Cmd | KSVG04773 |
| QX2.83.5.4 | Output 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1C.GCS\_1\_IO.Cmd | KSVG04774 |
| QW2.83.5.5 | All outputs | DwordIO |  |  |
| IW2.83.5.6 | UnitStatus | HwStatus |  |  |

Table 559. Controller\_3.2.83.5 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.83.5.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.5.2 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1D.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.5.3 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.5.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.83.5.5 |  |  |  |  |  |  |

Table 560. Controller\_3.2.83.5 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.83.5.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1C.GCS\_1\_IO.Cmd |
| QX2.83.5.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1D.GCS\_1\_IO.Cmd |
| QX2.83.5.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1C.GCS\_1\_IO.Cmd |
| QX2.83.5.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1C.GCS\_1\_IO.Cmd |
| QW2.83.5.5 |  |  |  |  |

Table 561. Controller\_3.2.83.5 DO910\* (DO4) (Status)

##### 6 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 562. Controller\_3 (172.16.96.153).2.83.6 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.83.6.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_A.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd | KSVG04775 |
| QX2.83.6.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_B.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd | KSVG04776 |
| QX2.83.6.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1G.GCS\_1\_IO.Cmd | KSVG04794 |
| QX2.83.6.4 | Output 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04777 |
| QW2.83.6.5 | All outputs | DwordIO |  |  |
| IW2.83.6.6 | UnitStatus | HwStatus |  |  |

Table 563. Controller\_3.2.83.6 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.83.6.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_A.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.6.2 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_B.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.6.3 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1G.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.6.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.83.6.5 |  |  |  |  |  |  |

Table 564. Controller\_3.2.83.6 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.83.6.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_A.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd |
| QX2.83.6.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_B.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.Cmd |
| QX2.83.6.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1G.GCS\_1\_IO.Cmd |
| QX2.83.6.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02A.GCS\_1A.GCS\_1\_IO.Cmd |
| QW2.83.6.5 |  |  |  |  |

Table 565. Controller\_3.2.83.6 DO910\* (DO4) (Status)

##### 7 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 566. Controller\_3 (172.16.96.153).2.83.7 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.83.7.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM11.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04780 |
| QX2.83.7.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04781 |
| QX2.83.7.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04782 |
| QX2.83.7.4 | Output 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1B.GCS\_1\_IO.Cmd | KSVG04783 |
| QW2.83.7.5 | All outputs | DwordIO |  |  |
| IW2.83.7.6 | UnitStatus | HwStatus |  |  |

Table 567. Controller\_3.2.83.7 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.83.7.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM11.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.7.2 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.7.3 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.7.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.83.7.5 |  |  |  |  |  |  |

Table 568. Controller\_3.2.83.7 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.83.7.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM11.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.83.7.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.83.7.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.83.7.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1B.GCS\_1\_IO.Cmd |
| QW2.83.7.5 |  |  |  |  |

Table 569. Controller\_3.2.83.7 DO910\* (DO4) (Status)

##### 8 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 570. Controller\_3 (172.16.96.153).2.83.8 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.83.8.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1B.GCS\_1\_IO.Cmd | KSVG04784 |
| QX2.83.8.2 | Output 2 | BoolIO | Ellato\_3.KSVG06775\_Cmd | KSVG06775 |
| QX2.83.8.3 | Output 3 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM05Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04261 |
| QX2.83.8.4 | Output 4 | BoolIO | Ellato\_3.KSVG06799\_Cmd | KSVG06799 |
| QW2.83.8.5 | All outputs | DwordIO |  |  |
| IW2.83.8.6 | UnitStatus | HwStatus |  |  |

Table 571. Controller\_3.2.83.8 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.83.8.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.8.2 | Ellato\_3.KSVG06775\_Cmd |  |  |  |  | false |
| QX2.83.8.3 | Ellato\_3.U\_14C42\_Tart.EqM05Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.8.4 | Ellato\_3.KSVG06799\_Cmd |  |  |  |  | false |
| QW2.83.8.5 |  |  |  |  |  |  |

Table 572. Controller\_3.2.83.8 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.83.8.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.83.8.2 |  |  |  | Ellato\_3.KSVG06775\_Cmd |
| QX2.83.8.3 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM05Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.83.8.4 |  |  |  | Ellato\_3.KSVG06799\_Cmd |
| QW2.83.8.5 |  |  |  |  |

Table 573. Controller\_3.2.83.8 DO910\* (DO4) (Status)

##### 9 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 574. Controller\_3 (172.16.96.153).2.83.9 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.83.9.1 | Output 1 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM05Hax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04265 |
| QX2.83.9.2 | Output 2 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM05Hax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG04266 |
| QX2.83.9.3 | Output 3 | BoolIO |  |  |
| QX2.83.9.4 | Output 4 | BoolIO |  |  |
| QW2.83.9.5 | All outputs | DwordIO |  |  |
| IW2.83.9.6 | UnitStatus | HwStatus |  |  |

Table 575. Controller\_3.2.83.9 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.83.9.1 | Ellato\_3.U\_14C42\_Tart.EqM05Hax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.9.2 | Ellato\_3.U\_14C42\_Tart.EqM05Hax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.83.9.3 |  |  |  |  |  | false |
| QX2.83.9.4 |  |  |  |  |  | false |
| QW2.83.9.5 |  |  |  |  |  |  |

Table 576. Controller\_3.2.83.9 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.83.9.1 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM05Hax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.83.9.2 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM05Hax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.83.9.3 |  |  |  |  |
| QX2.83.9.4 |  |  |  |  |
| QW2.83.9.5 |  |  |  |  |

Table 577. Controller\_3.2.83.9 DO910\* (DO4) (Status)

##### 11 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 578. Controller\_3 (172.16.96.153).2.83.11 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.83.11.1 | Input 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04661 |
| IX2.83.11.2 | Input 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1B.GCS\_1\_IO.FB0 | KSVG04676 |
| IX2.83.11.3 | Input 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1C.GCS\_1\_IO.FB0 | KSVG04686 |
| IX2.83.11.4 | Input 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM12A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04736 |
| IX2.83.11.5 | Input 5 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM10C.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04751 |
| IX2.83.11.6 | Input 6 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM10C.GCS\_1B.GCS\_1\_IO.FB0 | KSVG04752 |
| IX2.83.11.7 | Input 7 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM21A\_A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04756 |
| IX2.83.11.8 | Input 8 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM21A\_B.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04757 |
| IW2.83.11.9 | All inputs | DwordIO |  |  |
| QX2.83.11.10 | Output 1 | BoolIO |  |  |
| QX2.83.11.11 | Output 2 | BoolIO |  |  |
| QX2.83.11.12 | Output 3 | BoolIO |  |  |
| QX2.83.11.13 | Output 4 | BoolIO |  |  |
| QX2.83.11.14 | Output 5 | BoolIO |  |  |
| QX2.83.11.15 | Output 6 | BoolIO |  |  |
| QX2.83.11.16 | Output 7 | BoolIO |  |  |
| QX2.83.11.17 | Output 8 | BoolIO |  |  |
| QW2.83.11.18 | All outputs | DwordIO |  |  |
| IX2.83.11.19 | Status Input 1 | BoolIO |  |  |
| IX2.83.11.20 | Status Input 2 | BoolIO |  |  |
| IX2.83.11.21 | Status Input 3 | BoolIO |  |  |
| IX2.83.11.22 | Status Input 4 | BoolIO |  |  |
| IX2.83.11.23 | Status Input 5 | BoolIO |  |  |
| IX2.83.11.24 | Status Input 6 | BoolIO |  |  |
| IX2.83.11.25 | Status Input 7 | BoolIO |  |  |
| IX2.83.11.26 | Status Input 8 | BoolIO |  |  |
| IW2.83.11.27 | UnitStatus | HwStatus |  |  |

Table 579. Controller\_3.2.83.11 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.83.11.1 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.11.2 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.11.3 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.11.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM12A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.11.5 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM10C.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.11.6 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM10C.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.11.7 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM21A\_A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.11.8 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM21A\_B.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW2.83.11.9 |  |  |  |  |  |  |
| QX2.83.11.10 |  |  |  |  |  | false |
| QX2.83.11.11 |  |  |  |  |  | false |
| QX2.83.11.12 |  |  |  |  |  | false |
| QX2.83.11.13 |  |  |  |  |  | false |
| QX2.83.11.14 |  |  |  |  |  | false |
| QX2.83.11.15 |  |  |  |  |  | false |
| QX2.83.11.16 |  |  |  |  |  | false |
| QX2.83.11.17 |  |  |  |  |  | false |
| QW2.83.11.18 |  |  |  |  |  |  |
| IX2.83.11.19 |  |  |  |  |  | false |
| IX2.83.11.20 |  |  |  |  |  | false |
| IX2.83.11.21 |  |  |  |  |  | false |
| IX2.83.11.22 |  |  |  |  |  | false |
| IX2.83.11.23 |  |  |  |  |  | false |
| IX2.83.11.24 |  |  |  |  |  | false |
| IX2.83.11.25 |  |  |  |  |  | false |
| IX2.83.11.26 |  |  |  |  |  | false |

Table 580. Controller\_3.2.83.11 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.83.11.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.83.11.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.83.11.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM05B.GCS\_1C.GCS\_1\_IO.FB0 |
| IX2.83.11.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM12A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.83.11.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM10C.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.83.11.6 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM10C.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.83.11.7 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM21A\_A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.83.11.8 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM21A\_B.GCS\_1A.GCS\_1\_IO.FB0 |
| IW2.83.11.9 |  |  |  |  |
| QX2.83.11.10 |  |  |  |  |
| QX2.83.11.11 |  |  |  |  |
| QX2.83.11.12 |  |  |  |  |
| QX2.83.11.13 |  |  |  |  |
| QX2.83.11.14 |  |  |  |  |
| QX2.83.11.15 |  |  |  |  |
| QX2.83.11.16 |  |  |  |  |
| QX2.83.11.17 |  |  |  |  |
| QW2.83.11.18 |  |  |  |  |
| IX2.83.11.19 |  |  |  |  |
| IX2.83.11.20 |  |  |  |  |
| IX2.83.11.21 |  |  |  |  |
| IX2.83.11.22 |  |  |  |  |
| IX2.83.11.23 |  |  |  |  |
| IX2.83.11.24 |  |  |  |  |
| IX2.83.11.25 |  |  |  |  |
| IX2.83.11.26 |  |  |  |  |

Table 581. Controller\_3.2.83.11 DX910\* (DIO8 S) (Status)

##### 12 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 582. Controller\_3 (172.16.96.153).2.83.12 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.83.12.1 | Input 1 | BoolIO | Ellato\_3.U\_PL\_C3.EqMPLE.PL\_1.PL\_1\_IO | PL14B31 |
| IX2.83.12.2 | Input 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 | KSVG04762 |
| IX2.83.12.3 | Input 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.GCS\_1B.GCS\_1\_IO.FB0 | KSVG04763 |
| IX2.83.12.4 | Input 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM04A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04764 |
| IX2.83.12.5 | Input 5 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04766 |
| IX2.83.12.6 | Input 6 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1B.GCS\_1\_IO.FB0 | KSVG04767 |
| IX2.83.12.7 | Input 7 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM03A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04768 |
| IX2.83.12.8 | Input 8 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM03B.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04769 |
| IW2.83.12.9 | All inputs | DwordIO |  |  |
| QX2.83.12.10 | Output 1 | BoolIO |  |  |
| QX2.83.12.11 | Output 2 | BoolIO |  |  |
| QX2.83.12.12 | Output 3 | BoolIO |  |  |
| QX2.83.12.13 | Output 4 | BoolIO |  |  |
| QX2.83.12.14 | Output 5 | BoolIO |  |  |
| QX2.83.12.15 | Output 6 | BoolIO |  |  |
| QX2.83.12.16 | Output 7 | BoolIO |  |  |
| QX2.83.12.17 | Output 8 | BoolIO |  |  |
| QW2.83.12.18 | All outputs | DwordIO |  |  |
| IX2.83.12.19 | Status Input 1 | BoolIO |  |  |
| IX2.83.12.20 | Status Input 2 | BoolIO |  |  |
| IX2.83.12.21 | Status Input 3 | BoolIO |  |  |
| IX2.83.12.22 | Status Input 4 | BoolIO |  |  |
| IX2.83.12.23 | Status Input 5 | BoolIO |  |  |
| IX2.83.12.24 | Status Input 6 | BoolIO |  |  |
| IX2.83.12.25 | Status Input 7 | BoolIO |  |  |
| IX2.83.12.26 | Status Input 8 | BoolIO |  |  |
| IW2.83.12.27 | UnitStatus | HwStatus |  |  |

Table 583. Controller\_3.2.83.12 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.83.12.1 | Ellato\_3.U\_PL\_C3.EqMPLE.PL\_1.PL\_1\_IO |  |  |  |  | false |
| IX2.83.12.2 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.12.3 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.12.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM04A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.12.5 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.12.6 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.12.7 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM03A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.12.8 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM03B.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW2.83.12.9 |  |  |  |  |  |  |
| QX2.83.12.10 |  |  |  |  |  | false |
| QX2.83.12.11 |  |  |  |  |  | false |
| QX2.83.12.12 |  |  |  |  |  | false |
| QX2.83.12.13 |  |  |  |  |  | false |
| QX2.83.12.14 |  |  |  |  |  | false |
| QX2.83.12.15 |  |  |  |  |  | false |
| QX2.83.12.16 |  |  |  |  |  | false |
| QX2.83.12.17 |  |  |  |  |  | false |
| QW2.83.12.18 |  |  |  |  |  |  |
| IX2.83.12.19 |  |  |  |  |  | false |
| IX2.83.12.20 |  |  |  |  |  | false |
| IX2.83.12.21 |  |  |  |  |  | false |
| IX2.83.12.22 |  |  |  |  |  | false |
| IX2.83.12.23 |  |  |  |  |  | false |
| IX2.83.12.24 |  |  |  |  |  | false |
| IX2.83.12.25 |  |  |  |  |  | false |
| IX2.83.12.26 |  |  |  |  |  | false |

Table 584. Controller\_3.2.83.12 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.83.12.1 |  |  |  | Ellato\_3.U\_PL\_C3.EqMPLE.PL\_1.PL\_1\_IO |
| IX2.83.12.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.CNT\_1.GCS\_1.GCS\_1\_IO.FB0 |
| IX2.83.12.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM01.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.83.12.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM04A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.83.12.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.83.12.6 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.83.12.7 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM03A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.83.12.8 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM03B.GCS\_1A.GCS\_1\_IO.FB0 |
| IW2.83.12.9 |  |  |  |  |
| QX2.83.12.10 |  |  |  |  |
| QX2.83.12.11 |  |  |  |  |
| QX2.83.12.12 |  |  |  |  |
| QX2.83.12.13 |  |  |  |  |
| QX2.83.12.14 |  |  |  |  |
| QX2.83.12.15 |  |  |  |  |
| QX2.83.12.16 |  |  |  |  |
| QX2.83.12.17 |  |  |  |  |
| QW2.83.12.18 |  |  |  |  |
| IX2.83.12.19 |  |  |  |  |
| IX2.83.12.20 |  |  |  |  |
| IX2.83.12.21 |  |  |  |  |
| IX2.83.12.22 |  |  |  |  |
| IX2.83.12.23 |  |  |  |  |
| IX2.83.12.24 |  |  |  |  |
| IX2.83.12.25 |  |  |  |  |
| IX2.83.12.26 |  |  |  |  |

Table 585. Controller\_3.2.83.12 DX910\* (DIO8 S) (Status)

##### 13 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 586. Controller\_3 (172.16.96.153).2.83.13 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.83.13.1 | Input 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1C.GCS\_1\_IO.FB0 | KSVG04771 |
| IX2.83.13.2 | Input 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1D.GCS\_1\_IO.FB0 | KSVG04772 |
| IX2.83.13.3 | Input 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1C.GCS\_1\_IO.FB0 | KSVG04773 |
| IX2.83.13.4 | Input 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1C.GCS\_1\_IO.FB0 | KSVG04774 |
| IX2.83.13.5 | Input 5 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_A.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 | KSVG04775 |
| IX2.83.13.6 | Input 6 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_B.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 | KSVG04776 |
| IX2.83.13.7 | Input 7 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04777 |
| IX2.83.13.8 | Input 8 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1G.GCS\_1\_IO.FB0 | KSVG04794 |
| IW2.83.13.9 | All inputs | DwordIO |  |  |
| QX2.83.13.10 | Output 1 | BoolIO |  |  |
| QX2.83.13.11 | Output 2 | BoolIO |  |  |
| QX2.83.13.12 | Output 3 | BoolIO |  |  |
| QX2.83.13.13 | Output 4 | BoolIO |  |  |
| QX2.83.13.14 | Output 5 | BoolIO |  |  |
| QX2.83.13.15 | Output 6 | BoolIO |  |  |
| QX2.83.13.16 | Output 7 | BoolIO |  |  |
| QX2.83.13.17 | Output 8 | BoolIO |  |  |
| QW2.83.13.18 | All outputs | DwordIO |  |  |
| IX2.83.13.19 | Status Input 1 | BoolIO |  |  |
| IX2.83.13.20 | Status Input 2 | BoolIO |  |  |
| IX2.83.13.21 | Status Input 3 | BoolIO |  |  |
| IX2.83.13.22 | Status Input 4 | BoolIO |  |  |
| IX2.83.13.23 | Status Input 5 | BoolIO |  |  |
| IX2.83.13.24 | Status Input 6 | BoolIO |  |  |
| IX2.83.13.25 | Status Input 7 | BoolIO |  |  |
| IX2.83.13.26 | Status Input 8 | BoolIO |  |  |
| IW2.83.13.27 | UnitStatus | HwStatus |  |  |

Table 587. Controller\_3.2.83.13 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.83.13.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.13.2 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1D.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.13.3 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.13.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.13.5 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_A.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.13.6 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_B.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.13.7 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.13.8 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1G.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW2.83.13.9 |  |  |  |  |  |  |
| QX2.83.13.10 |  |  |  |  |  | false |
| QX2.83.13.11 |  |  |  |  |  | false |
| QX2.83.13.12 |  |  |  |  |  | false |
| QX2.83.13.13 |  |  |  |  |  | false |
| QX2.83.13.14 |  |  |  |  |  | false |
| QX2.83.13.15 |  |  |  |  |  | false |
| QX2.83.13.16 |  |  |  |  |  | false |
| QX2.83.13.17 |  |  |  |  |  | false |
| QW2.83.13.18 |  |  |  |  |  |  |
| IX2.83.13.19 |  |  |  |  |  | false |
| IX2.83.13.20 |  |  |  |  |  | false |
| IX2.83.13.21 |  |  |  |  |  | false |
| IX2.83.13.22 |  |  |  |  |  | false |
| IX2.83.13.23 |  |  |  |  |  | false |
| IX2.83.13.24 |  |  |  |  |  | false |
| IX2.83.13.25 |  |  |  |  |  | false |
| IX2.83.13.26 |  |  |  |  |  | false |

Table 588. Controller\_3.2.83.13 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.83.13.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1C.GCS\_1\_IO.FB0 |
| IX2.83.13.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02D.GCS\_1D.GCS\_1\_IO.FB0 |
| IX2.83.13.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1C.GCS\_1\_IO.FB0 |
| IX2.83.13.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1C.GCS\_1\_IO.FB0 |
| IX2.83.13.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_A.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 |
| IX2.83.13.6 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM27A\_B.CNT\_1Tax3.GCS\_1.GCS\_1\_IO.FB0 |
| IX2.83.13.7 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM02A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.83.13.8 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1G.GCS\_1\_IO.FB0 |
| IW2.83.13.9 |  |  |  |  |
| QX2.83.13.10 |  |  |  |  |
| QX2.83.13.11 |  |  |  |  |
| QX2.83.13.12 |  |  |  |  |
| QX2.83.13.13 |  |  |  |  |
| QX2.83.13.14 |  |  |  |  |
| QX2.83.13.15 |  |  |  |  |
| QX2.83.13.16 |  |  |  |  |
| QX2.83.13.17 |  |  |  |  |
| QW2.83.13.18 |  |  |  |  |
| IX2.83.13.19 |  |  |  |  |
| IX2.83.13.20 |  |  |  |  |
| IX2.83.13.21 |  |  |  |  |
| IX2.83.13.22 |  |  |  |  |
| IX2.83.13.23 |  |  |  |  |
| IX2.83.13.24 |  |  |  |  |
| IX2.83.13.25 |  |  |  |  |
| IX2.83.13.26 |  |  |  |  |

Table 589. Controller\_3.2.83.13 DX910\* (DIO8 S) (Status)

##### 14 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 590. Controller\_3 (172.16.96.153).2.83.14 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.83.14.1 | Input 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM11.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04780 |
| IX2.83.14.2 | Input 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04781 |
| IX2.83.14.3 | Input 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04782 |
| IX2.83.14.4 | Input 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1B.GCS\_1\_IO.FB0 | KSVG04783 |
| IX2.83.14.5 | Input 5 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1B.GCS\_1\_IO.FB0 | KSVG04784 |
| IX2.83.14.6 | Input 6 | BoolIO | Ellato\_3.KSVG06775\_FB0 | KSVG06775 |
| IX2.83.14.7 | Input 7 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM05Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04261 |
| IX2.83.14.8 | Input 8 | BoolIO | Ellato\_3.KSVG06799\_FB0 | KSVG06799 |
| IW2.83.14.9 | All inputs | DwordIO |  |  |
| QX2.83.14.10 | Output 1 | BoolIO |  |  |
| QX2.83.14.11 | Output 2 | BoolIO |  |  |
| QX2.83.14.12 | Output 3 | BoolIO |  |  |
| QX2.83.14.13 | Output 4 | BoolIO |  |  |
| QX2.83.14.14 | Output 5 | BoolIO |  |  |
| QX2.83.14.15 | Output 6 | BoolIO |  |  |
| QX2.83.14.16 | Output 7 | BoolIO |  |  |
| QX2.83.14.17 | Output 8 | BoolIO |  |  |
| QW2.83.14.18 | All outputs | DwordIO |  |  |
| IX2.83.14.19 | Status Input 1 | BoolIO |  |  |
| IX2.83.14.20 | Status Input 2 | BoolIO |  |  |
| IX2.83.14.21 | Status Input 3 | BoolIO |  |  |
| IX2.83.14.22 | Status Input 4 | BoolIO |  |  |
| IX2.83.14.23 | Status Input 5 | BoolIO |  |  |
| IX2.83.14.24 | Status Input 6 | BoolIO |  |  |
| IX2.83.14.25 | Status Input 7 | BoolIO |  |  |
| IX2.83.14.26 | Status Input 8 | BoolIO |  |  |
| IW2.83.14.27 | UnitStatus | HwStatus |  |  |

Table 591. Controller\_3.2.83.14 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.83.14.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM11.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.14.2 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.14.3 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.14.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.14.5 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.14.6 | Ellato\_3.KSVG06775\_FB0 |  |  |  |  | false |
| IX2.83.14.7 | Ellato\_3.U\_14C42\_Tart.EqM05Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.14.8 | Ellato\_3.KSVG06799\_FB0 |  |  |  |  | false |
| QW2.83.14.9 |  |  |  |  |  |  |
| QX2.83.14.10 |  |  |  |  |  | false |
| QX2.83.14.11 |  |  |  |  |  | false |
| QX2.83.14.12 |  |  |  |  |  | false |
| QX2.83.14.13 |  |  |  |  |  | false |
| QX2.83.14.14 |  |  |  |  |  | false |
| QX2.83.14.15 |  |  |  |  |  | false |
| QX2.83.14.16 |  |  |  |  |  | false |
| QX2.83.14.17 |  |  |  |  |  | false |
| QW2.83.14.18 |  |  |  |  |  |  |
| IX2.83.14.19 |  |  |  |  |  | false |
| IX2.83.14.20 |  |  |  |  |  | false |
| IX2.83.14.21 |  |  |  |  |  | false |
| IX2.83.14.22 |  |  |  |  |  | false |
| IX2.83.14.23 |  |  |  |  |  | false |
| IX2.83.14.24 |  |  |  |  |  | false |
| IX2.83.14.25 |  |  |  |  |  | false |
| IX2.83.14.26 |  |  |  |  |  | false |

Table 592. Controller\_3.2.83.14 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.83.14.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM11.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.83.14.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.83.14.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.83.14.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_A.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.83.14.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM22B\_B.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.83.14.6 |  |  |  | Ellato\_3.KSVG06775\_FB0 |
| IX2.83.14.7 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM05Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.83.14.8 |  |  |  | Ellato\_3.KSVG06799\_FB0 |
| IW2.83.14.9 |  |  |  |  |
| QX2.83.14.10 |  |  |  |  |
| QX2.83.14.11 |  |  |  |  |
| QX2.83.14.12 |  |  |  |  |
| QX2.83.14.13 |  |  |  |  |
| QX2.83.14.14 |  |  |  |  |
| QX2.83.14.15 |  |  |  |  |
| QX2.83.14.16 |  |  |  |  |
| QX2.83.14.17 |  |  |  |  |
| QW2.83.14.18 |  |  |  |  |
| IX2.83.14.19 |  |  |  |  |
| IX2.83.14.20 |  |  |  |  |
| IX2.83.14.21 |  |  |  |  |
| IX2.83.14.22 |  |  |  |  |
| IX2.83.14.23 |  |  |  |  |
| IX2.83.14.24 |  |  |  |  |
| IX2.83.14.25 |  |  |  |  |
| IX2.83.14.26 |  |  |  |  |

Table 593. Controller\_3.2.83.14 DX910\* (DIO8 S) (Status)

##### 15 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | true | bool |  |  |  |
| ch. 5: deactivate | true | bool |  |  |  |
| ch. 6: deactivate | true | bool |  |  |  |
| ch. 7: deactivate | true | bool |  |  |  |
| ch. 8: deactivate | true | bool |  |  |  |

Table 594. Controller\_3 (172.16.96.153).2.83.15 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.83.15.1 | Input 1 | BoolIO | Ellato\_3.KSVG06757\_FB0 | KSVG06757 |
| IX2.83.15.2 | Input 2 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM05Hax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04265 |
| IX2.83.15.3 | Input 3 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM05Hax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG04266 |
| IX2.83.15.4 | Input 4 | BoolIO |  |  |
| IX2.83.15.5 | Input 5 | BoolIO |  |  |
| IX2.83.15.6 | Input 6 | BoolIO |  |  |
| IX2.83.15.7 | Input 7 | BoolIO |  |  |
| IX2.83.15.8 | Input 8 | BoolIO |  |  |
| IW2.83.15.9 | All inputs | DwordIO |  |  |
| QX2.83.15.10 | Output 1 | BoolIO |  |  |
| QX2.83.15.11 | Output 2 | BoolIO |  |  |
| QX2.83.15.12 | Output 3 | BoolIO |  |  |
| QX2.83.15.13 | Output 4 | BoolIO |  |  |
| QX2.83.15.14 | Output 5 | BoolIO |  |  |
| QX2.83.15.15 | Output 6 | BoolIO |  |  |
| QX2.83.15.16 | Output 7 | BoolIO |  |  |
| QX2.83.15.17 | Output 8 | BoolIO |  |  |
| QW2.83.15.18 | All outputs | DwordIO |  |  |
| IX2.83.15.19 | Status Input 1 | BoolIO |  |  |
| IX2.83.15.20 | Status Input 2 | BoolIO |  |  |
| IX2.83.15.21 | Status Input 3 | BoolIO |  |  |
| IX2.83.15.22 | Status Input 4 | BoolIO |  |  |
| IX2.83.15.23 | Status Input 5 | BoolIO |  |  |
| IX2.83.15.24 | Status Input 6 | BoolIO |  |  |
| IX2.83.15.25 | Status Input 7 | BoolIO |  |  |
| IX2.83.15.26 | Status Input 8 | BoolIO |  |  |
| IW2.83.15.27 | UnitStatus | HwStatus |  |  |

Table 595. Controller\_3.2.83.15 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.83.15.1 | Ellato\_3.KSVG06757\_FB0 |  |  |  |  | false |
| IX2.83.15.2 | Ellato\_3.U\_14C42\_Tart.EqM05Hax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.15.3 | Ellato\_3.U\_14C42\_Tart.EqM05Hax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.83.15.4 |  |  |  |  |  | false |
| IX2.83.15.5 |  |  |  |  |  | false |
| IX2.83.15.6 |  |  |  |  |  | false |
| IX2.83.15.7 |  |  |  |  |  | false |
| IX2.83.15.8 |  |  |  |  |  | false |
| QW2.83.15.9 |  |  |  |  |  |  |
| QX2.83.15.10 |  |  |  |  |  | false |
| QX2.83.15.11 |  |  |  |  |  | false |
| QX2.83.15.12 |  |  |  |  |  | false |
| QX2.83.15.13 |  |  |  |  |  | false |
| QX2.83.15.14 |  |  |  |  |  | false |
| QX2.83.15.15 |  |  |  |  |  | false |
| QX2.83.15.16 |  |  |  |  |  | false |
| QX2.83.15.17 |  |  |  |  |  | false |
| QW2.83.15.18 |  |  |  |  |  |  |
| IX2.83.15.19 |  |  |  |  |  | false |
| IX2.83.15.20 |  |  |  |  |  | false |
| IX2.83.15.21 |  |  |  |  |  | false |
| IX2.83.15.22 |  |  |  |  |  | false |
| IX2.83.15.23 |  |  |  |  |  | false |
| IX2.83.15.24 |  |  |  |  |  | false |
| IX2.83.15.25 |  |  |  |  |  | false |
| IX2.83.15.26 |  |  |  |  |  | false |

Table 596. Controller\_3.2.83.15 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.83.15.1 |  |  |  | Ellato\_3.KSVG06757\_FB0 |
| IX2.83.15.2 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM05Hax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.83.15.3 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM05Hax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.83.15.4 |  |  |  |  |
| IX2.83.15.5 |  |  |  |  |
| IX2.83.15.6 |  |  |  |  |
| IX2.83.15.7 |  |  |  |  |
| IX2.83.15.8 |  |  |  |  |
| IW2.83.15.9 |  |  |  |  |
| QX2.83.15.10 |  |  |  |  |
| QX2.83.15.11 |  |  |  |  |
| QX2.83.15.12 |  |  |  |  |
| QX2.83.15.13 |  |  |  |  |
| QX2.83.15.14 |  |  |  |  |
| QX2.83.15.15 |  |  |  |  |
| QX2.83.15.16 |  |  |  |  |
| QX2.83.15.17 |  |  |  |  |
| QW2.83.15.18 |  |  |  |  |
| IX2.83.15.19 |  |  |  |  |
| IX2.83.15.20 |  |  |  |  |
| IX2.83.15.21 |  |  |  |  |
| IX2.83.15.22 |  |  |  |  |
| IX2.83.15.23 |  |  |  |  |
| IX2.83.15.24 |  |  |  |  |
| IX2.83.15.25 |  |  |  |  |
| IX2.83.15.26 |  |  |  |  |

Table 597. Controller\_3.2.83.15 DX910\* (DIO8 S) (Status)

#### 84 S900

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.84.0 | UnitStatus | HwStatus |  |  |

Table 598. Controller\_3.2.84 S900 (Connections)

##### 0 CI920\* (CIPB)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Grid Frequency | 50 Hz | enum |  |  |  |
| Analog Status | on | enum |  |  |  |
| Internal Bus Redundancy | false | bool |  |  |  |
| CIPB Redundancy | false | bool |  |  |  |
| Power Supply Redundancy | false | bool |  |  |  |

Table 599. Controller\_3 (172.16.96.153).2.84.0 CI920\* (CIPB) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW2.84.0.1 | UnitStatus | HwStatus |  |  |

Table 600. Controller\_3.2.84.0 CI920\* (CIPB) (Connections)

##### 1 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 601. Controller\_3 (172.16.96.153).2.84.1 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.84.1.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04788 |
| QX2.84.1.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1B.GCS\_1\_IO.Cmd | KSVG04789 |
| QX2.84.1.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1C.GCS\_1\_IO.Cmd | KSVG04790 |
| QX2.84.1.4 | Output 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1D.GCS\_1\_IO.Cmd | KSVG04791 |
| QW2.84.1.5 | All outputs | DwordIO |  |  |
| IW2.84.1.6 | UnitStatus | HwStatus |  |  |

Table 602. Controller\_3.2.84.1 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.84.1.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.84.1.2 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.84.1.3 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.84.1.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1D.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.84.1.5 |  |  |  |  |  |  |

Table 603. Controller\_3.2.84.1 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.84.1.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.84.1.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1B.GCS\_1\_IO.Cmd |
| QX2.84.1.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1C.GCS\_1\_IO.Cmd |
| QX2.84.1.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1D.GCS\_1\_IO.Cmd |
| QW2.84.1.5 |  |  |  |  |

Table 604. Controller\_3.2.84.1 DO910\* (DO4) (Status)

##### 2 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 605. Controller\_3 (172.16.96.153).2.84.2 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.84.2.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1E.GCS\_1\_IO.Cmd | KSVG04792 |
| QX2.84.2.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1F.GCS\_1\_IO.Cmd | KSVG04793 |
| QX2.84.2.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1H.GCS\_1\_IO.Cmd | KSVG04795 |
| QX2.84.2.4 | Output 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1I.GCS\_1\_IO.Cmd | KSVG04796 |
| QW2.84.2.5 | All outputs | DwordIO |  |  |
| IW2.84.2.6 | UnitStatus | HwStatus |  |  |

Table 606. Controller\_3.2.84.2 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.84.2.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1E.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.84.2.2 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1F.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.84.2.3 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1H.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.84.2.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1I.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW2.84.2.5 |  |  |  |  |  |  |

Table 607. Controller\_3.2.84.2 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.84.2.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1E.GCS\_1\_IO.Cmd |
| QX2.84.2.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1F.GCS\_1\_IO.Cmd |
| QX2.84.2.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1H.GCS\_1\_IO.Cmd |
| QX2.84.2.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1I.GCS\_1\_IO.Cmd |
| QW2.84.2.5 |  |  |  |  |

Table 608. Controller\_3.2.84.2 DO910\* (DO4) (Status)

##### 3 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 609. Controller\_3 (172.16.96.153).2.84.3 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.84.3.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1J.GCS\_1\_IO.Cmd | KSVG04797 |
| QX2.84.3.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1K.GCS\_1\_IO.Cmd | KSVG04798 |
| QX2.84.3.3 | Output 3 | BoolIO | Ellato\_3.KSVG06779\_Cmd | KSVG06779 |
| QX2.84.3.4 | Output 4 | BoolIO | Ellato\_3.KSVG06874\_Cmd | KSVG06874 |
| QW2.84.3.5 | All outputs | DwordIO |  |  |
| IW2.84.3.6 | UnitStatus | HwStatus |  |  |

Table 610. Controller\_3.2.84.3 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.84.3.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1J.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.84.3.2 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1K.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.84.3.3 | Ellato\_3.KSVG06779\_Cmd |  |  |  |  | false |
| QX2.84.3.4 | Ellato\_3.KSVG06874\_Cmd |  |  |  |  | false |
| QW2.84.3.5 |  |  |  |  |  |  |

Table 611. Controller\_3.2.84.3 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.84.3.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1J.GCS\_1\_IO.Cmd |
| QX2.84.3.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1K.GCS\_1\_IO.Cmd |
| QX2.84.3.3 |  |  |  | Ellato\_3.KSVG06779\_Cmd |
| QX2.84.3.4 |  |  |  | Ellato\_3.KSVG06874\_Cmd |
| QW2.84.3.5 |  |  |  |  |

Table 612. Controller\_3.2.84.3 DO910\* (DO4) (Status)

##### 4 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 613. Controller\_3 (172.16.96.153).2.84.4 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.84.4.1 | Output 1 | BoolIO | Ellato\_3.KSVG06774\_Cmd | KSVG06774 |
| QX2.84.4.2 | Output 2 | BoolIO | Ellato\_3.KSVG06773\_Cmd | KSVG06773 |
| QX2.84.4.3 | Output 3 | BoolIO | Ellato\_3.KSVG06758\_Cmd | KSVG06758 |
| QX2.84.4.4 | Output 4 | BoolIO | Ellato\_3.KSVG06794\_Cmd | KSVG06794 |
| QW2.84.4.5 | All outputs | DwordIO |  |  |
| IW2.84.4.6 | UnitStatus | HwStatus |  |  |

Table 614. Controller\_3.2.84.4 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.84.4.1 | Ellato\_3.KSVG06774\_Cmd |  |  |  |  | false |
| QX2.84.4.2 | Ellato\_3.KSVG06773\_Cmd |  |  |  |  | false |
| QX2.84.4.3 | Ellato\_3.KSVG06758\_Cmd |  |  |  |  | false |
| QX2.84.4.4 | Ellato\_3.KSVG06794\_Cmd |  |  |  |  | false |
| QW2.84.4.5 |  |  |  |  |  |  |

Table 615. Controller\_3.2.84.4 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.84.4.1 |  |  |  | Ellato\_3.KSVG06774\_Cmd |
| QX2.84.4.2 |  |  |  | Ellato\_3.KSVG06773\_Cmd |
| QX2.84.4.3 |  |  |  | Ellato\_3.KSVG06758\_Cmd |
| QX2.84.4.4 |  |  |  | Ellato\_3.KSVG06794\_Cmd |
| QW2.84.4.5 |  |  |  |  |

Table 616. Controller\_3.2.84.4 DO910\* (DO4) (Status)

##### 5 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 617. Controller\_3 (172.16.96.153).2.84.5 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX2.84.5.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM11ax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03980 |
| QX2.84.5.2 | Output 2 | BoolIO |  |  |
| QX2.84.5.3 | Output 3 | BoolIO |  |  |
| QX2.84.5.4 | Output 4 | BoolIO |  |  |
| QW2.84.5.5 | All outputs | DwordIO |  |  |
| IW2.84.5.6 | UnitStatus | HwStatus |  |  |

Table 618. Controller\_3.2.84.5 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX2.84.5.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM11ax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX2.84.5.2 |  |  |  |  |  | false |
| QX2.84.5.3 |  |  |  |  |  | false |
| QX2.84.5.4 |  |  |  |  |  | false |
| QW2.84.5.5 |  |  |  |  |  |  |

Table 619. Controller\_3.2.84.5 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX2.84.5.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM11ax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX2.84.5.2 |  |  |  |  |
| QX2.84.5.3 |  |  |  |  |
| QX2.84.5.4 |  |  |  |  |
| QW2.84.5.5 |  |  |  |  |

Table 620. Controller\_3.2.84.5 DO910\* (DO4) (Status)

##### 11 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 621. Controller\_3 (172.16.96.153).2.84.11 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.84.11.1 | Input 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04788 |
| IX2.84.11.2 | Input 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1B.GCS\_1\_IO.FB0 | KSVG04789 |
| IX2.84.11.3 | Input 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1C.GCS\_1\_IO.FB0 | KSVG04790 |
| IX2.84.11.4 | Input 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1D.GCS\_1\_IO.FB0 | KSVG04791 |
| IX2.84.11.5 | Input 5 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1E.GCS\_1\_IO.FB0 | KSVG04792 |
| IX2.84.11.6 | Input 6 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1F.GCS\_1\_IO.FB0 | KSVG04793 |
| IX2.84.11.7 | Input 7 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1H.GCS\_1\_IO.FB0 | KSVG04795 |
| IX2.84.11.8 | Input 8 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1I.GCS\_1\_IO.FB0 | KSVG04796 |
| IW2.84.11.9 | All inputs | DwordIO |  |  |
| QX2.84.11.10 | Output 1 | BoolIO |  |  |
| QX2.84.11.11 | Output 2 | BoolIO |  |  |
| QX2.84.11.12 | Output 3 | BoolIO |  |  |
| QX2.84.11.13 | Output 4 | BoolIO |  |  |
| QX2.84.11.14 | Output 5 | BoolIO |  |  |
| QX2.84.11.15 | Output 6 | BoolIO |  |  |
| QX2.84.11.16 | Output 7 | BoolIO |  |  |
| QX2.84.11.17 | Output 8 | BoolIO |  |  |
| QW2.84.11.18 | All outputs | DwordIO |  |  |
| IX2.84.11.19 | Status Input 1 | BoolIO |  |  |
| IX2.84.11.20 | Status Input 2 | BoolIO |  |  |
| IX2.84.11.21 | Status Input 3 | BoolIO |  |  |
| IX2.84.11.22 | Status Input 4 | BoolIO |  |  |
| IX2.84.11.23 | Status Input 5 | BoolIO |  |  |
| IX2.84.11.24 | Status Input 6 | BoolIO |  |  |
| IX2.84.11.25 | Status Input 7 | BoolIO |  |  |
| IX2.84.11.26 | Status Input 8 | BoolIO |  |  |
| IW2.84.11.27 | UnitStatus | HwStatus |  |  |

Table 622. Controller\_3.2.84.11 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.84.11.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.84.11.2 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.84.11.3 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.84.11.4 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1D.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.84.11.5 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1E.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.84.11.6 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1F.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.84.11.7 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1H.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.84.11.8 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1I.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW2.84.11.9 |  |  |  |  |  |  |
| QX2.84.11.10 |  |  |  |  |  | false |
| QX2.84.11.11 |  |  |  |  |  | false |
| QX2.84.11.12 |  |  |  |  |  | false |
| QX2.84.11.13 |  |  |  |  |  | false |
| QX2.84.11.14 |  |  |  |  |  | false |
| QX2.84.11.15 |  |  |  |  |  | false |
| QX2.84.11.16 |  |  |  |  |  | false |
| QX2.84.11.17 |  |  |  |  |  | false |
| QW2.84.11.18 |  |  |  |  |  |  |
| IX2.84.11.19 |  |  |  |  |  | false |
| IX2.84.11.20 |  |  |  |  |  | false |
| IX2.84.11.21 |  |  |  |  |  | false |
| IX2.84.11.22 |  |  |  |  |  | false |
| IX2.84.11.23 |  |  |  |  |  | false |
| IX2.84.11.24 |  |  |  |  |  | false |
| IX2.84.11.25 |  |  |  |  |  | false |
| IX2.84.11.26 |  |  |  |  |  | false |

Table 623. Controller\_3.2.84.11 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.84.11.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.84.11.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1B.GCS\_1\_IO.FB0 |
| IX2.84.11.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1C.GCS\_1\_IO.FB0 |
| IX2.84.11.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1D.GCS\_1\_IO.FB0 |
| IX2.84.11.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1E.GCS\_1\_IO.FB0 |
| IX2.84.11.6 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1F.GCS\_1\_IO.FB0 |
| IX2.84.11.7 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1H.GCS\_1\_IO.FB0 |
| IX2.84.11.8 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1I.GCS\_1\_IO.FB0 |
| IW2.84.11.9 |  |  |  |  |
| QX2.84.11.10 |  |  |  |  |
| QX2.84.11.11 |  |  |  |  |
| QX2.84.11.12 |  |  |  |  |
| QX2.84.11.13 |  |  |  |  |
| QX2.84.11.14 |  |  |  |  |
| QX2.84.11.15 |  |  |  |  |
| QX2.84.11.16 |  |  |  |  |
| QX2.84.11.17 |  |  |  |  |
| QW2.84.11.18 |  |  |  |  |
| IX2.84.11.19 |  |  |  |  |
| IX2.84.11.20 |  |  |  |  |
| IX2.84.11.21 |  |  |  |  |
| IX2.84.11.22 |  |  |  |  |
| IX2.84.11.23 |  |  |  |  |
| IX2.84.11.24 |  |  |  |  |
| IX2.84.11.25 |  |  |  |  |
| IX2.84.11.26 |  |  |  |  |

Table 624. Controller\_3.2.84.11 DX910\* (DIO8 S) (Status)

##### 12 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 625. Controller\_3 (172.16.96.153).2.84.12 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.84.12.1 | Input 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1J.GCS\_1\_IO.FB0 | KSVG04797 |
| IX2.84.12.2 | Input 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1K.GCS\_1\_IO.FB0 | KSVG04798 |
| IX2.84.12.3 | Input 3 | BoolIO | Ellato\_3.KSVG06779\_FB0 | KSVG06779 |
| IX2.84.12.4 | Input 4 | BoolIO | Ellato\_3.KSVG06874\_FB0 | KSVG06874 |
| IX2.84.12.5 | Input 5 | BoolIO | Ellato\_3.KSVG06774\_FB0 | KSVG06774 |
| IX2.84.12.6 | Input 6 | BoolIO | Ellato\_3.KSVG06773\_FB0 | KSVG06773 |
| IX2.84.12.7 | Input 7 | BoolIO | Ellato\_3.KSVG06758\_FB0 | KSVG06758 |
| IX2.84.12.8 | Input 8 | BoolIO | Ellato\_3.KSVG06794\_FB0 | KSVG06794 |
| IW2.84.12.9 | All inputs | DwordIO |  |  |
| QX2.84.12.10 | Output 1 | BoolIO |  |  |
| QX2.84.12.11 | Output 2 | BoolIO |  |  |
| QX2.84.12.12 | Output 3 | BoolIO |  |  |
| QX2.84.12.13 | Output 4 | BoolIO |  |  |
| QX2.84.12.14 | Output 5 | BoolIO |  |  |
| QX2.84.12.15 | Output 6 | BoolIO |  |  |
| QX2.84.12.16 | Output 7 | BoolIO |  |  |
| QX2.84.12.17 | Output 8 | BoolIO |  |  |
| QW2.84.12.18 | All outputs | DwordIO |  |  |
| IX2.84.12.19 | Status Input 1 | BoolIO |  |  |
| IX2.84.12.20 | Status Input 2 | BoolIO |  |  |
| IX2.84.12.21 | Status Input 3 | BoolIO |  |  |
| IX2.84.12.22 | Status Input 4 | BoolIO |  |  |
| IX2.84.12.23 | Status Input 5 | BoolIO |  |  |
| IX2.84.12.24 | Status Input 6 | BoolIO |  |  |
| IX2.84.12.25 | Status Input 7 | BoolIO |  |  |
| IX2.84.12.26 | Status Input 8 | BoolIO |  |  |
| IW2.84.12.27 | UnitStatus | HwStatus |  |  |

Table 626. Controller\_3.2.84.12 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.84.12.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1J.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.84.12.2 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1K.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.84.12.3 | Ellato\_3.KSVG06779\_FB0 |  |  |  |  | false |
| IX2.84.12.4 | Ellato\_3.KSVG06874\_FB0 |  |  |  |  | false |
| IX2.84.12.5 | Ellato\_3.KSVG06774\_FB0 |  |  |  |  | false |
| IX2.84.12.6 | Ellato\_3.KSVG06773\_FB0 |  |  |  |  | false |
| IX2.84.12.7 | Ellato\_3.KSVG06758\_FB0 |  |  |  |  | false |
| IX2.84.12.8 | Ellato\_3.KSVG06794\_FB0 |  |  |  |  | false |
| QW2.84.12.9 |  |  |  |  |  |  |
| QX2.84.12.10 |  |  |  |  |  | false |
| QX2.84.12.11 |  |  |  |  |  | false |
| QX2.84.12.12 |  |  |  |  |  | false |
| QX2.84.12.13 |  |  |  |  |  | false |
| QX2.84.12.14 |  |  |  |  |  | false |
| QX2.84.12.15 |  |  |  |  |  | false |
| QX2.84.12.16 |  |  |  |  |  | false |
| QX2.84.12.17 |  |  |  |  |  | false |
| QW2.84.12.18 |  |  |  |  |  |  |
| IX2.84.12.19 |  |  |  |  |  | false |
| IX2.84.12.20 |  |  |  |  |  | false |
| IX2.84.12.21 |  |  |  |  |  | false |
| IX2.84.12.22 |  |  |  |  |  | false |
| IX2.84.12.23 |  |  |  |  |  | false |
| IX2.84.12.24 |  |  |  |  |  | false |
| IX2.84.12.25 |  |  |  |  |  | false |
| IX2.84.12.26 |  |  |  |  |  | false |

Table 627. Controller\_3.2.84.12 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.84.12.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1J.GCS\_1\_IO.FB0 |
| IX2.84.12.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.GCS\_1K.GCS\_1\_IO.FB0 |
| IX2.84.12.3 |  |  |  | Ellato\_3.KSVG06779\_FB0 |
| IX2.84.12.4 |  |  |  | Ellato\_3.KSVG06874\_FB0 |
| IX2.84.12.5 |  |  |  | Ellato\_3.KSVG06774\_FB0 |
| IX2.84.12.6 |  |  |  | Ellato\_3.KSVG06773\_FB0 |
| IX2.84.12.7 |  |  |  | Ellato\_3.KSVG06758\_FB0 |
| IX2.84.12.8 |  |  |  | Ellato\_3.KSVG06794\_FB0 |
| IW2.84.12.9 |  |  |  |  |
| QX2.84.12.10 |  |  |  |  |
| QX2.84.12.11 |  |  |  |  |
| QX2.84.12.12 |  |  |  |  |
| QX2.84.12.13 |  |  |  |  |
| QX2.84.12.14 |  |  |  |  |
| QX2.84.12.15 |  |  |  |  |
| QX2.84.12.16 |  |  |  |  |
| QX2.84.12.17 |  |  |  |  |
| QW2.84.12.18 |  |  |  |  |
| IX2.84.12.19 |  |  |  |  |
| IX2.84.12.20 |  |  |  |  |
| IX2.84.12.21 |  |  |  |  |
| IX2.84.12.22 |  |  |  |  |
| IX2.84.12.23 |  |  |  |  |
| IX2.84.12.24 |  |  |  |  |
| IX2.84.12.25 |  |  |  |  |
| IX2.84.12.26 |  |  |  |  |

Table 628. Controller\_3.2.84.12 DX910\* (DIO8 S) (Status)

##### 13 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | true | bool |  |  |  |
| ch. 4: deactivate | true | bool |  |  |  |
| ch. 5: deactivate | true | bool |  |  |  |
| ch. 6: deactivate | true | bool |  |  |  |
| ch. 7: deactivate | true | bool |  |  |  |
| ch. 8: deactivate | true | bool |  |  |  |

Table 629. Controller\_3 (172.16.96.153).2.84.13 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX2.84.13.1 | Input 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM11ax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03980 |
| IX2.84.13.2 | Input 2 | BoolIO | Ellato\_3.U\_PL\_C3.EqMPLD.PL\_1.PL\_1\_IO | PL 14B32 |
| IX2.84.13.3 | Input 3 | BoolIO |  |  |
| IX2.84.13.4 | Input 4 | BoolIO |  |  |
| IX2.84.13.5 | Input 5 | BoolIO |  |  |
| IX2.84.13.6 | Input 6 | BoolIO |  |  |
| IX2.84.13.7 | Input 7 | BoolIO |  |  |
| IX2.84.13.8 | Input 8 | BoolIO |  |  |
| IW2.84.13.9 | All inputs | DwordIO |  |  |
| QX2.84.13.10 | Output 1 | BoolIO |  |  |
| QX2.84.13.11 | Output 2 | BoolIO |  |  |
| QX2.84.13.12 | Output 3 | BoolIO |  |  |
| QX2.84.13.13 | Output 4 | BoolIO |  |  |
| QX2.84.13.14 | Output 5 | BoolIO |  |  |
| QX2.84.13.15 | Output 6 | BoolIO |  |  |
| QX2.84.13.16 | Output 7 | BoolIO |  |  |
| QX2.84.13.17 | Output 8 | BoolIO |  |  |
| QW2.84.13.18 | All outputs | DwordIO |  |  |
| IX2.84.13.19 | Status Input 1 | BoolIO |  |  |
| IX2.84.13.20 | Status Input 2 | BoolIO |  |  |
| IX2.84.13.21 | Status Input 3 | BoolIO |  |  |
| IX2.84.13.22 | Status Input 4 | BoolIO |  |  |
| IX2.84.13.23 | Status Input 5 | BoolIO |  |  |
| IX2.84.13.24 | Status Input 6 | BoolIO |  |  |
| IX2.84.13.25 | Status Input 7 | BoolIO |  |  |
| IX2.84.13.26 | Status Input 8 | BoolIO |  |  |
| IW2.84.13.27 | UnitStatus | HwStatus |  |  |

Table 630. Controller\_3.2.84.13 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX2.84.13.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM11ax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX2.84.13.2 | Ellato\_3.U\_PL\_C3.EqMPLD.PL\_1.PL\_1\_IO |  |  |  |  | false |
| IX2.84.13.3 |  |  |  |  |  | false |
| IX2.84.13.4 |  |  |  |  |  | false |
| IX2.84.13.5 |  |  |  |  |  | false |
| IX2.84.13.6 |  |  |  |  |  | false |
| IX2.84.13.7 |  |  |  |  |  | false |
| IX2.84.13.8 |  |  |  |  |  | false |
| QW2.84.13.9 |  |  |  |  |  |  |
| QX2.84.13.10 |  |  |  |  |  | false |
| QX2.84.13.11 |  |  |  |  |  | false |
| QX2.84.13.12 |  |  |  |  |  | false |
| QX2.84.13.13 |  |  |  |  |  | false |
| QX2.84.13.14 |  |  |  |  |  | false |
| QX2.84.13.15 |  |  |  |  |  | false |
| QX2.84.13.16 |  |  |  |  |  | false |
| QX2.84.13.17 |  |  |  |  |  | false |
| QW2.84.13.18 |  |  |  |  |  |  |
| IX2.84.13.19 |  |  |  |  |  | false |
| IX2.84.13.20 |  |  |  |  |  | false |
| IX2.84.13.21 |  |  |  |  |  | false |
| IX2.84.13.22 |  |  |  |  |  | false |
| IX2.84.13.23 |  |  |  |  |  | false |
| IX2.84.13.24 |  |  |  |  |  | false |
| IX2.84.13.25 |  |  |  |  |  | false |
| IX2.84.13.26 |  |  |  |  |  | false |

Table 631. Controller\_3.2.84.13 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX2.84.13.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM11ax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX2.84.13.2 |  |  |  | Ellato\_3.U\_PL\_C3.EqMPLD.PL\_1.PL\_1\_IO |
| IX2.84.13.3 |  |  |  |  |
| IX2.84.13.4 |  |  |  |  |
| IX2.84.13.5 |  |  |  |  |
| IX2.84.13.6 |  |  |  |  |
| IX2.84.13.7 |  |  |  |  |
| IX2.84.13.8 |  |  |  |  |
| IW2.84.13.9 |  |  |  |  |
| QX2.84.13.10 |  |  |  |  |
| QX2.84.13.11 |  |  |  |  |
| QX2.84.13.12 |  |  |  |  |
| QX2.84.13.13 |  |  |  |  |
| QX2.84.13.14 |  |  |  |  |
| QX2.84.13.15 |  |  |  |  |
| QX2.84.13.16 |  |  |  |  |
| QX2.84.13.17 |  |  |  |  |
| QW2.84.13.18 |  |  |  |  |
| IX2.84.13.19 |  |  |  |  |
| IX2.84.13.20 |  |  |  |  |
| IX2.84.13.21 |  |  |  |  |
| IX2.84.13.22 |  |  |  |  |
| IX2.84.13.23 |  |  |  |  |
| IX2.84.13.24 |  |  |  |  |
| IX2.84.13.25 |  |  |  |  |
| IX2.84.13.26 |  |  |  |  |

Table 632. Controller\_3.2.84.13 DX910\* (DIO8 S) (Status)

### 3 (9) CI854

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Node address (FDL\_Add) | 1 | dint |  | 1 | 125 |
| Baudrate | 1.5M | enum | bit/s |  |  |
| Slot time (TSL) | 300 | dint | tbit | 37 | 16383 |
| Min station delay (min TSDR) | 11 | dint | tbit | 11 | 1023 |
| Max station delay (max TSDR) | 150 | dint | tbit | 37 | 65535 |
| Quiet time (TQui) | 0 | dint | tbit | 0 | 127 |
| Setup time (TSet) | 20 | dint | tbit | 1 | 255 |
| Target rotation time (TTR) | 39681 | dint | tbit | 255 | 999999 |
| Gap factor (G) | 10 | dint |  | 1 | 100 |
| Highest station address (HSA) | 126 | dint |  | 1 | 126 |
| Max retry limit | 1 | dint |  | 0 | 7 |
| BP flag | Auto Clear OFF | enum |  |  |  |
| Min slave interval | 10 | dint | 100us | 1 | 65535 |
| Poll time out | 50 | dint | ms | 1 | 65535 |
| Data control time | 1014 | dint | 10ms | 1 | 65535 |
| Watchdog time | 5070 | dint | ms | 10 | 650250 |
| Line redundancy | Disabled | enum |  |  |  |
| Delta TTR | 0 | dint | % | 0 | 100 |
| Calculation method | Actual value based | enum |  |  |  |

Table 633. Controller\_3 (172.16.96.153).3 CI854 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.0 | UnitStatus | HwStatus |  |  |

Table 634. Controller\_3.3 CI854 (Connections)

#### 11 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.11.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 635. Controller\_3.3.11 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.11.0.0 | Temp1 Value | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO | TIT01008 |
| IW3.11.0.1 | Temp1 Status | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO\_Status |  |
| IW3.11.0.2 | UnitStatus | HwStatus |  |  |

Table 636. Controller\_3.3.11.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.11.0.0 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW3.11.0.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO\_Status |  |  |  |  |  |

Table 637. Controller\_3.3.11.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.11.0.0 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO |
| IW3.11.0.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO\_Status |

Table 638. Controller\_3.3.11.0 Temperature 1 (Status)

#### 12 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.12.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 639. Controller\_3.3.12 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.12.0.0 | Temp1 Value | RealIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO | TIT03908 |
| IW3.12.0.1 | Temp1 Status | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO\_Status |  |
| IW3.12.0.2 | UnitStatus | HwStatus |  |  |

Table 640. Controller\_3.3.12.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.12.0.0 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW3.12.0.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO\_Status |  |  |  |  |  |

Table 641. Controller\_3.3.12.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.12.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO |
| IW3.12.0.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO\_Status |

Table 642. Controller\_3.3.12.0 Temperature 1 (Status)

#### 13 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.13.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 643. Controller\_3.3.13 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.13.0.0 | Temp1 Value | RealIO | Nyers\_Drospiren\_3.U\_19A90\_00.IND\_1.IND\_1\_IO | TIT09001 |
| IW3.13.0.1 | Temp1 Status | DintIO | Nyers\_Drospiren\_3.U\_19A90\_00.IND\_1.IND\_1\_IO\_Status |  |
| IW3.13.0.2 | UnitStatus | HwStatus |  |  |

Table 644. Controller\_3.3.13.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.13.0.0 | Nyers\_Drospiren\_3.U\_19A90\_00.IND\_1.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW3.13.0.1 | Nyers\_Drospiren\_3.U\_19A90\_00.IND\_1.IND\_1\_IO\_Status |  |  |  |  |  |

Table 645. Controller\_3.3.13.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.13.0.0 |  |  |  | Nyers\_Drospiren\_3.U\_19A90\_00.IND\_1.IND\_1\_IO |
| IW3.13.0.1 |  |  |  | Nyers\_Drospiren\_3.U\_19A90\_00.IND\_1.IND\_1\_IO\_Status |

Table 646. Controller\_3.3.13.0 Temperature 1 (Status)

#### 18 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.18.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 647. Controller\_3.3.18 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.18.0.0 | Temp1 Value | RealIO | Ellato\_3.TIT01302 | TIT01302 |
| IW3.18.0.1 | Temp1 Status | DintIO | Ellato\_3.TIT01302\_Status |  |
| IW3.18.0.2 | UnitStatus | HwStatus |  |  |

Table 648. Controller\_3.3.18.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.18.0.0 | Ellato\_3.TIT01302 | -50.0 | 150.0 | °C | 2 | false |
| IW3.18.0.1 | Ellato\_3.TIT01302\_Status |  |  |  |  |  |

Table 649. Controller\_3.3.18.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.18.0.0 |  |  |  | Ellato\_3.TIT01302 |
| IW3.18.0.1 |  |  |  | Ellato\_3.TIT01302\_Status |

Table 650. Controller\_3.3.18.0 Temperature 1 (Status)

#### 19 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.19.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 651. Controller\_3.3.19 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.19.0.0 | Temp1 Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_1A.IND\_1\_IO | TIT02602 |
| IW3.19.0.1 | Temp1 Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_1A.IND\_1\_IO\_Status |  |
| IW3.19.0.2 | UnitStatus | HwStatus |  |  |

Table 652. Controller\_3.3.19.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.19.0.0 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_1A.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW3.19.0.1 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_1A.IND\_1\_IO\_Status |  |  |  |  |  |

Table 653. Controller\_3.3.19.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.19.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_1A.IND\_1\_IO |
| IW3.19.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_1A.IND\_1\_IO\_Status |

Table 654. Controller\_3.3.19.0 Temperature 1 (Status)

#### 20 Pressure Transmitter (9700)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.20.0 | UnitStatus | HwStatus |  |  |

Table 655. Controller\_3.3.20 Pressure Transmitter (9700) (Connections)

##### 0 Analog Input (AI)short

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.20.0.0 | Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.CNT\_4B.IND\_5.IND\_5\_IO | PIT02611 |
| IW3.20.0.1 | Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.CNT\_4B.IND\_5.IND\_5\_Status |  |
| IW3.20.0.2 | UnitStatus | HwStatus |  |  |

Table 656. Controller\_3.3.20.0 Analog Input (AI)short (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.20.0.0 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.CNT\_4B.IND\_5.IND\_5\_IO | -1.0 | 11.0 | bar | 2 | false |
| IW3.20.0.1 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.CNT\_4B.IND\_5.IND\_5\_Status |  |  |  |  |  |

Table 657. Controller\_3.3.20.0 Analog Input (AI)short (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.20.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.CNT\_4B.IND\_5.IND\_5\_IO |
| IW3.20.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.CNT\_4B.IND\_5.IND\_5\_Status |

Table 658. Controller\_3.3.20.0 Analog Input (AI)short (Status)

#### 21 Pressure Transmitter (9700)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.21.0 | UnitStatus | HwStatus |  |  |

Table 659. Controller\_3.3.21 Pressure Transmitter (9700) (Connections)

##### 0 Analog Input (AI)short

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.21.0.0 | Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5C.IND\_5\_IO | PIT02612 |
| IW3.21.0.1 | Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5C.IND\_5\_Status |  |
| IW3.21.0.2 | UnitStatus | HwStatus |  |  |

Table 660. Controller\_3.3.21.0 Analog Input (AI)short (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.21.0.0 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5C.IND\_5\_IO | -1.0 | 11.0 | bar | 2 | false |
| IW3.21.0.1 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5C.IND\_5\_Status |  |  |  |  |  |

Table 661. Controller\_3.3.21.0 Analog Input (AI)short (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.21.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5C.IND\_5\_IO |
| IW3.21.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5C.IND\_5\_Status |

Table 662. Controller\_3.3.21.0 Analog Input (AI)short (Status)

#### 22 Pressure Transmitter (9700)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.22.0 | UnitStatus | HwStatus |  |  |

Table 663. Controller\_3.3.22 Pressure Transmitter (9700) (Connections)

##### 0 Analog Input (AI)short

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.22.0.0 | Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5A.IND\_5\_IO | PTI02616 |
| IW3.22.0.1 | Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5A.IND\_5\_Status |  |
| IW3.22.0.2 | UnitStatus | HwStatus |  |  |

Table 664. Controller\_3.3.22.0 Analog Input (AI)short (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.22.0.0 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5A.IND\_5\_IO | -1.0 | 11.0 | bar | 2 | false |
| IW3.22.0.1 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5A.IND\_5\_Status |  |  |  |  |  |

Table 665. Controller\_3.3.22.0 Analog Input (AI)short (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.22.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5A.IND\_5\_IO |
| IW3.22.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5A.IND\_5\_Status |

Table 666. Controller\_3.3.22.0 Analog Input (AI)short (Status)

#### 23 Pressure Transmitter (9700)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.23.0 | UnitStatus | HwStatus |  |  |

Table 667. Controller\_3.3.23 Pressure Transmitter (9700) (Connections)

##### 0 Analog Input (AI)short

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.23.0.0 | Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5B.IND\_5\_IO | PTI02617 |
| IW3.23.0.1 | Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5B.IND\_5\_Status |  |
| IW3.23.0.2 | UnitStatus | HwStatus |  |  |

Table 668. Controller\_3.3.23.0 Analog Input (AI)short (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.23.0.0 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5B.IND\_5\_IO | -1.0 | 11.0 | bar | 2 | false |
| IW3.23.0.1 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5B.IND\_5\_Status |  |  |  |  |  |

Table 669. Controller\_3.3.23.0 Analog Input (AI)short (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.23.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5B.IND\_5\_IO |
| IW3.23.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_5B.IND\_5\_Status |

Table 670. Controller\_3.3.23.0 Analog Input (AI)short (Status)

#### 25 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.25.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 671. Controller\_3.3.25 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.25.0.0 | Temp1 Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO | TIT04708 |
| IW3.25.0.1 | Temp1 Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO\_Status |  |
| IW3.25.0.2 | UnitStatus | HwStatus |  |  |

Table 672. Controller\_3.3.25.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.25.0.0 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW3.25.0.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO\_Status |  |  |  |  |  |

Table 673. Controller\_3.3.25.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.25.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO |
| IW3.25.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.K5\_TC.Transmitter\_JacketTemp.IND\_1\_IO\_Status |

Table 674. Controller\_3.3.25.0 Temperature 1 (Status)

#### 26 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.26.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 675. Controller\_3.3.26 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.26.0.0 | Temp1 Value | RealIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_B.IND\_1\_IO | TIT01006 |
| IW3.26.0.1 | Temp1 Status | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_B.IND\_1\_IO\_Status |  |
| IW3.26.0.2 | UnitStatus | HwStatus |  |  |

Table 676. Controller\_3.3.26.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.26.0.0 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_B.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW3.26.0.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_B.IND\_1\_IO\_Status |  |  |  |  |  |

Table 677. Controller\_3.3.26.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.26.0.0 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_B.IND\_1\_IO |
| IW3.26.0.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.IND\_1\_B.IND\_1\_IO\_Status |

Table 678. Controller\_3.3.26.0 Temperature 1 (Status)

#### 27 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.27.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 679. Controller\_3.3.27 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.27.0.0 | Temp1 Value | RealIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax\_C.IND\_1\_IO | TIT03906 |
| IW3.27.0.1 | Temp1 Status | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax\_C.IND\_1\_IO\_Status |  |
| IW3.27.0.2 | UnitStatus | HwStatus |  |  |

Table 680. Controller\_3.3.27.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.27.0.0 | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax\_C.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW3.27.0.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax\_C.IND\_1\_IO\_Status |  |  |  |  |  |

Table 681. Controller\_3.3.27.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.27.0.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax\_C.IND\_1\_IO |
| IW3.27.0.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.IND\_1ax\_C.IND\_1\_IO\_Status |

Table 682. Controller\_3.3.27.0 Temperature 1 (Status)

#### 28 ABB\_TFx12\_PA

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.28.0 | UnitStatus V3\_0\_12 | HwStatus |  |  |

Table 683. Controller\_3.3.28 ABB\_TFx12\_PA (Connections)

##### 0 Temperature 1

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.28.0.0 | Temp1 Value | RealIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_1B.IND\_1\_IO | TIT04706 |
| IW3.28.0.1 | Temp1 Status | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_1B.IND\_1\_IO\_Status |  |
| IW3.28.0.2 | UnitStatus | HwStatus |  |  |

Table 684. Controller\_3.3.28.0 Temperature 1 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.28.0.0 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_1B.IND\_1\_IO | -50.0 | 150.0 | °C | 2 | false |
| IW3.28.0.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_1B.IND\_1\_IO\_Status |  |  |  |  |  |

Table 685. Controller\_3.3.28.0 Temperature 1 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.28.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_1B.IND\_1\_IO |
| IW3.28.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.IND\_1B.IND\_1\_IO\_Status |

Table 686. Controller\_3.3.28.0 Temperature 1 (Status)

#### 60 (124) CI840

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Output Hold Time | 15 | dint | 10 ms | 15 | 190 |
| Power Supervision | false | bool |  |  |  |
| Power Supervision opto extension | false | bool |  |  |  |
| Extended HART | false | bool |  |  |  |
| Redundant module | true | bool |  |  |  |

Table 687. Controller\_3 (172.16.96.153).3.60 CI840 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.60.1 | FCI diag | DwordIO |  |  |
| IW3.60.2 | UnitStatus | HwStatus |  |  |

Table 688. Controller\_3.3.60 CI840 (Connections)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.60.1 |  |  |  |  |

Table 689. Controller\_3.3.60 CI840 (Status)

##### 1 DI810

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Filter time | 2 | enum | ms |  |  |
| Sensor power supervision | true | bool |  |  |  |
| Activate channel 1 | Active | enum |  |  |  |
| Activate channel 2 | Active | enum |  |  |  |
| Activate channel 3 | Active | enum |  |  |  |
| Activate channel 4 | Active | enum |  |  |  |
| Activate channel 5 | Active | enum |  |  |  |
| Activate channel 6 | Active | enum |  |  |  |
| Activate channel 7 | Active | enum |  |  |  |
| Activate channel 8 | Active | enum |  |  |  |
| Activate channel 9 | Active | enum |  |  |  |
| Activate channel 10 | Active | enum |  |  |  |
| Activate channel 11 | Active | enum |  |  |  |
| Activate channel 12 | Active | enum |  |  |  |
| Activate channel 13 | Active | enum |  |  |  |
| Activate channel 14 | Active | enum |  |  |  |
| Activate channel 15 | Active | enum |  |  |  |
| Activate channel 16 | Active | enum |  |  |  |
| ISP Control channel 1 | Keep current value | enum |  |  |  |
| ISP Control channel 2 | Keep current value | enum |  |  |  |
| ISP Control channel 3 | Keep current value | enum |  |  |  |
| ISP Control channel 4 | Keep current value | enum |  |  |  |
| ISP Control channel 5 | Keep current value | enum |  |  |  |
| ISP Control channel 6 | Keep current value | enum |  |  |  |
| ISP Control channel 7 | Keep current value | enum |  |  |  |
| ISP Control channel 8 | Keep current value | enum |  |  |  |
| ISP Control channel 9 | Keep current value | enum |  |  |  |
| ISP Control channel 10 | Keep current value | enum |  |  |  |
| ISP Control channel 11 | Keep current value | enum |  |  |  |
| ISP Control channel 12 | Keep current value | enum |  |  |  |
| ISP Control channel 13 | Keep current value | enum |  |  |  |
| ISP Control channel 14 | Keep current value | enum |  |  |  |
| ISP Control channel 15 | Keep current value | enum |  |  |  |
| ISP Control channel 16 | Keep current value | enum |  |  |  |
| ISP Value channel 1 | false | bool |  |  |  |
| ISP Value channel 2 | false | bool |  |  |  |
| ISP Value channel 3 | false | bool |  |  |  |
| ISP Value channel 4 | false | bool |  |  |  |
| ISP Value channel 5 | false | bool |  |  |  |
| ISP Value channel 6 | false | bool |  |  |  |
| ISP Value channel 7 | false | bool |  |  |  |
| ISP Value channel 8 | false | bool |  |  |  |
| ISP Value channel 9 | false | bool |  |  |  |
| ISP Value channel 10 | false | bool |  |  |  |
| ISP Value channel 11 | false | bool |  |  |  |
| ISP Value channel 12 | false | bool |  |  |  |
| ISP Value channel 13 | false | bool |  |  |  |
| ISP Value channel 14 | false | bool |  |  |  |
| ISP Value channel 15 | false | bool |  |  |  |
| ISP Value channel 16 | false | bool |  |  |  |

Table 690. Controller\_3 (172.16.96.153).3.60.1 DI810 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX3.60.1.1 | Input 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM15Cax3.FVM\_2.FVM\_2\_IO.FB1 | OS00244 |
| IX3.60.1.2 | Input 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_A.FVM\_2.FVM\_2\_IO.FB1 | OS01035 |
| IX3.60.1.3 | Input 3 | BoolIO |  |  |
| IX3.60.1.4 | Input 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Bax3.FVM\_2.FVM\_2\_IO.FB1 | OS01254 |
| IX3.60.1.5 | Input 5 | BoolIO | Ellato\_3.U\_04C354\_00.EqM15I.FVM\_2.FVM\_2\_IO.FB1 | OS35425 |
| IX3.60.1.6 | Input 6 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM15B\_A.FVM\_2.FVM\_2\_IO.FB1 | OS00651 |
| IX3.60.1.7 | Input 7 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM15B\_B.FVM\_2.FVM\_2\_IO.FB1 | OS00652 |
| IX3.60.1.8 | Input 8 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.KSZ\_1.FVM\_2\_IO.FB1 | OS01099 |
| IX3.60.1.9 | Input 9 | BoolIO | Ellato\_3.OS06855\_FB1 | OS06855 |
| IX3.60.1.10 | Input 10 | BoolIO | Ellato\_3.OS06755\_FB1 | OS06755 |
| IX3.60.1.11 | Input 11 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_B.FVM\_2.FVM\_2\_IO.FB1 | OS01025 |
| IX3.60.1.12 | Input 12 | BoolIO | Ellato\_3.U\_04C91\_00.EqM15Iax3.FVM\_2.FVM\_2\_IO.FB1 | OS09156 |
| IX3.60.1.13 | Input 13 | BoolIO | Ciklopro\_Ad\_Enolet\_3.OS03135 | OS03135 |
| IX3.60.1.14 | Input 14 | BoolIO |  |  |
| IX3.60.1.15 | Input 15 | BoolIO |  |  |
| IX3.60.1.16 | Input 16 | BoolIO |  |  |
| IW3.60.1.17 | All Inputs | DwordIO |  |  |
| IW3.60.1.18 | Channel status | DwordIO |  |  |
| IW3.60.1.19 | UnitStatus | HwStatus |  |  |

Table 691. Controller\_3.3.60.1 DI810 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX3.60.1.1 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM15Cax3.FVM\_2.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.60.1.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_A.FVM\_2.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.60.1.3 |  |  |  |  |  | false |
| IX3.60.1.4 | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Bax3.FVM\_2.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.60.1.5 | Ellato\_3.U\_04C354\_00.EqM15I.FVM\_2.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.60.1.6 | Ellato\_3.U\_02C06\_Tart.EqM15B\_A.FVM\_2.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.60.1.7 | Ellato\_3.U\_02C06\_Tart.EqM15B\_B.FVM\_2.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.60.1.8 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.KSZ\_1.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.60.1.9 | Ellato\_3.OS06855\_FB1 |  |  |  |  | false |
| IX3.60.1.10 | Ellato\_3.OS06755\_FB1 |  |  |  |  | false |
| IX3.60.1.11 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_B.FVM\_2.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.60.1.12 | Ellato\_3.U\_04C91\_00.EqM15Iax3.FVM\_2.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.60.1.13 | Ciklopro\_Ad\_Enolet\_3.OS03135 |  |  |  |  | false |
| IX3.60.1.14 |  |  |  |  |  | false |
| IX3.60.1.15 |  |  |  |  |  | false |
| IX3.60.1.16 |  |  |  |  |  | false |
| QW3.60.1.17 |  |  |  |  |  |  |
| QW3.60.1.18 |  |  |  |  |  |  |

Table 692. Controller\_3.3.60.1 DI810 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX3.60.1.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM15Cax3.FVM\_2.FVM\_2\_IO.FB1 |
| IX3.60.1.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_A.FVM\_2.FVM\_2\_IO.FB1 |
| IX3.60.1.3 |  |  |  |  |
| IX3.60.1.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Bax3.FVM\_2.FVM\_2\_IO.FB1 |
| IX3.60.1.5 |  |  |  | Ellato\_3.U\_04C354\_00.EqM15I.FVM\_2.FVM\_2\_IO.FB1 |
| IX3.60.1.6 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM15B\_A.FVM\_2.FVM\_2\_IO.FB1 |
| IX3.60.1.7 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM15B\_B.FVM\_2.FVM\_2\_IO.FB1 |
| IX3.60.1.8 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.KSZ\_1.FVM\_2\_IO.FB1 |
| IX3.60.1.9 |  |  |  | Ellato\_3.OS06855\_FB1 |
| IX3.60.1.10 |  |  |  | Ellato\_3.OS06755\_FB1 |
| IX3.60.1.11 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_B.FVM\_2.FVM\_2\_IO.FB1 |
| IX3.60.1.12 |  |  |  | Ellato\_3.U\_04C91\_00.EqM15Iax3.FVM\_2.FVM\_2\_IO.FB1 |
| IX3.60.1.13 |  |  |  | Ciklopro\_Ad\_Enolet\_3.OS03135 |
| IX3.60.1.14 |  |  |  |  |
| IX3.60.1.15 |  |  |  |  |
| IX3.60.1.16 |  |  |  |  |
| IW3.60.1.17 |  |  |  |  |
| IW3.60.1.18 |  |  |  |  |

Table 693. Controller\_3.3.60.1 DI810 (Status)

##### 2 DO810

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| External power supervision | true | bool |  |  |  |
| Activate channel 1 | Active | enum |  |  |  |
| Activate channel 2 | Active | enum |  |  |  |
| Activate channel 3 | Active | enum |  |  |  |
| Activate channel 4 | Active | enum |  |  |  |
| Activate channel 5 | Active | enum |  |  |  |
| Activate channel 6 | Active | enum |  |  |  |
| Activate channel 7 | Active | enum |  |  |  |
| Activate channel 8 | Active | enum |  |  |  |
| Activate channel 9 | Active | enum |  |  |  |
| Activate channel 10 | Active | enum |  |  |  |
| Activate channel 11 | Active | enum |  |  |  |
| Activate channel 12 | Active | enum |  |  |  |
| Activate channel 13 | Active | enum |  |  |  |
| Activate channel 14 | Active | enum |  |  |  |
| Activate channel 15 | Active | enum |  |  |  |
| Activate channel 16 | Active | enum |  |  |  |
| OSP control channel 1 | Set OSP value | enum |  |  |  |
| OSP control channel 2 | Set OSP value | enum |  |  |  |
| OSP control channel 3 | Set OSP value | enum |  |  |  |
| OSP control channel 4 | Set OSP value | enum |  |  |  |
| OSP control channel 5 | Set OSP value | enum |  |  |  |
| OSP control channel 6 | Set OSP value | enum |  |  |  |
| OSP control channel 7 | Set OSP value | enum |  |  |  |
| OSP control channel 8 | Set OSP value | enum |  |  |  |
| OSP control channel 9 | Set OSP value | enum |  |  |  |
| OSP control channel 10 | Set OSP value | enum |  |  |  |
| OSP control channel 11 | Set OSP value | enum |  |  |  |
| OSP control channel 12 | Set OSP value | enum |  |  |  |
| OSP control channel 13 | Set OSP value | enum |  |  |  |
| OSP control channel 14 | Set OSP value | enum |  |  |  |
| OSP control channel 15 | Set OSP value | enum |  |  |  |
| OSP control channel 16 | Set OSP value | enum |  |  |  |
| OSP value channel 1 | false | bool |  |  |  |
| OSP value channel 2 | false | bool |  |  |  |
| OSP value channel 3 | false | bool |  |  |  |
| OSP value channel 4 | false | bool |  |  |  |
| OSP value channel 5 | false | bool |  |  |  |
| OSP value channel 6 | false | bool |  |  |  |
| OSP value channel 7 | false | bool |  |  |  |
| OSP value channel 8 | false | bool |  |  |  |
| OSP value channel 9 | false | bool |  |  |  |
| OSP value channel 10 | false | bool |  |  |  |
| OSP value channel 11 | false | bool |  |  |  |
| OSP value channel 12 | false | bool |  |  |  |
| OSP value channel 13 | false | bool |  |  |  |
| OSP value channel 14 | false | bool |  |  |  |
| OSP value channel 15 | false | bool |  |  |  |
| OSP value channel 16 | false | bool |  |  |  |

Table 694. Controller\_3 (172.16.96.153).3.60.2 DO810 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX3.60.2.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM15Cax3.FVM\_2.FVM\_2\_IO.Cmd | KS00244 |
| QX3.60.2.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_IO.Cmd | KS00255 |
| QX3.60.2.3 | Output 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_A.FVM\_2.FVM\_2\_IO.Cmd | KS01035 |
| QX3.60.2.4 | Output 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_IO.Cmd | KS01055 |
| QX3.60.2.5 | Output 5 | BoolIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.FVM\_1\_IO.Cmd | KS01252 |
| QX3.60.2.6 | Output 6 | BoolIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Bax3.FVM\_2.FVM\_2\_IO.Cmd | KS01254 |
| QX3.60.2.7 | Output 7 | BoolIO | Ellato\_3.U\_04C354\_00.EqM15I.FVM\_2.FVM\_2\_IO.Cmd | KS35425 |
| QX3.60.2.8 | Output 8 | BoolIO | Ciklopro\_Ad\_Enolet\_3.KS03135 | KS03135 |
| QX3.60.2.9 | Output 9 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.FVM\_1\_IO.Cmd | KS00655 |
| QX3.60.2.10 | Output 10 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM15B\_B.FVM\_2.FVM\_2\_IO.Cmd | KS00652 |
| QX3.60.2.11 | Output 11 | BoolIO | Ellato\_3.U\_02C06\_Tart.EqM15B\_A.FVM\_2.FVM\_2\_IO.Cmd | KS00651 |
| QX3.60.2.12 | Output 12 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.KSZ\_1.FVM\_2\_IO.Cmd | KS01099 |
| QX3.60.2.13 | Output 13 | BoolIO | Ellato\_3.KS06855\_Cmd | KS06855 |
| QX3.60.2.14 | Output 14 | BoolIO | Ellato\_3.KS06755\_Cmd | KS06755 |
| QX3.60.2.15 | Output 15 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_B.FVM\_2.FVM\_2\_IO.Cmd | KS01025 |
| QX3.60.2.16 | Output 16 | BoolIO | Ellato\_3.U\_04C91\_00.EqM15Iax3.FVM\_2.FVM\_2\_IO.Cmd | KS09156 |
| QW3.60.2.17 | All Outputs | DwordIO |  |  |
| IW3.60.2.18 | Channel status | DwordIO |  |  |
| IW3.60.2.19 | UnitStatus | HwStatus |  |  |

Table 695. Controller\_3.3.60.2 DO810 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX3.60.2.1 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM15Cax3.FVM\_2.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.60.2.2 | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_IO.Cmd |  |  |  |  | false |
| QX3.60.2.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_A.FVM\_2.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.60.2.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_IO.Cmd |  |  |  |  | false |
| QX3.60.2.5 | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.FVM\_1\_IO.Cmd |  |  |  |  | false |
| QX3.60.2.6 | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Bax3.FVM\_2.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.60.2.7 | Ellato\_3.U\_04C354\_00.EqM15I.FVM\_2.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.60.2.8 | Ciklopro\_Ad\_Enolet\_3.KS03135 |  |  |  |  | false |
| QX3.60.2.9 | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.FVM\_1\_IO.Cmd |  |  |  |  | false |
| QX3.60.2.10 | Ellato\_3.U\_02C06\_Tart.EqM15B\_B.FVM\_2.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.60.2.11 | Ellato\_3.U\_02C06\_Tart.EqM15B\_A.FVM\_2.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.60.2.12 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.KSZ\_1.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.60.2.13 | Ellato\_3.KS06855\_Cmd |  |  |  |  | false |
| QX3.60.2.14 | Ellato\_3.KS06755\_Cmd |  |  |  |  | false |
| QX3.60.2.15 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_B.FVM\_2.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.60.2.16 | Ellato\_3.U\_04C91\_00.EqM15Iax3.FVM\_2.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QW3.60.2.17 |  |  |  |  |  |  |
| QW3.60.2.18 |  |  |  |  |  |  |

Table 696. Controller\_3.3.60.2 DO810 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX3.60.2.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM15Cax3.FVM\_2.FVM\_2\_IO.Cmd |
| QX3.60.2.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_IO.Cmd |
| QX3.60.2.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_A.FVM\_2.FVM\_2\_IO.Cmd |
| QX3.60.2.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_IO.Cmd |
| QX3.60.2.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.FVM\_1\_IO.Cmd |
| QX3.60.2.6 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Bax3.FVM\_2.FVM\_2\_IO.Cmd |
| QX3.60.2.7 |  |  |  | Ellato\_3.U\_04C354\_00.EqM15I.FVM\_2.FVM\_2\_IO.Cmd |
| QX3.60.2.8 |  |  |  | Ciklopro\_Ad\_Enolet\_3.KS03135 |
| QX3.60.2.9 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.FVM\_1\_IO.Cmd |
| QX3.60.2.10 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM15B\_B.FVM\_2.FVM\_2\_IO.Cmd |
| QX3.60.2.11 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM15B\_A.FVM\_2.FVM\_2\_IO.Cmd |
| QX3.60.2.12 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM09Aax3.KSZ\_1.FVM\_2\_IO.Cmd |
| QX3.60.2.13 |  |  |  | Ellato\_3.KS06855\_Cmd |
| QX3.60.2.14 |  |  |  | Ellato\_3.KS06755\_Cmd |
| QX3.60.2.15 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_B.FVM\_2.FVM\_2\_IO.Cmd |
| QX3.60.2.16 |  |  |  | Ellato\_3.U\_04C91\_00.EqM15Iax3.FVM\_2.FVM\_2\_IO.Cmd |
| QW3.60.2.17 |  |  |  |  |
| IW3.60.2.18 |  |  |  |  |

Table 697. Controller\_3.3.60.2 DO810 (Status)

#### 61 (125) CI840

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Output Hold Time | 15 | dint | 10 ms | 15 | 190 |
| Power Supervision | false | bool |  |  |  |
| Power Supervision opto extension | false | bool |  |  |  |
| Extended HART | false | bool |  |  |  |
| Redundant module | true | bool |  |  |  |

Table 698. Controller\_3 (172.16.96.153).3.61 CI840 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.61.1 | FCI diag | DwordIO |  |  |
| IW3.61.2 | UnitStatus | HwStatus |  |  |

Table 699. Controller\_3.3.61 CI840 (Connections)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.61.1 |  |  |  |  |

Table 700. Controller\_3.3.61 CI840 (Status)

##### 1 DI810

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Filter time | 2 | enum | ms |  |  |
| Sensor power supervision | true | bool |  |  |  |
| Activate channel 1 | Active | enum |  |  |  |
| Activate channel 2 | Active | enum |  |  |  |
| Activate channel 3 | Active | enum |  |  |  |
| Activate channel 4 | Active | enum |  |  |  |
| Activate channel 5 | Active | enum |  |  |  |
| Activate channel 6 | Active | enum |  |  |  |
| Activate channel 7 | Active | enum |  |  |  |
| Activate channel 8 | Active | enum |  |  |  |
| Activate channel 9 | Active | enum |  |  |  |
| Activate channel 10 | Active | enum |  |  |  |
| Activate channel 11 | Active | enum |  |  |  |
| Activate channel 12 | Active | enum |  |  |  |
| Activate channel 13 | Active | enum |  |  |  |
| Activate channel 14 | Active | enum |  |  |  |
| Activate channel 15 | Active | enum |  |  |  |
| Activate channel 16 | Active | enum |  |  |  |
| ISP Control channel 1 | Keep current value | enum |  |  |  |
| ISP Control channel 2 | Keep current value | enum |  |  |  |
| ISP Control channel 3 | Keep current value | enum |  |  |  |
| ISP Control channel 4 | Keep current value | enum |  |  |  |
| ISP Control channel 5 | Keep current value | enum |  |  |  |
| ISP Control channel 6 | Keep current value | enum |  |  |  |
| ISP Control channel 7 | Keep current value | enum |  |  |  |
| ISP Control channel 8 | Keep current value | enum |  |  |  |
| ISP Control channel 9 | Keep current value | enum |  |  |  |
| ISP Control channel 10 | Keep current value | enum |  |  |  |
| ISP Control channel 11 | Keep current value | enum |  |  |  |
| ISP Control channel 12 | Keep current value | enum |  |  |  |
| ISP Control channel 13 | Keep current value | enum |  |  |  |
| ISP Control channel 14 | Keep current value | enum |  |  |  |
| ISP Control channel 15 | Keep current value | enum |  |  |  |
| ISP Control channel 16 | Keep current value | enum |  |  |  |
| ISP Value channel 1 | false | bool |  |  |  |
| ISP Value channel 2 | false | bool |  |  |  |
| ISP Value channel 3 | false | bool |  |  |  |
| ISP Value channel 4 | false | bool |  |  |  |
| ISP Value channel 5 | false | bool |  |  |  |
| ISP Value channel 6 | false | bool |  |  |  |
| ISP Value channel 7 | false | bool |  |  |  |
| ISP Value channel 8 | false | bool |  |  |  |
| ISP Value channel 9 | false | bool |  |  |  |
| ISP Value channel 10 | false | bool |  |  |  |
| ISP Value channel 11 | false | bool |  |  |  |
| ISP Value channel 12 | false | bool |  |  |  |
| ISP Value channel 13 | false | bool |  |  |  |
| ISP Value channel 14 | false | bool |  |  |  |
| ISP Value channel 15 | false | bool |  |  |  |
| ISP Value channel 16 | false | bool |  |  |  |

Table 701. Controller\_3 (172.16.96.153).3.61.1 DI810 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX3.61.1.1 | Input 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM15Bax3.FVM\_2.FVM\_2\_IO.FB1 | KS02454 |
| IX3.61.1.2 | Input 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.KSZ\_1.FVM\_2\_IO.FB1 | KS03999 |
| IX3.61.1.3 | Input 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.KSZ\_1.FVM\_2\_IO.FB1 | KS04799 |
| IX3.61.1.4 | Input 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_A.FVM\_2.FVM\_2\_IO.FB1 | KS02634 |
| IX3.61.1.5 | Input 5 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_B.FVM\_2.FVM\_2\_IO.FB1 | KS02635 |
| IX3.61.1.6 | Input 6 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_C.FVM\_2.FVM\_2\_IO.FB1 | KS02636 |
| IX3.61.1.7 | Input 7 | BoolIO | Ellato\_3.KS03535\_FB1 | KS03535 |
| IX3.61.1.8 | Input 8 | BoolIO | Nyers\_Drospiren\_3.U\_19A90\_00.EqM15Iax3.FVM\_2.FVM\_2\_IO.FB1 | KS09025 |
| IX3.61.1.9 | Input 9 | BoolIO |  |  |
| IX3.61.1.10 | Input 10 | BoolIO |  |  |
| IX3.61.1.11 | Input 11 | BoolIO |  |  |
| IX3.61.1.12 | Input 12 | BoolIO | Ellato\_3.OS04331\_FB1 | KS04331 |
| IX3.61.1.13 | Input 13 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.KSZ\_1.FVM\_2\_IO.FB1 | KS04699 |
| IX3.61.1.14 | Input 14 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM15Cax3.FVM\_2.FVM\_2\_IO.FB1 | KS03935 |
| IX3.61.1.15 | Input 15 | BoolIO |  |  |
| IX3.61.1.16 | Input 16 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM15C.FVM\_2.FVM\_2\_IO.FB1 | KS04635 |
| IW3.61.1.17 | All Inputs | DwordIO |  |  |
| IW3.61.1.18 | Channel status | DwordIO |  |  |
| IW3.61.1.19 | UnitStatus | HwStatus |  |  |

Table 702. Controller\_3.3.61.1 DI810 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX3.61.1.1 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM15Bax3.FVM\_2.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.61.1.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.KSZ\_1.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.61.1.3 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.KSZ\_1.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.61.1.4 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_A.FVM\_2.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.61.1.5 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_B.FVM\_2.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.61.1.6 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_C.FVM\_2.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.61.1.7 | Ellato\_3.KS03535\_FB1 |  |  |  |  | false |
| IX3.61.1.8 | Nyers\_Drospiren\_3.U\_19A90\_00.EqM15Iax3.FVM\_2.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.61.1.9 |  |  |  |  |  | false |
| IX3.61.1.10 |  |  |  |  |  | false |
| IX3.61.1.11 |  |  |  |  |  | false |
| IX3.61.1.12 | Ellato\_3.OS04331\_FB1 |  |  |  |  | false |
| IX3.61.1.13 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.KSZ\_1.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.61.1.14 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM15Cax3.FVM\_2.FVM\_2\_IO.FB1 |  |  |  |  | false |
| IX3.61.1.15 |  |  |  |  |  | false |
| IX3.61.1.16 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM15C.FVM\_2.FVM\_2\_IO.FB1 |  |  |  |  | false |
| QW3.61.1.17 |  |  |  |  |  |  |
| QW3.61.1.18 |  |  |  |  |  |  |

Table 703. Controller\_3.3.61.1 DI810 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX3.61.1.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM15Bax3.FVM\_2.FVM\_2\_IO.FB1 |
| IX3.61.1.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.KSZ\_1.FVM\_2\_IO.FB1 |
| IX3.61.1.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.KSZ\_1.FVM\_2\_IO.FB1 |
| IX3.61.1.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_A.FVM\_2.FVM\_2\_IO.FB1 |
| IX3.61.1.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_B.FVM\_2.FVM\_2\_IO.FB1 |
| IX3.61.1.6 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_C.FVM\_2.FVM\_2\_IO.FB1 |
| IX3.61.1.7 |  |  |  | Ellato\_3.KS03535\_FB1 |
| IX3.61.1.8 |  |  |  | Nyers\_Drospiren\_3.U\_19A90\_00.EqM15Iax3.FVM\_2.FVM\_2\_IO.FB1 |
| IX3.61.1.9 |  |  |  |  |
| IX3.61.1.10 |  |  |  |  |
| IX3.61.1.11 |  |  |  |  |
| IX3.61.1.12 |  |  |  | Ellato\_3.OS04331\_FB1 |
| IX3.61.1.13 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.KSZ\_1.FVM\_2\_IO.FB1 |
| IX3.61.1.14 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM15Cax3.FVM\_2.FVM\_2\_IO.FB1 |
| IX3.61.1.15 |  |  |  |  |
| IX3.61.1.16 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM15C.FVM\_2.FVM\_2\_IO.FB1 |
| IW3.61.1.17 |  |  |  |  |
| IW3.61.1.18 |  |  |  |  |

Table 704. Controller\_3.3.61.1 DI810 (Status)

##### 2 DO810

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| External power supervision | true | bool |  |  |  |
| Activate channel 1 | Active | enum |  |  |  |
| Activate channel 2 | Active | enum |  |  |  |
| Activate channel 3 | Active | enum |  |  |  |
| Activate channel 4 | Active | enum |  |  |  |
| Activate channel 5 | Active | enum |  |  |  |
| Activate channel 6 | Active | enum |  |  |  |
| Activate channel 7 | Active | enum |  |  |  |
| Activate channel 8 | Active | enum |  |  |  |
| Activate channel 9 | Active | enum |  |  |  |
| Activate channel 10 | Active | enum |  |  |  |
| Activate channel 11 | Active | enum |  |  |  |
| Activate channel 12 | Active | enum |  |  |  |
| Activate channel 13 | Active | enum |  |  |  |
| Activate channel 14 | Active | enum |  |  |  |
| Activate channel 15 | Active | enum |  |  |  |
| Activate channel 16 | Active | enum |  |  |  |
| OSP control channel 1 | Set OSP value | enum |  |  |  |
| OSP control channel 2 | Set OSP value | enum |  |  |  |
| OSP control channel 3 | Set OSP value | enum |  |  |  |
| OSP control channel 4 | Set OSP value | enum |  |  |  |
| OSP control channel 5 | Set OSP value | enum |  |  |  |
| OSP control channel 6 | Set OSP value | enum |  |  |  |
| OSP control channel 7 | Set OSP value | enum |  |  |  |
| OSP control channel 8 | Set OSP value | enum |  |  |  |
| OSP control channel 9 | Set OSP value | enum |  |  |  |
| OSP control channel 10 | Set OSP value | enum |  |  |  |
| OSP control channel 11 | Set OSP value | enum |  |  |  |
| OSP control channel 12 | Set OSP value | enum |  |  |  |
| OSP control channel 13 | Set OSP value | enum |  |  |  |
| OSP control channel 14 | Set OSP value | enum |  |  |  |
| OSP control channel 15 | Set OSP value | enum |  |  |  |
| OSP control channel 16 | Set OSP value | enum |  |  |  |
| OSP value channel 1 | false | bool |  |  |  |
| OSP value channel 2 | false | bool |  |  |  |
| OSP value channel 3 | false | bool |  |  |  |
| OSP value channel 4 | false | bool |  |  |  |
| OSP value channel 5 | false | bool |  |  |  |
| OSP value channel 6 | false | bool |  |  |  |
| OSP value channel 7 | false | bool |  |  |  |
| OSP value channel 8 | false | bool |  |  |  |
| OSP value channel 9 | false | bool |  |  |  |
| OSP value channel 10 | false | bool |  |  |  |
| OSP value channel 11 | false | bool |  |  |  |
| OSP value channel 12 | false | bool |  |  |  |
| OSP value channel 13 | false | bool |  |  |  |
| OSP value channel 14 | false | bool |  |  |  |
| OSP value channel 15 | false | bool |  |  |  |
| OSP value channel 16 | false | bool |  |  |  |

Table 705. Controller\_3 (172.16.96.153).3.61.2 DO810 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX3.61.2.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM15Bax3.FVM\_2.FVM\_2\_IO.Cmd | KS02454 |
| QX3.61.2.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.FVM\_1\_IO.Cmd | KS03955 |
| QX3.61.2.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.KSZ\_1.FVM\_2\_IO.Cmd | KS03999 |
| QX3.61.2.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.FVM\_1\_IO.Cmd | KS04655 |
| QX3.61.2.5 | Output 5 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.FVM\_1\_IO.Cmd | KS04755 |
| QX3.61.2.6 | Output 6 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.KSZ\_1.FVM\_2\_IO.Cmd | KS04799 |
| QX3.61.2.7 | Output 7 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_A.FVM\_2.FVM\_2\_IO.Cmd | KS02634 |
| QX3.61.2.8 | Output 8 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_B.FVM\_2.FVM\_2\_IO.Cmd | KS02635 |
| QX3.61.2.9 | Output 9 | BoolIO | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.FVM\_1\_IO.Cmd | KS04255 |
| QX3.61.2.10 | Output 10 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_C.FVM\_2.FVM\_2\_IO.Cmd | KS02636 |
| QX3.61.2.11 | Output 11 | BoolIO | Ellato\_3.KS03535\_Cmd | KS03535 |
| QX3.61.2.12 | Output 12 | BoolIO | Ellato\_3.KS04331\_Cmd | KS04331 |
| QX3.61.2.13 | Output 13 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.KSZ\_1.FVM\_2\_IO.Cmd | KS04699 |
| QX3.61.2.14 | Output 14 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM15Cax3.FVM\_2.FVM\_2\_IO.Cmd | KS03935 |
| QX3.61.2.15 | Output 15 | BoolIO | Nyers\_Drospiren\_3.U\_19A90\_00.EqM15Iax3.FVM\_2.FVM\_2\_IO.Cmd | KS09025 |
| QX3.61.2.16 | Output 16 | BoolIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM15C.FVM\_2.FVM\_2\_IO.Cmd | KS04635 |
| QW3.61.2.17 | All Outputs | DwordIO |  |  |
| IW3.61.2.18 | Channel status | DwordIO |  |  |
| IW3.61.2.19 | UnitStatus | HwStatus |  |  |

Table 706. Controller\_3.3.61.2 DO810 (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX3.61.2.1 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM15Bax3.FVM\_2.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.61.2.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.FVM\_1\_IO.Cmd |  |  |  |  | false |
| QX3.61.2.3 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.KSZ\_1.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.61.2.4 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.FVM\_1\_IO.Cmd |  |  |  |  | false |
| QX3.61.2.5 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.FVM\_1\_IO.Cmd |  |  |  |  | false |
| QX3.61.2.6 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.KSZ\_1.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.61.2.7 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_A.FVM\_2.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.61.2.8 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_B.FVM\_2.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.61.2.9 | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.FVM\_1\_IO.Cmd |  |  |  |  | false |
| QX3.61.2.10 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_C.FVM\_2.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.61.2.11 | Ellato\_3.KS03535\_Cmd |  |  |  |  | false |
| QX3.61.2.12 | Ellato\_3.KS04331\_Cmd |  |  |  |  | false |
| QX3.61.2.13 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.KSZ\_1.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.61.2.14 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM15Cax3.FVM\_2.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.61.2.15 | Nyers\_Drospiren\_3.U\_19A90\_00.EqM15Iax3.FVM\_2.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QX3.61.2.16 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM15C.FVM\_2.FVM\_2\_IO.Cmd |  |  |  |  | false |
| QW3.61.2.17 |  |  |  |  |  |  |
| QW3.61.2.18 |  |  |  |  |  |  |

Table 707. Controller\_3.3.61.2 DO810 (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX3.61.2.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM15Bax3.FVM\_2.FVM\_2\_IO.Cmd |
| QX3.61.2.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.FVM\_1\_IO.Cmd |
| QX3.61.2.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Aax3.KSZ\_1.FVM\_2\_IO.Cmd |
| QX3.61.2.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.FVM\_1\_IO.Cmd |
| QX3.61.2.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.FVM\_1\_IO.Cmd |
| QX3.61.2.6 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM09A.KSZ\_1.FVM\_2\_IO.Cmd |
| QX3.61.2.7 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_A.FVM\_2.FVM\_2\_IO.Cmd |
| QX3.61.2.8 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_B.FVM\_2.FVM\_2\_IO.Cmd |
| QX3.61.2.9 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.FVM\_1\_IO.Cmd |
| QX3.61.2.10 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM15B\_C.FVM\_2.FVM\_2\_IO.Cmd |
| QX3.61.2.11 |  |  |  | Ellato\_3.KS03535\_Cmd |
| QX3.61.2.12 |  |  |  | Ellato\_3.KS04331\_Cmd |
| QX3.61.2.13 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM09A.KSZ\_1.FVM\_2\_IO.Cmd |
| QX3.61.2.14 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM15Cax3.FVM\_2.FVM\_2\_IO.Cmd |
| QX3.61.2.15 |  |  |  | Nyers\_Drospiren\_3.U\_19A90\_00.EqM15Iax3.FVM\_2.FVM\_2\_IO.Cmd |
| QX3.61.2.16 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM15C.FVM\_2.FVM\_2\_IO.Cmd |
| QW3.61.2.17 |  |  |  |  |
| IW3.61.2.18 |  |  |  |  |

Table 708. Controller\_3.3.61.2 DO810 (Status)

#### 81 S900

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.81.0 | UnitStatus | HwStatus |  |  |

Table 709. Controller\_3.3.81 S900 (Connections)

##### 0 CI920\* (CIPB)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Grid Frequency | 50 Hz | enum |  |  |  |
| Analog Status | on | enum |  |  |  |
| Internal Bus Redundancy | false | bool |  |  |  |
| CIPB Redundancy | false | bool |  |  |  |
| Power Supply Redundancy | false | bool |  |  |  |

Table 710. Controller\_3 (172.16.96.153).3.81.0 CI920\* (CIPB) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.81.0.1 | UnitStatus | HwStatus |  |  |

Table 711. Controller\_3.3.81.0 CI920\* (CIPB) (Connections)

##### 1 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 712. Controller\_3 (172.16.96.153).3.81.1 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX3.81.1.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02Bax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG02650 |
| QX3.81.1.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02Bax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG02660 |
| QX3.81.1.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM10Dax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG02451 |
| QX3.81.1.4 | Output 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG02460 |
| QW3.81.1.5 | All outputs | DwordIO |  |  |
| IW3.81.1.6 | UnitStatus | HwStatus |  |  |

Table 713. Controller\_3.3.81.1 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX3.81.1.1 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02Bax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.1.2 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02Bax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.1.3 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM10Dax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.1.4 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW3.81.1.5 |  |  |  |  |  |  |

Table 714. Controller\_3.3.81.1 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX3.81.1.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02Bax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX3.81.1.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02Bax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX3.81.1.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM10Dax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX3.81.1.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QW3.81.1.5 |  |  |  |  |

Table 715. Controller\_3.3.81.1 DO910\* (DO4) (Status)

##### 2 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 716. Controller\_3 (172.16.96.153).3.81.2 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX3.81.2.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG02464 |
| QX3.81.2.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG02468 |
| QX3.81.2.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02A3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG02450 |
| QX3.81.2.4 | Output 4 | BoolIO |  |  |
| QW3.81.2.5 | All outputs | DwordIO |  |  |
| IW3.81.2.6 | UnitStatus | HwStatus |  |  |

Table 717. Controller\_3.3.81.2 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX3.81.2.1 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.2.2 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.2.3 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02A3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.2.4 |  |  |  |  |  | false |
| QW3.81.2.5 |  |  |  |  |  |  |

Table 718. Controller\_3.3.81.2 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX3.81.2.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX3.81.2.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX3.81.2.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02A3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX3.81.2.4 |  |  |  |  |
| QW3.81.2.5 |  |  |  |  |

Table 719. Controller\_3.3.81.2 DO910\* (DO4) (Status)

##### 3 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 720. Controller\_3 (172.16.96.153).3.81.3 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX3.81.3.1 | Output 1 | BoolIO |  |  |
| QX3.81.3.2 | Output 2 | BoolIO | Ellato\_3.KSVG06865\_Cmd | KSVG06865 |
| QX3.81.3.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM12Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG01237 |
| QX3.81.3.4 | Output 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.GCS\_1ax3.GCS\_1\_IO.Cmd | KSVG01287 |
| QW3.81.3.5 | All outputs | DwordIO |  |  |
| IW3.81.3.6 | UnitStatus | HwStatus |  |  |

Table 721. Controller\_3.3.81.3 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX3.81.3.1 |  |  |  |  |  | false |
| QX3.81.3.2 | Ellato\_3.KSVG06865\_Cmd |  |  |  |  | false |
| QX3.81.3.3 | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM12Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.3.4 | Cikloprpil\_D6\_laktol.U\_08A12\_00.GCS\_1ax3.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW3.81.3.5 |  |  |  |  |  |  |

Table 722. Controller\_3.3.81.3 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX3.81.3.1 |  |  |  |  |
| QX3.81.3.2 |  |  |  | Ellato\_3.KSVG06865\_Cmd |
| QX3.81.3.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM12Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX3.81.3.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.GCS\_1ax3.GCS\_1\_IO.Cmd |
| QW3.81.3.5 |  |  |  |  |

Table 723. Controller\_3.3.81.3 DO910\* (DO4) (Status)

##### 4 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 724. Controller\_3 (172.16.96.153).3.81.4 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX3.81.4.1 | Output 1 | BoolIO | Ellato\_3.KSVG06867\_Cmd | KSVG06867 |
| QX3.81.4.2 | Output 2 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM05Gax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03961 |
| QX3.81.4.3 | Output 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM05Gax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG03970 |
| QX3.81.4.4 | Output 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG01061 |
| QW3.81.4.5 | All outputs | DwordIO |  |  |
| IW3.81.4.6 | UnitStatus | HwStatus |  |  |

Table 725. Controller\_3.3.81.4 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX3.81.4.1 | Ellato\_3.KSVG06867\_Cmd |  |  |  |  | false |
| QX3.81.4.2 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM05Gax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.4.3 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM05Gax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.4.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW3.81.4.5 |  |  |  |  |  |  |

Table 726. Controller\_3.3.81.4 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX3.81.4.1 |  |  |  | Ellato\_3.KSVG06867\_Cmd |
| QX3.81.4.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM05Gax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX3.81.4.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM05Gax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX3.81.4.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QW3.81.4.5 |  |  |  |  |

Table 727. Controller\_3.3.81.4 DO910\* (DO4) (Status)

##### 5 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 728. Controller\_3 (172.16.96.153).3.81.5 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX3.81.5.1 | Output 1 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1C.GCS\_1\_IO.Cmd | KSVG01086 |
| QX3.81.5.2 | Output 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1B.GCS\_1\_IO.Cmd | KSVG01087 |
| QX3.81.5.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM01D2.GCS\_1A.GCS\_1\_IO.Cmd | KSVG02453 |
| QX3.81.5.4 | Output 4 | BoolIO |  |  |
| QW3.81.5.5 | All outputs | DwordIO |  |  |
| IW3.81.5.6 | UnitStatus | HwStatus |  |  |

Table 729. Controller\_3.3.81.5 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX3.81.5.1 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.5.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.5.3 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM01D2.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.5.4 |  |  |  |  |  | false |
| QW3.81.5.5 |  |  |  |  |  |  |

Table 730. Controller\_3.3.81.5 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX3.81.5.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1C.GCS\_1\_IO.Cmd |
| QX3.81.5.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1B.GCS\_1\_IO.Cmd |
| QX3.81.5.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM01D2.GCS\_1A.GCS\_1\_IO.Cmd |
| QX3.81.5.4 |  |  |  |  |
| QW3.81.5.5 |  |  |  |  |

Table 731. Controller\_3.3.81.5 DO910\* (DO4) (Status)

##### 6 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 732. Controller\_3 (172.16.96.153).3.81.6 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX3.81.6.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1A.GCS\_1\_IO.Cmd | KSVG04761 |
| QX3.81.6.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02A.GCS\_1A.GCS\_1\_IO.Cmd | KSVG02662 |
| QX3.81.6.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1B.GCS\_1\_IO.Cmd | KSVG04786 |
| QX3.81.6.4 | Output 4 | BoolIO |  |  |
| QW3.81.6.5 | All outputs | DwordIO |  |  |
| IW3.81.6.6 | UnitStatus | HwStatus |  |  |

Table 733. Controller\_3.3.81.6 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX3.81.6.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.6.2 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02A.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.6.3 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.6.4 |  |  |  |  |  | false |
| QW3.81.6.5 |  |  |  |  |  |  |

Table 734. Controller\_3.3.81.6 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX3.81.6.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1A.GCS\_1\_IO.Cmd |
| QX3.81.6.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02A.GCS\_1A.GCS\_1\_IO.Cmd |
| QX3.81.6.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1B.GCS\_1\_IO.Cmd |
| QX3.81.6.4 |  |  |  |  |
| QW3.81.6.5 |  |  |  |  |

Table 735. Controller\_3.3.81.6 DO910\* (DO4) (Status)

##### 7 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 736. Controller\_3 (172.16.96.153).3.81.7 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX3.81.7.1 | Output 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1C.GCS\_1\_IO.Cmd | KSVG04787 |
| QX3.81.7.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1A.GCS\_1\_IO.Cmd | KSVG02670 |
| QX3.81.7.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1B.GCS\_1\_IO.Cmd | KSVG02680 |
| QX3.81.7.4 | Output 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1C.GCS\_1\_IO.Cmd | KSVG02690 |
| QW3.81.7.5 | All outputs | DwordIO |  |  |
| IW3.81.7.6 | UnitStatus | HwStatus |  |  |

Table 737. Controller\_3.3.81.7 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX3.81.7.1 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.7.2 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.7.3 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1B.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.7.4 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1C.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW3.81.7.5 |  |  |  |  |  |  |

Table 738. Controller\_3.3.81.7 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX3.81.7.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1C.GCS\_1\_IO.Cmd |
| QX3.81.7.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1A.GCS\_1\_IO.Cmd |
| QX3.81.7.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1B.GCS\_1\_IO.Cmd |
| QX3.81.7.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1C.GCS\_1\_IO.Cmd |
| QW3.81.7.5 |  |  |  |  |

Table 739. Controller\_3.3.81.7 DO910\* (DO4) (Status)

##### 8 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 740. Controller\_3 (172.16.96.153).3.81.8 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX3.81.8.1 | Output 1 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Eax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG03960 |
| QX3.81.8.2 | Output 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM01Dax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG02472 |
| QX3.81.8.3 | Output 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02Aax3.GCS\_1A.GCS\_1\_IO.Cmd | KSVG02466 |
| QX3.81.8.4 | Output 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02A2.GCS\_1A.GCS\_1\_IO.Cmd | KSVG02467 |
| QW3.81.8.5 | All outputs | DwordIO |  |  |
| IW3.81.8.6 | UnitStatus | HwStatus |  |  |

Table 741. Controller\_3.3.81.8 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX3.81.8.1 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Eax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.8.2 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM01Dax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.8.3 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02Aax3.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QX3.81.8.4 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02A2.GCS\_1A.GCS\_1\_IO.Cmd |  |  |  |  | false |
| QW3.81.8.5 |  |  |  |  |  |  |

Table 742. Controller\_3.3.81.8 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX3.81.8.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Eax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX3.81.8.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM01Dax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX3.81.8.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02Aax3.GCS\_1A.GCS\_1\_IO.Cmd |
| QX3.81.8.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02A2.GCS\_1A.GCS\_1\_IO.Cmd |
| QW3.81.8.5 |  |  |  |  |

Table 743. Controller\_3.3.81.8 DO910\* (DO4) (Status)

##### 9 DO910\* (DO4)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | on | enum |  |  |  |
| ch. 3: open line detect | on | enum |  |  |  |
| ch. 4: open line detect | on | enum |  |  |  |
| ch. 1: failsafe mode | min value | enum |  |  |  |
| ch. 2: failsafe mode | min value | enum |  |  |  |
| ch. 3: failsafe mode | min value | enum |  |  |  |
| ch. 4: failsafe mode | min value | enum |  |  |  |
| ch. 1: polarity | normal | enum |  |  |  |
| ch. 2: polarity | normal | enum |  |  |  |
| ch. 3: polarity | normal | enum |  |  |  |
| ch. 4: polarity | normal | enum |  |  |  |

Table 744. Controller\_3 (172.16.96.153).3.81.9 DO910\* (DO4) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QX3.81.9.1 | Output 1 | BoolIO |  |  |
| QX3.81.9.2 | Output 2 | BoolIO |  |  |
| QX3.81.9.3 | Output 3 | BoolIO |  |  |
| QX3.81.9.4 | Output 4 | BoolIO |  |  |
| QW3.81.9.5 | All outputs | DwordIO |  |  |
| IW3.81.9.6 | UnitStatus | HwStatus |  |  |

Table 745. Controller\_3.3.81.9 DO910\* (DO4) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| QX3.81.9.1 |  |  |  |  |  | false |
| QX3.81.9.2 |  |  |  |  |  | false |
| QX3.81.9.3 |  |  |  |  |  | false |
| QX3.81.9.4 |  |  |  |  |  | false |
| QW3.81.9.5 |  |  |  |  |  |  |

Table 746. Controller\_3.3.81.9 DO910\* (DO4) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QX3.81.9.1 |  |  |  |  |
| QX3.81.9.2 |  |  |  |  |
| QX3.81.9.3 |  |  |  |  |
| QX3.81.9.4 |  |  |  |  |
| QW3.81.9.5 |  |  |  |  |

Table 747. Controller\_3.3.81.9 DO910\* (DO4) (Status)

##### 10 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | true | bool |  |  |  |
| ch. 2: deactivate | true | bool |  |  |  |
| ch. 3: deactivate | true | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | true | bool |  |  |  |
| ch. 6: deactivate | true | bool |  |  |  |
| ch. 7: deactivate | true | bool |  |  |  |
| ch. 8: deactivate | true | bool |  |  |  |

Table 748. Controller\_3 (172.16.96.153).3.81.10 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX3.81.10.1 | Input 1 | BoolIO |  |  |
| IX3.81.10.2 | Input 2 | BoolIO |  |  |
| IX3.81.10.3 | Input 3 | BoolIO |  |  |
| IX3.81.10.4 | Input 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02A3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG02450 |
| IX3.81.10.5 | Input 5 | BoolIO |  |  |
| IX3.81.10.6 | Input 6 | BoolIO |  |  |
| IX3.81.10.7 | Input 7 | BoolIO |  |  |
| IX3.81.10.8 | Input 8 | BoolIO |  |  |
| IW3.81.10.9 | All inputs | DwordIO |  |  |
| QX3.81.10.10 | Output 1 | BoolIO |  |  |
| QX3.81.10.11 | Output 2 | BoolIO |  |  |
| QX3.81.10.12 | Output 3 | BoolIO |  |  |
| QX3.81.10.13 | Output 4 | BoolIO |  |  |
| QX3.81.10.14 | Output 5 | BoolIO |  |  |
| QX3.81.10.15 | Output 6 | BoolIO |  |  |
| QX3.81.10.16 | Output 7 | BoolIO |  |  |
| QX3.81.10.17 | Output 8 | BoolIO |  |  |
| QW3.81.10.18 | All outputs | DwordIO |  |  |
| IX3.81.10.19 | Status Input 1 | BoolIO |  |  |
| IX3.81.10.20 | Status Input 2 | BoolIO |  |  |
| IX3.81.10.21 | Status Input 3 | BoolIO |  |  |
| IX3.81.10.22 | Status Input 4 | BoolIO |  |  |
| IX3.81.10.23 | Status Input 5 | BoolIO |  |  |
| IX3.81.10.24 | Status Input 6 | BoolIO |  |  |
| IX3.81.10.25 | Status Input 7 | BoolIO |  |  |
| IX3.81.10.26 | Status Input 8 | BoolIO |  |  |
| IW3.81.10.27 | UnitStatus | HwStatus |  |  |

Table 749. Controller\_3.3.81.10 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX3.81.10.1 |  |  |  |  |  | false |
| IX3.81.10.2 |  |  |  |  |  | false |
| IX3.81.10.3 |  |  |  |  |  | false |
| IX3.81.10.4 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02A3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.10.5 |  |  |  |  |  | false |
| IX3.81.10.6 |  |  |  |  |  | false |
| IX3.81.10.7 |  |  |  |  |  | false |
| IX3.81.10.8 |  |  |  |  |  | false |
| QW3.81.10.9 |  |  |  |  |  |  |
| QX3.81.10.10 |  |  |  |  |  | false |
| QX3.81.10.11 |  |  |  |  |  | false |
| QX3.81.10.12 |  |  |  |  |  | false |
| QX3.81.10.13 |  |  |  |  |  | false |
| QX3.81.10.14 |  |  |  |  |  | false |
| QX3.81.10.15 |  |  |  |  |  | false |
| QX3.81.10.16 |  |  |  |  |  | false |
| QX3.81.10.17 |  |  |  |  |  | false |
| QW3.81.10.18 |  |  |  |  |  |  |
| IX3.81.10.19 |  |  |  |  |  | false |
| IX3.81.10.20 |  |  |  |  |  | false |
| IX3.81.10.21 |  |  |  |  |  | false |
| IX3.81.10.22 |  |  |  |  |  | false |
| IX3.81.10.23 |  |  |  |  |  | false |
| IX3.81.10.24 |  |  |  |  |  | false |
| IX3.81.10.25 |  |  |  |  |  | false |
| IX3.81.10.26 |  |  |  |  |  | false |

Table 750. Controller\_3.3.81.10 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX3.81.10.1 |  |  |  |  |
| IX3.81.10.2 |  |  |  |  |
| IX3.81.10.3 |  |  |  |  |
| IX3.81.10.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02A3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.10.5 |  |  |  |  |
| IX3.81.10.6 |  |  |  |  |
| IX3.81.10.7 |  |  |  |  |
| IX3.81.10.8 |  |  |  |  |
| IW3.81.10.9 |  |  |  |  |
| QX3.81.10.10 |  |  |  |  |
| QX3.81.10.11 |  |  |  |  |
| QX3.81.10.12 |  |  |  |  |
| QX3.81.10.13 |  |  |  |  |
| QX3.81.10.14 |  |  |  |  |
| QX3.81.10.15 |  |  |  |  |
| QX3.81.10.16 |  |  |  |  |
| QX3.81.10.17 |  |  |  |  |
| QW3.81.10.18 |  |  |  |  |
| IX3.81.10.19 |  |  |  |  |
| IX3.81.10.20 |  |  |  |  |
| IX3.81.10.21 |  |  |  |  |
| IX3.81.10.22 |  |  |  |  |
| IX3.81.10.23 |  |  |  |  |
| IX3.81.10.24 |  |  |  |  |
| IX3.81.10.25 |  |  |  |  |
| IX3.81.10.26 |  |  |  |  |

Table 751. Controller\_3.3.81.10 DX910\* (DIO8 S) (Status)

##### 11 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | true | bool |  |  |  |
| ch. 8: deactivate | true | bool |  |  |  |

Table 752. Controller\_3 (172.16.96.153).3.81.11 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX3.81.11.1 | Input 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02Bax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG02650 |
| IX3.81.11.2 | Input 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02Bax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG02660 |
| IX3.81.11.3 | Input 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM10Dax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG02451 |
| IX3.81.11.4 | Input 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG02460 |
| IX3.81.11.5 | Input 5 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG02464 |
| IX3.81.11.6 | Input 6 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG02468 |
| IX3.81.11.7 | Input 7 | BoolIO |  |  |
| IX3.81.11.8 | Input 8 | BoolIO |  |  |
| IW3.81.11.9 | All inputs | DwordIO |  |  |
| QX3.81.11.10 | Output 1 | BoolIO |  |  |
| QX3.81.11.11 | Output 2 | BoolIO |  |  |
| QX3.81.11.12 | Output 3 | BoolIO |  |  |
| QX3.81.11.13 | Output 4 | BoolIO |  |  |
| QX3.81.11.14 | Output 5 | BoolIO |  |  |
| QX3.81.11.15 | Output 6 | BoolIO |  |  |
| QX3.81.11.16 | Output 7 | BoolIO |  |  |
| QX3.81.11.17 | Output 8 | BoolIO |  |  |
| QW3.81.11.18 | All outputs | DwordIO |  |  |
| IX3.81.11.19 | Status Input 1 | BoolIO |  |  |
| IX3.81.11.20 | Status Input 2 | BoolIO |  |  |
| IX3.81.11.21 | Status Input 3 | BoolIO |  |  |
| IX3.81.11.22 | Status Input 4 | BoolIO |  |  |
| IX3.81.11.23 | Status Input 5 | BoolIO |  |  |
| IX3.81.11.24 | Status Input 6 | BoolIO |  |  |
| IX3.81.11.25 | Status Input 7 | BoolIO |  |  |
| IX3.81.11.26 | Status Input 8 | BoolIO |  |  |
| IW3.81.11.27 | UnitStatus | HwStatus |  |  |

Table 753. Controller\_3.3.81.11 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX3.81.11.1 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02Bax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.11.2 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02Bax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.11.3 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM10Dax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.11.4 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.11.5 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.11.6 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.11.7 |  |  |  |  |  | false |
| IX3.81.11.8 |  |  |  |  |  | false |
| QW3.81.11.9 |  |  |  |  |  |  |
| QX3.81.11.10 |  |  |  |  |  | false |
| QX3.81.11.11 |  |  |  |  |  | false |
| QX3.81.11.12 |  |  |  |  |  | false |
| QX3.81.11.13 |  |  |  |  |  | false |
| QX3.81.11.14 |  |  |  |  |  | false |
| QX3.81.11.15 |  |  |  |  |  | false |
| QX3.81.11.16 |  |  |  |  |  | false |
| QX3.81.11.17 |  |  |  |  |  | false |
| QW3.81.11.18 |  |  |  |  |  |  |
| IX3.81.11.19 |  |  |  |  |  | false |
| IX3.81.11.20 |  |  |  |  |  | false |
| IX3.81.11.21 |  |  |  |  |  | false |
| IX3.81.11.22 |  |  |  |  |  | false |
| IX3.81.11.23 |  |  |  |  |  | false |
| IX3.81.11.24 |  |  |  |  |  | false |
| IX3.81.11.25 |  |  |  |  |  | false |
| IX3.81.11.26 |  |  |  |  |  | false |

Table 754. Controller\_3.3.81.11 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX3.81.11.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02Bax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.11.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02Bax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IX3.81.11.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM10Dax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.11.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM03Bax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.11.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM04Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.11.6 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM03Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.11.7 |  |  |  |  |
| IX3.81.11.8 |  |  |  |  |
| IW3.81.11.9 |  |  |  |  |
| QX3.81.11.10 |  |  |  |  |
| QX3.81.11.11 |  |  |  |  |
| QX3.81.11.12 |  |  |  |  |
| QX3.81.11.13 |  |  |  |  |
| QX3.81.11.14 |  |  |  |  |
| QX3.81.11.15 |  |  |  |  |
| QX3.81.11.16 |  |  |  |  |
| QX3.81.11.17 |  |  |  |  |
| QW3.81.11.18 |  |  |  |  |
| IX3.81.11.19 |  |  |  |  |
| IX3.81.11.20 |  |  |  |  |
| IX3.81.11.21 |  |  |  |  |
| IX3.81.11.22 |  |  |  |  |
| IX3.81.11.23 |  |  |  |  |
| IX3.81.11.24 |  |  |  |  |
| IX3.81.11.25 |  |  |  |  |
| IX3.81.11.26 |  |  |  |  |

Table 755. Controller\_3.3.81.11 DX910\* (DIO8 S) (Status)

##### 12 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | true | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | true | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 756. Controller\_3 (172.16.96.153).3.81.12 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX3.81.12.1 | Input 1 | BoolIO |  |  |
| IX3.81.12.2 | Input 2 | BoolIO | Ellato\_3.KSVG06865\_FB0 | KSVG06865 |
| IX3.81.12.3 | Input 3 | BoolIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.FS\_IO | FS01231 |
| IX3.81.12.4 | Input 4 | BoolIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM12Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG01237 |
| IX3.81.12.5 | Input 5 | BoolIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.GCS\_1ax3.GCS\_1\_IO.FB0 | KSVG01287 |
| IX3.81.12.6 | Input 6 | BoolIO | Ellato\_3.KSVG06867\_FB0 | KSVG06867 |
| IX3.81.12.7 | Input 7 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM05Gax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03961 |
| IX3.81.12.8 | Input 8 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM05Gax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG03970 |
| IW3.81.12.9 | All inputs | DwordIO |  |  |
| QX3.81.12.10 | Output 1 | BoolIO |  |  |
| QX3.81.12.11 | Output 2 | BoolIO |  |  |
| QX3.81.12.12 | Output 3 | BoolIO |  |  |
| QX3.81.12.13 | Output 4 | BoolIO |  |  |
| QX3.81.12.14 | Output 5 | BoolIO |  |  |
| QX3.81.12.15 | Output 6 | BoolIO |  |  |
| QX3.81.12.16 | Output 7 | BoolIO |  |  |
| QX3.81.12.17 | Output 8 | BoolIO |  |  |
| QW3.81.12.18 | All outputs | DwordIO |  |  |
| IX3.81.12.19 | Status Input 1 | BoolIO |  |  |
| IX3.81.12.20 | Status Input 2 | BoolIO |  |  |
| IX3.81.12.21 | Status Input 3 | BoolIO |  |  |
| IX3.81.12.22 | Status Input 4 | BoolIO |  |  |
| IX3.81.12.23 | Status Input 5 | BoolIO |  |  |
| IX3.81.12.24 | Status Input 6 | BoolIO |  |  |
| IX3.81.12.25 | Status Input 7 | BoolIO |  |  |
| IX3.81.12.26 | Status Input 8 | BoolIO |  |  |
| IW3.81.12.27 | UnitStatus | HwStatus |  |  |

Table 757. Controller\_3.3.81.12 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX3.81.12.1 |  |  |  |  |  | false |
| IX3.81.12.2 | Ellato\_3.KSVG06865\_FB0 |  |  |  |  | false |
| IX3.81.12.3 | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.FS\_IO |  |  |  |  | false |
| IX3.81.12.4 | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM12Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.12.5 | Cikloprpil\_D6\_laktol.U\_08A12\_00.GCS\_1ax3.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.12.6 | Ellato\_3.KSVG06867\_FB0 |  |  |  |  | false |
| IX3.81.12.7 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM05Gax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.12.8 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM05Gax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW3.81.12.9 |  |  |  |  |  |  |
| QX3.81.12.10 |  |  |  |  |  | false |
| QX3.81.12.11 |  |  |  |  |  | false |
| QX3.81.12.12 |  |  |  |  |  | false |
| QX3.81.12.13 |  |  |  |  |  | false |
| QX3.81.12.14 |  |  |  |  |  | false |
| QX3.81.12.15 |  |  |  |  |  | false |
| QX3.81.12.16 |  |  |  |  |  | false |
| QX3.81.12.17 |  |  |  |  |  | false |
| QW3.81.12.18 |  |  |  |  |  |  |
| IX3.81.12.19 |  |  |  |  |  | false |
| IX3.81.12.20 |  |  |  |  |  | false |
| IX3.81.12.21 |  |  |  |  |  | false |
| IX3.81.12.22 |  |  |  |  |  | false |
| IX3.81.12.23 |  |  |  |  |  | false |
| IX3.81.12.24 |  |  |  |  |  | false |
| IX3.81.12.25 |  |  |  |  |  | false |
| IX3.81.12.26 |  |  |  |  |  | false |

Table 758. Controller\_3.3.81.12 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX3.81.12.1 |  |  |  |  |
| IX3.81.12.2 |  |  |  | Ellato\_3.KSVG06865\_FB0 |
| IX3.81.12.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.FS\_IO |
| IX3.81.12.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM12Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.12.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.GCS\_1ax3.GCS\_1\_IO.FB0 |
| IX3.81.12.6 |  |  |  | Ellato\_3.KSVG06867\_FB0 |
| IX3.81.12.7 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM05Gax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.12.8 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM05Gax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IW3.81.12.9 |  |  |  |  |
| QX3.81.12.10 |  |  |  |  |
| QX3.81.12.11 |  |  |  |  |
| QX3.81.12.12 |  |  |  |  |
| QX3.81.12.13 |  |  |  |  |
| QX3.81.12.14 |  |  |  |  |
| QX3.81.12.15 |  |  |  |  |
| QX3.81.12.16 |  |  |  |  |
| QX3.81.12.17 |  |  |  |  |
| QW3.81.12.18 |  |  |  |  |
| IX3.81.12.19 |  |  |  |  |
| IX3.81.12.20 |  |  |  |  |
| IX3.81.12.21 |  |  |  |  |
| IX3.81.12.22 |  |  |  |  |
| IX3.81.12.23 |  |  |  |  |
| IX3.81.12.24 |  |  |  |  |
| IX3.81.12.25 |  |  |  |  |
| IX3.81.12.26 |  |  |  |  |

Table 759. Controller\_3.3.81.12 DX910\* (DIO8 S) (Status)

##### 13 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | true | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 760. Controller\_3 (172.16.96.153).3.81.13 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX3.81.13.1 | Input 1 | BoolIO |  |  |
| IX3.81.13.2 | Input 2 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG01061 |
| IX3.81.13.3 | Input 3 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1C.GCS\_1\_IO.FB0 | KSVG01086 |
| IX3.81.13.4 | Input 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1B.GCS\_1\_IO.FB0 | KSVG01087 |
| IX3.81.13.5 | Input 5 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02A.GCS\_1A.GCS\_1\_IO.FB0 | KSVG02662 |
| IX3.81.13.6 | Input 6 | BoolIO | Ellato\_3.FS06832 | FS06832 |
| IX3.81.13.7 | Input 7 | BoolIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Eax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG03960 |
| IX3.81.13.8 | Input 8 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1A.GCS\_1\_IO.FB0 | KSVG04761 |
| IW3.81.13.9 | All inputs | DwordIO |  |  |
| QX3.81.13.10 | Output 1 | BoolIO |  |  |
| QX3.81.13.11 | Output 2 | BoolIO |  |  |
| QX3.81.13.12 | Output 3 | BoolIO |  |  |
| QX3.81.13.13 | Output 4 | BoolIO |  |  |
| QX3.81.13.14 | Output 5 | BoolIO |  |  |
| QX3.81.13.15 | Output 6 | BoolIO |  |  |
| QX3.81.13.16 | Output 7 | BoolIO |  |  |
| QX3.81.13.17 | Output 8 | BoolIO |  |  |
| QW3.81.13.18 | All outputs | DwordIO |  |  |
| IX3.81.13.19 | Status Input 1 | BoolIO |  |  |
| IX3.81.13.20 | Status Input 2 | BoolIO |  |  |
| IX3.81.13.21 | Status Input 3 | BoolIO |  |  |
| IX3.81.13.22 | Status Input 4 | BoolIO |  |  |
| IX3.81.13.23 | Status Input 5 | BoolIO |  |  |
| IX3.81.13.24 | Status Input 6 | BoolIO |  |  |
| IX3.81.13.25 | Status Input 7 | BoolIO |  |  |
| IX3.81.13.26 | Status Input 8 | BoolIO |  |  |
| IW3.81.13.27 | UnitStatus | HwStatus |  |  |

Table 761. Controller\_3.3.81.13 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX3.81.13.1 |  |  |  |  |  | false |
| IX3.81.13.2 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.13.3 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.13.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.13.5 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02A.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.13.6 | Ellato\_3.FS06832 |  |  |  |  | false |
| IX3.81.13.7 | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Eax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.13.8 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| QW3.81.13.9 |  |  |  |  |  |  |
| QX3.81.13.10 |  |  |  |  |  | false |
| QX3.81.13.11 |  |  |  |  |  | false |
| QX3.81.13.12 |  |  |  |  |  | false |
| QX3.81.13.13 |  |  |  |  |  | false |
| QX3.81.13.14 |  |  |  |  |  | false |
| QX3.81.13.15 |  |  |  |  |  | false |
| QX3.81.13.16 |  |  |  |  |  | false |
| QX3.81.13.17 |  |  |  |  |  | false |
| QW3.81.13.18 |  |  |  |  |  |  |
| IX3.81.13.19 |  |  |  |  |  | false |
| IX3.81.13.20 |  |  |  |  |  | false |
| IX3.81.13.21 |  |  |  |  |  | false |
| IX3.81.13.22 |  |  |  |  |  | false |
| IX3.81.13.23 |  |  |  |  |  | false |
| IX3.81.13.24 |  |  |  |  |  | false |
| IX3.81.13.25 |  |  |  |  |  | false |
| IX3.81.13.26 |  |  |  |  |  | false |

Table 762. Controller\_3.3.81.13 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX3.81.13.1 |  |  |  |  |
| IX3.81.13.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.13.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1C.GCS\_1\_IO.FB0 |
| IX3.81.13.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM05Bax3.GCS\_1B.GCS\_1\_IO.FB0 |
| IX3.81.13.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02A.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.13.6 |  |  |  | Ellato\_3.FS06832 |
| IX3.81.13.7 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM09Eax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.13.8 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1A.GCS\_1\_IO.FB0 |
| IW3.81.13.9 |  |  |  |  |
| QX3.81.13.10 |  |  |  |  |
| QX3.81.13.11 |  |  |  |  |
| QX3.81.13.12 |  |  |  |  |
| QX3.81.13.13 |  |  |  |  |
| QX3.81.13.14 |  |  |  |  |
| QX3.81.13.15 |  |  |  |  |
| QX3.81.13.16 |  |  |  |  |
| QX3.81.13.17 |  |  |  |  |
| QW3.81.13.18 |  |  |  |  |
| IX3.81.13.19 |  |  |  |  |
| IX3.81.13.20 |  |  |  |  |
| IX3.81.13.21 |  |  |  |  |
| IX3.81.13.22 |  |  |  |  |
| IX3.81.13.23 |  |  |  |  |
| IX3.81.13.24 |  |  |  |  |
| IX3.81.13.25 |  |  |  |  |
| IX3.81.13.26 |  |  |  |  |

Table 763. Controller\_3.3.81.13 DX910\* (DIO8 S) (Status)

##### 14 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | true | bool |  |  |  |
| ch. 3: deactivate | true | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 764. Controller\_3 (172.16.96.153).3.81.14 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX3.81.14.1 | Input 1 | BoolIO | Ellato\_3.FS06732 | FS06732 |
| IX3.81.14.2 | Input 2 | BoolIO |  |  |
| IX3.81.14.3 | Input 3 | BoolIO |  |  |
| IX3.81.14.4 | Input 4 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM01D2.GCS\_1A.GCS\_1\_IO.FB0 | KSVG02453 |
| IX3.81.14.5 | Input 5 | BoolIO |  |  |
| IX3.81.14.6 | Input 6 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1B.GCS\_1\_IO.FB0 | KSVG04786 |
| IX3.81.14.7 | Input 7 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1C.GCS\_1\_IO.FB0 | KSVG04787 |
| IX3.81.14.8 | Input 8 | BoolIO | Ellato\_3.U\_PL\_C3.EqMPLH.PL\_1.PL\_1\_IO | PL 14A31 |
| IW3.81.14.9 | All inputs | DwordIO |  |  |
| QX3.81.14.10 | Output 1 | BoolIO |  |  |
| QX3.81.14.11 | Output 2 | BoolIO |  |  |
| QX3.81.14.12 | Output 3 | BoolIO |  |  |
| QX3.81.14.13 | Output 4 | BoolIO |  |  |
| QX3.81.14.14 | Output 5 | BoolIO |  |  |
| QX3.81.14.15 | Output 6 | BoolIO |  |  |
| QX3.81.14.16 | Output 7 | BoolIO |  |  |
| QX3.81.14.17 | Output 8 | BoolIO |  |  |
| QW3.81.14.18 | All outputs | DwordIO |  |  |
| IX3.81.14.19 | Status Input 1 | BoolIO |  |  |
| IX3.81.14.20 | Status Input 2 | BoolIO |  |  |
| IX3.81.14.21 | Status Input 3 | BoolIO |  |  |
| IX3.81.14.22 | Status Input 4 | BoolIO |  |  |
| IX3.81.14.23 | Status Input 5 | BoolIO |  |  |
| IX3.81.14.24 | Status Input 6 | BoolIO |  |  |
| IX3.81.14.25 | Status Input 7 | BoolIO |  |  |
| IX3.81.14.26 | Status Input 8 | BoolIO |  |  |
| IW3.81.14.27 | UnitStatus | HwStatus |  |  |

Table 765. Controller\_3.3.81.14 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX3.81.14.1 | Ellato\_3.FS06732 |  |  |  |  | false |
| IX3.81.14.2 |  |  |  |  |  | false |
| IX3.81.14.3 |  |  |  |  |  | false |
| IX3.81.14.4 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM01D2.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.14.5 |  |  |  |  |  | false |
| IX3.81.14.6 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.14.7 | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.14.8 | Ellato\_3.U\_PL\_C3.EqMPLH.PL\_1.PL\_1\_IO |  |  |  |  | false |
| QW3.81.14.9 |  |  |  |  |  |  |
| QX3.81.14.10 |  |  |  |  |  | false |
| QX3.81.14.11 |  |  |  |  |  | false |
| QX3.81.14.12 |  |  |  |  |  | false |
| QX3.81.14.13 |  |  |  |  |  | false |
| QX3.81.14.14 |  |  |  |  |  | false |
| QX3.81.14.15 |  |  |  |  |  | false |
| QX3.81.14.16 |  |  |  |  |  | false |
| QX3.81.14.17 |  |  |  |  |  | false |
| QW3.81.14.18 |  |  |  |  |  |  |
| IX3.81.14.19 |  |  |  |  |  | false |
| IX3.81.14.20 |  |  |  |  |  | false |
| IX3.81.14.21 |  |  |  |  |  | false |
| IX3.81.14.22 |  |  |  |  |  | false |
| IX3.81.14.23 |  |  |  |  |  | false |
| IX3.81.14.24 |  |  |  |  |  | false |
| IX3.81.14.25 |  |  |  |  |  | false |
| IX3.81.14.26 |  |  |  |  |  | false |

Table 766. Controller\_3.3.81.14 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX3.81.14.1 |  |  |  | Ellato\_3.FS06732 |
| IX3.81.14.2 |  |  |  |  |
| IX3.81.14.3 |  |  |  |  |
| IX3.81.14.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM01D2.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.14.5 |  |  |  |  |
| IX3.81.14.6 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1B.GCS\_1\_IO.FB0 |
| IX3.81.14.7 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM05B.GCS\_1C.GCS\_1\_IO.FB0 |
| IX3.81.14.8 |  |  |  | Ellato\_3.U\_PL\_C3.EqMPLH.PL\_1.PL\_1\_IO |
| IW3.81.14.9 |  |  |  |  |
| QX3.81.14.10 |  |  |  |  |
| QX3.81.14.11 |  |  |  |  |
| QX3.81.14.12 |  |  |  |  |
| QX3.81.14.13 |  |  |  |  |
| QX3.81.14.14 |  |  |  |  |
| QX3.81.14.15 |  |  |  |  |
| QX3.81.14.16 |  |  |  |  |
| QX3.81.14.17 |  |  |  |  |
| QW3.81.14.18 |  |  |  |  |
| IX3.81.14.19 |  |  |  |  |
| IX3.81.14.20 |  |  |  |  |
| IX3.81.14.21 |  |  |  |  |
| IX3.81.14.22 |  |  |  |  |
| IX3.81.14.23 |  |  |  |  |
| IX3.81.14.24 |  |  |  |  |
| IX3.81.14.25 |  |  |  |  |
| IX3.81.14.26 |  |  |  |  |

Table 767. Controller\_3.3.81.14 DX910\* (DIO8 S) (Status)

##### 15 DX910\* (DIO8 S)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1,2: short detect | on | enum |  |  |  |
| ch. 3,4: short detect | on | enum |  |  |  |
| ch. 5,6: short detect | on | enum |  |  |  |
| ch. 7,8: short detect | on | enum |  |  |  |
| ch. 1,2: open line detect | on | enum |  |  |  |
| ch. 3,4: open line detect | on | enum |  |  |  |
| ch. 5,6: open line detect | on | enum |  |  |  |
| ch. 7,8: open line detect | on | enum |  |  |  |
| ch. 1,2: failsafe mode | min value | enum |  |  |  |
| ch. 3,4: failsafe mode | min value | enum |  |  |  |
| ch. 5,6: failsafe mode | min value | enum |  |  |  |
| ch. 7,8: failsafe mode | min value | enum |  |  |  |
| ch. 1,2: direction | input | enum |  |  |  |
| ch. 3,4: direction | input | enum |  |  |  |
| ch. 5,6: direction | input | enum |  |  |  |
| ch. 7,8: direction | input | enum |  |  |  |
| ch. 1,2: polarity | normal | enum |  |  |  |
| ch. 3,4: polarity | normal | enum |  |  |  |
| ch. 5,6: polarity | normal | enum |  |  |  |
| ch. 7,8: polarity | normal | enum |  |  |  |
| ch. 1,2: damping | off | enum |  |  |  |
| ch. 3,4: damping | off | enum |  |  |  |
| ch. 5,6: damping | off | enum |  |  |  |
| ch. 7,8: damping | off | enum |  |  |  |
| ch. 1: deactivate | false | bool |  |  |  |
| ch. 2: deactivate | false | bool |  |  |  |
| ch. 3: deactivate | false | bool |  |  |  |
| ch. 4: deactivate | false | bool |  |  |  |
| ch. 5: deactivate | false | bool |  |  |  |
| ch. 6: deactivate | false | bool |  |  |  |
| ch. 7: deactivate | false | bool |  |  |  |
| ch. 8: deactivate | false | bool |  |  |  |

Table 768. Controller\_3 (172.16.96.153).3.81.15 DX910\* (DIO8 S) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IX3.81.15.1 | Input 1 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1A.GCS\_1\_IO.FB0 | KSVG02670 |
| IX3.81.15.2 | Input 2 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1B.GCS\_1\_IO.FB0 | KSVG02680 |
| IX3.81.15.3 | Input 3 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1C.GCS\_1\_IO.FB0 | KSVG02690 |
| IX3.81.15.4 | Input 4 | BoolIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_A.FS\_IO | FS01038 |
| IX3.81.15.5 | Input 5 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM01Dax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG02472 |
| IX3.81.15.6 | Input 6 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02Aax3.GCS\_1A.GCS\_1\_IO.FB0 | KSVG02466 |
| IX3.81.15.7 | Input 7 | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02A2.GCS\_1A.GCS\_1\_IO.FB0 | KSVG02467 |
| IX3.81.15.8 | Input 8 | BoolIO |  |  |
| IW3.81.15.9 | All inputs | DwordIO |  |  |
| QX3.81.15.10 | Output 1 | BoolIO |  |  |
| QX3.81.15.11 | Output 2 | BoolIO |  |  |
| QX3.81.15.12 | Output 3 | BoolIO |  |  |
| QX3.81.15.13 | Output 4 | BoolIO |  |  |
| QX3.81.15.14 | Output 5 | BoolIO |  |  |
| QX3.81.15.15 | Output 6 | BoolIO |  |  |
| QX3.81.15.16 | Output 7 | BoolIO |  |  |
| QX3.81.15.17 | Output 8 | BoolIO |  |  |
| QW3.81.15.18 | All outputs | DwordIO |  |  |
| IX3.81.15.19 | Status Input 1 | BoolIO |  |  |
| IX3.81.15.20 | Status Input 2 | BoolIO |  |  |
| IX3.81.15.21 | Status Input 3 | BoolIO |  |  |
| IX3.81.15.22 | Status Input 4 | BoolIO |  |  |
| IX3.81.15.23 | Status Input 5 | BoolIO |  |  |
| IX3.81.15.24 | Status Input 6 | BoolIO |  |  |
| IX3.81.15.25 | Status Input 7 | BoolIO |  |  |
| IX3.81.15.26 | Status Input 8 | BoolIO |  |  |
| IW3.81.15.27 | UnitStatus | HwStatus |  |  |

Table 769. Controller\_3.3.81.15 DX910\* (DIO8 S) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IX3.81.15.1 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.15.2 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1B.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.15.3 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1C.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.15.4 | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_A.FS\_IO |  |  |  |  | false |
| IX3.81.15.5 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM01Dax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.15.6 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02Aax3.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.15.7 | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02A2.GCS\_1A.GCS\_1\_IO.FB0 |  |  |  |  | false |
| IX3.81.15.8 |  |  |  |  |  | false |
| QW3.81.15.9 |  |  |  |  |  |  |
| QX3.81.15.10 |  |  |  |  |  | false |
| QX3.81.15.11 |  |  |  |  |  | false |
| QX3.81.15.12 |  |  |  |  |  | false |
| QX3.81.15.13 |  |  |  |  |  | false |
| QX3.81.15.14 |  |  |  |  |  | false |
| QX3.81.15.15 |  |  |  |  |  | false |
| QX3.81.15.16 |  |  |  |  |  | false |
| QX3.81.15.17 |  |  |  |  |  | false |
| QW3.81.15.18 |  |  |  |  |  |  |
| IX3.81.15.19 |  |  |  |  |  | false |
| IX3.81.15.20 |  |  |  |  |  | false |
| IX3.81.15.21 |  |  |  |  |  | false |
| IX3.81.15.22 |  |  |  |  |  | false |
| IX3.81.15.23 |  |  |  |  |  | false |
| IX3.81.15.24 |  |  |  |  |  | false |
| IX3.81.15.25 |  |  |  |  |  | false |
| IX3.81.15.26 |  |  |  |  |  | false |

Table 770. Controller\_3.3.81.15 DX910\* (DIO8 S) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IX3.81.15.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.15.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1B.GCS\_1\_IO.FB0 |
| IX3.81.15.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.EqM02C.GCS\_1C.GCS\_1\_IO.FB0 |
| IX3.81.15.4 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM15C\_A.FS\_IO |
| IX3.81.15.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM01Dax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.15.6 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02Aax3.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.15.7 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.EqM02A2.GCS\_1A.GCS\_1\_IO.FB0 |
| IX3.81.15.8 |  |  |  |  |
| IW3.81.15.9 |  |  |  |  |
| QX3.81.15.10 |  |  |  |  |
| QX3.81.15.11 |  |  |  |  |
| QX3.81.15.12 |  |  |  |  |
| QX3.81.15.13 |  |  |  |  |
| QX3.81.15.14 |  |  |  |  |
| QX3.81.15.15 |  |  |  |  |
| QX3.81.15.16 |  |  |  |  |
| QX3.81.15.17 |  |  |  |  |
| QW3.81.15.18 |  |  |  |  |
| IX3.81.15.19 |  |  |  |  |
| IX3.81.15.20 |  |  |  |  |
| IX3.81.15.21 |  |  |  |  |
| IX3.81.15.22 |  |  |  |  |
| IX3.81.15.23 |  |  |  |  |
| IX3.81.15.24 |  |  |  |  |
| IX3.81.15.25 |  |  |  |  |
| IX3.81.15.26 |  |  |  |  |

Table 771. Controller\_3.3.81.15 DX910\* (DIO8 S) (Status)

##### 16 AI930\* (AI4H A)

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| ch. 1: short detect | on | enum |  |  |  |
| ch. 2: short detect | on | enum |  |  |  |
| ch. 3: short detect | on | enum |  |  |  |
| ch. 4: short detect | on | enum |  |  |  |
| ch. 1: open line detect | on | enum |  |  |  |
| ch. 2: open line detect | off | enum |  |  |  |
| ch. 3: open line detect | off | enum |  |  |  |
| ch. 4: open line detect | off | enum |  |  |  |
| ch. 1: HART status / range | off / 4..20mA | enum |  |  |  |
| ch. 2: HART status / range | off / 0..20mA | enum |  |  |  |
| ch. 3: HART status / range | off / 0..20mA | enum |  |  |  |
| ch. 4: HART status / range | off / 0..20mA | enum |  |  |  |
| ch. 1: filter (PT1) | off | enum |  |  |  |
| ch. 2: filter (PT1) | off | enum |  |  |  |
| ch. 3: filter (PT1) | off | enum |  |  |  |
| ch. 4: filter (PT1) | off | enum |  |  |  |

Table 772. Controller\_3 (172.16.96.153).3.81.16 AI930\* (AI4H A) (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.81.16.1 | Input 1 | RealIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_2.IND\_2\_IO | LIT02622 |
| IW3.81.16.2 | Input 2 | RealIO |  |  |
| IW3.81.16.3 | Input 3 | RealIO |  |  |
| IW3.81.16.4 | Input 4 | RealIO |  |  |
| IW3.81.16.5 | UnitStatus | HwStatus |  |  |

Table 773. Controller\_3.3.81.16 AI930\* (AI4H A) (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.81.16.1 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_2.IND\_2\_IO | 0.0 | 100.0 |  | 1 | false |
| IW3.81.16.2 |  | 0.0 | 100.0 |  | 1 | false |
| IW3.81.16.3 |  | 0.0 | 100.0 |  | 1 | false |
| IW3.81.16.4 |  | 0.0 | 100.0 |  | 1 | false |

Table 774. Controller\_3.3.81.16 AI930\* (AI4H A) (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.81.16.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.IND\_2.IND\_2\_IO |
| IW3.81.16.2 |  |  |  |  |
| IW3.81.16.3 |  |  |  |  |
| IW3.81.16.4 |  |  |  |  |

Table 775. Controller\_3.3.81.16 AI930\* (AI4H A) (Status)

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| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Fail Safe mode | Stop | enum |  |  |  |
| Operation mode | Profidrive | enum |  |  |  |
| Control-zero mode | Use data | enum |  |  |  |
| Cut-Off time (0 = no cut-off) | 1 | dint |  | 0 | 65535 |
| Failsafe PZD1 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD2 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD3 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD4 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD5 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD6 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD7 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD8 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD9 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD10 | 0 | dint |  | 0 | 65535 |

Table 776. Controller\_3 (172.16.96.153).3.85 ABB Drives RPBA-01 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.85.0 | UnitStatus | HwStatus |  |  |

Table 777. Controller\_3.3.85 ABB Drives RPBA-01 (Connections)

##### 1 PPO Type 2

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.85.1.0 | ID\_PAR\_IN | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.Parameter\_Read | KCV 04655 |
| IW3.85.1.1 | INDEX\_IN | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.Index\_Read | KCV 04655 |
| IW3.85.1.2 | VALUE\_PAR\_IN | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.Value\_Read | KCV 04655 |
| IW3.85.1.3 | SW | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.StatusWord | KCV 04655 |
| IW3.85.1.4 | ACT\_VALUE | DintIO |  |  |
| IW3.85.1.5 | ACT\_FREQ | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.FB | KCV 04655 |
| IW3.85.1.6 | ACT\_CURRENT | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.Amps | KCV 04655 |
| IW3.85.1.7 | ACT\_TORQUE | DintIO |  |  |
| IW3.85.1.8 | FREE\_ACT | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.FB1 | KCV 04655 |
| QW3.85.1.9 | ID\_PAR\_WRITE | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.Parameter\_Write | KCV 04655 |
| QW3.85.1.10 | INDEX\_WRITE | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.Index\_Write | KCV 04655 |
| QW3.85.1.11 | VALUE\_PAR\_WRITE | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.Value\_Write | KCV 04655 |
| QW3.85.1.12 | CW | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.CW | KCV 04655 |
| QW3.85.1.13 | REF\_VALUE\_WRITE | DintIO | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.SP | KCV 04655 |
| QW3.85.1.14 | REF\_FREQ\_WRITE | DintIO |  |  |
| QW3.85.1.15 | REF\_CURRENT\_WRITE | DintIO |  |  |
| QW3.85.1.16 | REF\_TORQUE\_WRITE | DintIO |  |  |
| QW3.85.1.17 | FREE\_REF\_WRITE | DintIO |  |  |
| IW3.85.1.18 | UnitStatus | HwStatus |  |  |

Table 778. Controller\_3.3.85.1 PPO Type 2 (Connections)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.85.1.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.Parameter\_Read |
| IW3.85.1.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.Index\_Read |
| IW3.85.1.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.Value\_Read |
| IW3.85.1.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.StatusWord |
| IW3.85.1.4 |  |  |  |  |
| IW3.85.1.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.FB |
| IW3.85.1.6 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.Amps |
| IW3.85.1.7 |  |  |  |  |
| IW3.85.1.8 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.FB1 |
| QW3.85.1.9 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.Parameter\_Write |
| QW3.85.1.10 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.Index\_Write |
| QW3.85.1.11 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.Value\_Write |
| QW3.85.1.12 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.CW |
| QW3.85.1.13 |  |  |  | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.SP |
| QW3.85.1.14 |  |  |  |  |
| QW3.85.1.15 |  |  |  |  |
| QW3.85.1.16 |  |  |  |  |
| QW3.85.1.17 |  |  |  |  |

Table 779. Controller\_3.3.85.1 PPO Type 2 (Status)

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| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Fail Safe mode | Stop | enum |  |  |  |
| Operation mode | Profidrive | enum |  |  |  |
| Control-zero mode | Use data | enum |  |  |  |
| Cut-Off time (0 = no cut-off) | 1 | dint |  | 0 | 65535 |
| Failsafe PZD1 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD2 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD3 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD4 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD5 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD6 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD7 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD8 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD9 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD10 | 0 | dint |  | 0 | 65535 |

Table 780. Controller\_3 (172.16.96.153).3.86 ABB Drives RPBA-01 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.86.0 | UnitStatus | HwStatus |  |  |

Table 781. Controller\_3.3.86 ABB Drives RPBA-01 (Connections)

##### 1 PPO Type 2

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.86.1.0 | ID\_PAR\_IN | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.Parameter\_Read | KCV 03955 |
| IW3.86.1.1 | INDEX\_IN | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.Index\_Read | KCV 03955 |
| IW3.86.1.2 | VALUE\_PAR\_IN | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.Value\_Read | KCV 03955 |
| IW3.86.1.3 | SW | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.StatusWord | KCV 03955 |
| IW3.86.1.4 | ACT\_VALUE | DintIO |  |  |
| IW3.86.1.5 | ACT\_FREQ | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.FVM\_1\_DintIO.FB | KCV 03955 |
| IW3.86.1.6 | ACT\_CURRENT | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.FVM\_1\_DintIO.Amps | KCV 03955 |
| IW3.86.1.7 | ACT\_TORQUE | DintIO |  |  |
| IW3.86.1.8 | FREE\_ACT | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.FVM\_1\_DintIO.FB1 | KCV 03955 |
| QW3.86.1.9 | ID\_PAR\_WRITE | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.Parameter\_Write | KCV 03955 |
| QW3.86.1.10 | INDEX\_WRITE | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.Index\_Write | KCV 03955 |
| QW3.86.1.11 | VALUE\_PAR\_WRITE | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.Value\_Write | KCV 03955 |
| QW3.86.1.12 | CW | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.FVM\_1\_DintIO.CW | KCV 03955 |
| QW3.86.1.13 | REF\_VALUE\_WRITE | DintIO | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.FVM\_1\_DintIO.SP | KCV 03955 |
| QW3.86.1.14 | REF\_FREQ\_WRITE | DintIO |  |  |
| QW3.86.1.15 | REF\_CURRENT\_WRITE | DintIO |  |  |
| QW3.86.1.16 | REF\_TORQUE\_WRITE | DintIO |  |  |
| QW3.86.1.17 | FREE\_REF\_WRITE | DintIO |  |  |
| IW3.86.1.18 | UnitStatus | HwStatus |  |  |

Table 782. Controller\_3.3.86.1 PPO Type 2 (Connections)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.86.1.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.Parameter\_Read |
| IW3.86.1.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.Index\_Read |
| IW3.86.1.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.Value\_Read |
| IW3.86.1.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.StatusWord |
| IW3.86.1.4 |  |  |  |  |
| IW3.86.1.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.FVM\_1\_DintIO.FB |
| IW3.86.1.6 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.FVM\_1\_DintIO.Amps |
| IW3.86.1.7 |  |  |  |  |
| IW3.86.1.8 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.FVM\_1\_DintIO.FB1 |
| QW3.86.1.9 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.Parameter\_Write |
| QW3.86.1.10 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.Index\_Write |
| QW3.86.1.11 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.Value\_Write |
| QW3.86.1.12 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.FVM\_1\_DintIO.CW |
| QW3.86.1.13 |  |  |  | Cikloprpil\_D6\_laktol.U\_12B39\_00.EqM06Aax3\_A.FVM\_1\_PB.FVM\_1\_DintIO.SP |
| QW3.86.1.14 |  |  |  |  |
| QW3.86.1.15 |  |  |  |  |
| QW3.86.1.16 |  |  |  |  |
| QW3.86.1.17 |  |  |  |  |

Table 783. Controller\_3.3.86.1 PPO Type 2 (Status)

#### 93 ABB Drives RPBA-01

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Fail Safe mode | Stop | enum |  |  |  |
| Operation mode | Profidrive | enum |  |  |  |
| Control-zero mode | Use data | enum |  |  |  |
| Cut-Off time (0 = no cut-off) | 1 | dint |  | 0 | 65535 |
| Failsafe PZD1 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD2 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD3 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD4 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD5 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD6 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD7 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD8 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD9 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD10 | 0 | dint |  | 0 | 65535 |

Table 784. Controller\_3 (172.16.96.153).3.93 ABB Drives RPBA-01 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.93.0 | UnitStatus | HwStatus |  |  |

Table 785. Controller\_3.3.93 ABB Drives RPBA-01 (Connections)

##### 1 PPO Type 2

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.93.1.0 | ID\_PAR\_IN | DintIO | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.Parameter\_Read | KCV04255 |
| IW3.93.1.1 | INDEX\_IN | DintIO | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.Index\_Read | KCV04255 |
| IW3.93.1.2 | VALUE\_PAR\_IN | DintIO | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.Value\_Read | KCV04255 |
| IW3.93.1.3 | SW | DintIO | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.StatusWord | KCV04255 |
| IW3.93.1.4 | ACT\_VALUE | DintIO |  |  |
| IW3.93.1.5 | ACT\_FREQ | DintIO | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.FB | KCV04255 |
| IW3.93.1.6 | ACT\_CURRENT | DintIO | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.Amps | KCV04255 |
| IW3.93.1.7 | ACT\_TORQUE | DintIO |  |  |
| IW3.93.1.8 | FREE\_ACT | DintIO | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.FB1 | KCV04255 |
| QW3.93.1.9 | ID\_PAR\_WRITE | DintIO | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.Parameter\_Write | KCV04255 |
| QW3.93.1.10 | INDEX\_WRITE | DintIO | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.Index\_Write | KCV04255 |
| QW3.93.1.11 | VALUE\_PAR\_WRITE | DintIO | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.Value\_Write | KCV04255 |
| QW3.93.1.12 | CW | DintIO | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.CW | KCV04255 |
| QW3.93.1.13 | REF\_VALUE\_WRITE | DintIO | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.SP | KCV04255 |
| QW3.93.1.14 | REF\_FREQ\_WRITE | DintIO |  |  |
| QW3.93.1.15 | REF\_CURRENT\_WRITE | DintIO |  |  |
| QW3.93.1.16 | REF\_TORQUE\_WRITE | DintIO |  |  |
| QW3.93.1.17 | FREE\_REF\_WRITE | DintIO |  |  |
| IW3.93.1.18 | UnitStatus | HwStatus |  |  |

Table 786. Controller\_3.3.93.1 PPO Type 2 (Connections)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.93.1.0 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.Parameter\_Read |
| IW3.93.1.1 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.Index\_Read |
| IW3.93.1.2 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.Value\_Read |
| IW3.93.1.3 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.StatusWord |
| IW3.93.1.4 |  |  |  |  |
| IW3.93.1.5 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.FB |
| IW3.93.1.6 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.Amps |
| IW3.93.1.7 |  |  |  |  |
| IW3.93.1.8 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.FB1 |
| QW3.93.1.9 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.Parameter\_Write |
| QW3.93.1.10 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.Index\_Write |
| QW3.93.1.11 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.Value\_Write |
| QW3.93.1.12 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.CW |
| QW3.93.1.13 |  |  |  | Ellato\_3.U\_14C42\_Tart.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.SP |
| QW3.93.1.14 |  |  |  |  |
| QW3.93.1.15 |  |  |  |  |
| QW3.93.1.16 |  |  |  |  |
| QW3.93.1.17 |  |  |  |  |

Table 787. Controller\_3.3.93.1 PPO Type 2 (Status)

#### 94 ABB Drives RPBA-01

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Fail Safe mode | Stop | enum |  |  |  |
| Operation mode | Profidrive | enum |  |  |  |
| Control-zero mode | Use data | enum |  |  |  |
| Cut-Off time (0 = no cut-off) | 1 | dint |  | 0 | 65535 |
| Failsafe PZD1 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD2 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD3 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD4 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD5 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD6 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD7 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD8 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD9 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD10 | 0 | dint |  | 0 | 65535 |

Table 788. Controller\_3 (172.16.96.153).3.94 ABB Drives RPBA-01 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.94.0 | UnitStatus | HwStatus |  |  |

Table 789. Controller\_3.3.94 ABB Drives RPBA-01 (Connections)

##### 1 PPO Type 2

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.94.1.0 | ID\_PAR\_IN | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.Parameter\_Read | KCV 04755 |
| IW3.94.1.1 | INDEX\_IN | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.Index\_Read | KCV 04755 |
| IW3.94.1.2 | VALUE\_PAR\_IN | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.Value\_Read | KCV 04755 |
| IW3.94.1.3 | SW | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.StatusWord | KCV 04755 |
| IW3.94.1.4 | ACT\_VALUE | DintIO |  |  |
| IW3.94.1.5 | ACT\_FREQ | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.FB | KCV 04755 |
| IW3.94.1.6 | ACT\_CURRENT | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.Amps | KCV 04755 |
| IW3.94.1.7 | ACT\_TORQUE | DintIO |  |  |
| IW3.94.1.8 | FREE\_ACT | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.FB1 | KCV 04755 |
| QW3.94.1.9 | ID\_PAR\_WRITE | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.Parameter\_Write | KCV 04755 |
| QW3.94.1.10 | INDEX\_WRITE | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.Index\_Write | KCV 04755 |
| QW3.94.1.11 | VALUE\_PAR\_WRITE | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.Value\_Write | KCV 04755 |
| QW3.94.1.12 | CW | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.CW | KCV 04755 |
| QW3.94.1.13 | REF\_VALUE\_WRITE | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.SP | KCV 04755 |
| QW3.94.1.14 | REF\_FREQ\_WRITE | DintIO |  |  |
| QW3.94.1.15 | REF\_CURRENT\_WRITE | DintIO |  |  |
| QW3.94.1.16 | REF\_TORQUE\_WRITE | DintIO |  |  |
| QW3.94.1.17 | FREE\_REF\_WRITE | DintIO |  |  |
| IW3.94.1.18 | UnitStatus | HwStatus |  |  |

Table 790. Controller\_3.3.94.1 PPO Type 2 (Connections)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.94.1.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.Parameter\_Read |
| IW3.94.1.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.Index\_Read |
| IW3.94.1.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.Value\_Read |
| IW3.94.1.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.StatusWord |
| IW3.94.1.4 |  |  |  |  |
| IW3.94.1.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.FB |
| IW3.94.1.6 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.Amps |
| IW3.94.1.7 |  |  |  |  |
| IW3.94.1.8 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.FB1 |
| QW3.94.1.9 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.Parameter\_Write |
| QW3.94.1.10 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.Index\_Write |
| QW3.94.1.11 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.Value\_Write |
| QW3.94.1.12 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.CW |
| QW3.94.1.13 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14B47\_00.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.SP |
| QW3.94.1.14 |  |  |  |  |
| QW3.94.1.15 |  |  |  |  |
| QW3.94.1.16 |  |  |  |  |
| QW3.94.1.17 |  |  |  |  |

Table 791. Controller\_3.3.94.1 PPO Type 2 (Status)

#### 95 ABB Drives RPBA-01

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Fail Safe mode | Stop | enum |  |  |  |
| Operation mode | Profidrive | enum |  |  |  |
| Control-zero mode | Use data | enum |  |  |  |
| Cut-Off time (0 = no cut-off) | 1 | dint |  | 0 | 65535 |
| Failsafe PZD1 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD2 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD3 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD4 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD5 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD6 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD7 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD8 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD9 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD10 | 0 | dint |  | 0 | 65535 |

Table 792. Controller\_3 (172.16.96.153).3.95 ABB Drives RPBA-01 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.95.0 | UnitStatus | HwStatus |  |  |

Table 793. Controller\_3.3.95 ABB Drives RPBA-01 (Connections)

##### 1 PPO Type 2

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.95.1.0 | ID\_PAR\_IN | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.Parameter\_Read | KCV 00255 |
| IW3.95.1.1 | INDEX\_IN | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.Index\_Read | KCV 00255 |
| IW3.95.1.2 | VALUE\_PAR\_IN | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.Value\_Read | KCV 00255 |
| IW3.95.1.3 | SW | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.StatusWord | KCV 00255 |
| IW3.95.1.4 | ACT\_VALUE | DintIO |  |  |
| IW3.95.1.5 | ACT\_FREQ | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.FB | KCV 00255 |
| IW3.95.1.6 | ACT\_CURRENT | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.Amps | KCV 00255 |
| IW3.95.1.7 | ACT\_TORQUE | DintIO |  |  |
| IW3.95.1.8 | FREE\_ACT | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.FB1 | KCV 00255 |
| QW3.95.1.9 | ID\_PAR\_WRITE | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.Parameter\_Write | KCV 00255 |
| QW3.95.1.10 | INDEX\_WRITE | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.Index\_Write | KCV 00255 |
| QW3.95.1.11 | VALUE\_PAR\_WRITE | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.Value\_Write | KCV 00255 |
| QW3.95.1.12 | CW | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.CW | KCV 00255 |
| QW3.95.1.13 | REF\_VALUE\_WRITE | DintIO | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.SP | KCV 00255 |
| QW3.95.1.14 | REF\_FREQ\_WRITE | DintIO |  |  |
| QW3.95.1.15 | REF\_CURRENT\_WRITE | DintIO |  |  |
| QW3.95.1.16 | REF\_TORQUE\_WRITE | DintIO |  |  |
| QW3.95.1.17 | FREE\_REF\_WRITE | DintIO |  |  |
| IW3.95.1.18 | UnitStatus | HwStatus |  |  |

Table 794. Controller\_3.3.95.1 PPO Type 2 (Connections)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.95.1.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.Parameter\_Read |
| IW3.95.1.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.Index\_Read |
| IW3.95.1.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.Value\_Read |
| IW3.95.1.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.StatusWord |
| IW3.95.1.4 |  |  |  |  |
| IW3.95.1.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.FB |
| IW3.95.1.6 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.Amps |
| IW3.95.1.7 |  |  |  |  |
| IW3.95.1.8 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.FB1 |
| QW3.95.1.9 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.Parameter\_Write |
| QW3.95.1.10 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.Index\_Write |
| QW3.95.1.11 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.Value\_Write |
| QW3.95.1.12 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.CW |
| QW3.95.1.13 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_02C02\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.SP |
| QW3.95.1.14 |  |  |  |  |
| QW3.95.1.15 |  |  |  |  |
| QW3.95.1.16 |  |  |  |  |
| QW3.95.1.17 |  |  |  |  |

Table 795. Controller\_3.3.95.1 PPO Type 2 (Status)

#### 96 ABB Drives RPBA-01

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Fail Safe mode | Stop | enum |  |  |  |
| Operation mode | Profidrive | enum |  |  |  |
| Control-zero mode | Use data | enum |  |  |  |
| Cut-Off time (0 = no cut-off) | 1 | dint |  | 0 | 65535 |
| Failsafe PZD1 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD2 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD3 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD4 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD5 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD6 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD7 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD8 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD9 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD10 | 0 | dint |  | 0 | 65535 |

Table 796. Controller\_3 (172.16.96.153).3.96 ABB Drives RPBA-01 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.96.0 | UnitStatus | HwStatus |  |  |

Table 797. Controller\_3.3.96 ABB Drives RPBA-01 (Connections)

##### 1 PPO Type 2

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.96.1.0 | ID\_PAR\_IN | DintIO | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.Parameter\_Read | KCV00655 |
| IW3.96.1.1 | INDEX\_IN | DintIO | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.Index\_Read | KCV00655 |
| IW3.96.1.2 | VALUE\_PAR\_IN | DintIO | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.Value\_Read | KCV00655 |
| IW3.96.1.3 | SW | DintIO | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.StatusWord | KCV00655 |
| IW3.96.1.4 | ACT\_VALUE | DintIO |  |  |
| IW3.96.1.5 | ACT\_FREQ | DintIO | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.FB | KCV00655 |
| IW3.96.1.6 | ACT\_CURRENT | DintIO | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.Amps | KCV00655 |
| IW3.96.1.7 | ACT\_TORQUE | DintIO |  |  |
| IW3.96.1.8 | FREE\_ACT | DintIO | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.FB1 | KCV00655 |
| QW3.96.1.9 | ID\_PAR\_WRITE | DintIO | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.Parameter\_Write | KCV00655 |
| QW3.96.1.10 | INDEX\_WRITE | DintIO | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.Index\_Write | KCV00655 |
| QW3.96.1.11 | VALUE\_PAR\_WRITE | DintIO | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.Value\_Write | KCV00655 |
| QW3.96.1.12 | CW | DintIO | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.CW | KCV00655 |
| QW3.96.1.13 | REF\_VALUE\_WRITE | DintIO | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.SP | KCV00655 |
| QW3.96.1.14 | REF\_FREQ\_WRITE | DintIO |  |  |
| QW3.96.1.15 | REF\_CURRENT\_WRITE | DintIO |  |  |
| QW3.96.1.16 | REF\_TORQUE\_WRITE | DintIO |  |  |
| QW3.96.1.17 | FREE\_REF\_WRITE | DintIO |  |  |
| IW3.96.1.18 | UnitStatus | HwStatus |  |  |

Table 798. Controller\_3.3.96.1 PPO Type 2 (Connections)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.96.1.0 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.Parameter\_Read |
| IW3.96.1.1 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.Index\_Read |
| IW3.96.1.2 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.Value\_Read |
| IW3.96.1.3 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.StatusWord |
| IW3.96.1.4 |  |  |  |  |
| IW3.96.1.5 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.FB |
| IW3.96.1.6 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.Amps |
| IW3.96.1.7 |  |  |  |  |
| IW3.96.1.8 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.FB1 |
| QW3.96.1.9 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.Parameter\_Write |
| QW3.96.1.10 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.Index\_Write |
| QW3.96.1.11 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.Value\_Write |
| QW3.96.1.12 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.CW |
| QW3.96.1.13 |  |  |  | Ellato\_3.U\_02C06\_Tart.EqM06A.FVM\_1\_PB.FVM\_1\_DintIO.SP |
| QW3.96.1.14 |  |  |  |  |
| QW3.96.1.15 |  |  |  |  |
| QW3.96.1.16 |  |  |  |  |
| QW3.96.1.17 |  |  |  |  |

Table 799. Controller\_3.3.96.1 PPO Type 2 (Status)

#### 97 ABB Drives RPBA-01

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Fail Safe mode | Stop | enum |  |  |  |
| Operation mode | Profidrive | enum |  |  |  |
| Control-zero mode | Use data | enum |  |  |  |
| Cut-Off time (0 = no cut-off) | 1 | dint |  | 0 | 65535 |
| Failsafe PZD1 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD2 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD3 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD4 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD5 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD6 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD7 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD8 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD9 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD10 | 0 | dint |  | 0 | 65535 |

Table 800. Controller\_3 (172.16.96.153).3.97 ABB Drives RPBA-01 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.97.0 | UnitStatus | HwStatus |  |  |

Table 801. Controller\_3.3.97 ABB Drives RPBA-01 (Connections)

##### 1 PPO Type 2

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.97.1.0 | ID\_PAR\_IN | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.Parameter\_Read | KCV 01055 |
| IW3.97.1.1 | INDEX\_IN | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.Index\_Read | KCV 01055 |
| IW3.97.1.2 | VALUE\_PAR\_IN | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.Value\_Read | KCV 01055 |
| IW3.97.1.3 | SW | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.StatusWord | KCV 01055 |
| IW3.97.1.4 | ACT\_VALUE | DintIO |  |  |
| IW3.97.1.5 | ACT\_FREQ | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.FB | KCV 01055 |
| IW3.97.1.6 | ACT\_CURRENT | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.Amps | KCV 01055 |
| IW3.97.1.7 | ACT\_TORQUE | DintIO |  |  |
| IW3.97.1.8 | FREE\_ACT | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.FB1 | KCV 01055 |
| QW3.97.1.9 | ID\_PAR\_WRITE | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.Parameter\_Write | KCV 01055 |
| QW3.97.1.10 | INDEX\_WRITE | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.Index\_Write | KCV 01055 |
| QW3.97.1.11 | VALUE\_PAR\_WRITE | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.Value\_Write | KCV 01055 |
| QW3.97.1.12 | CW | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.CW | KCV 01055 |
| QW3.97.1.13 | REF\_VALUE\_WRITE | DintIO | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.SP | KCV 01055 |
| QW3.97.1.14 | REF\_FREQ\_WRITE | DintIO |  |  |
| QW3.97.1.15 | REF\_CURRENT\_WRITE | DintIO |  |  |
| QW3.97.1.16 | REF\_TORQUE\_WRITE | DintIO |  |  |
| QW3.97.1.17 | FREE\_REF\_WRITE | DintIO |  |  |
| IW3.97.1.18 | UnitStatus | HwStatus |  |  |

Table 802. Controller\_3.3.97.1 PPO Type 2 (Connections)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.97.1.0 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.Parameter\_Read |
| IW3.97.1.1 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.Index\_Read |
| IW3.97.1.2 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.Value\_Read |
| IW3.97.1.3 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.StatusWord |
| IW3.97.1.4 |  |  |  |  |
| IW3.97.1.5 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.FB |
| IW3.97.1.6 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.Amps |
| IW3.97.1.7 |  |  |  |  |
| IW3.97.1.8 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.FB1 |
| QW3.97.1.9 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.Parameter\_Write |
| QW3.97.1.10 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.Index\_Write |
| QW3.97.1.11 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.Value\_Write |
| QW3.97.1.12 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.CW |
| QW3.97.1.13 |  |  |  | Ciklopro\_Ad\_Enolet\_3.U\_02B10\_00.EqM06Aax3.FVM\_1\_PB.FVM\_1\_DintIO.SP |
| QW3.97.1.14 |  |  |  |  |
| QW3.97.1.15 |  |  |  |  |
| QW3.97.1.16 |  |  |  |  |
| QW3.97.1.17 |  |  |  |  |

Table 803. Controller\_3.3.97.1 PPO Type 2 (Status)

#### 98 ABB Drives RPBA-01

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Fail Safe mode | Stop | enum |  |  |  |
| Operation mode | Profidrive | enum |  |  |  |
| Control-zero mode | Use data | enum |  |  |  |
| Cut-Off time (0 = no cut-off) | 1 | dint |  | 0 | 65535 |
| Failsafe PZD1 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD2 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD3 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD4 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD5 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD6 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD7 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD8 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD9 | 0 | dint |  | 0 | 65535 |
| Failsafe PZD10 | 0 | dint |  | 0 | 65535 |

Table 804. Controller\_3 (172.16.96.153).3.98 ABB Drives RPBA-01 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.98.0 | UnitStatus | HwStatus |  |  |

Table 805. Controller\_3.3.98 ABB Drives RPBA-01 (Connections)

##### 1 PPO Type 2

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.98.1.0 | ID\_PAR\_IN | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.Parameter\_Read | KCV 01252 |
| IW3.98.1.1 | INDEX\_IN | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.Index\_Read | KCV 01252 |
| IW3.98.1.2 | VALUE\_PAR\_IN | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.Value\_Read | KCV 01252 |
| IW3.98.1.3 | SW | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.StatusWord | KCV 01252 |
| IW3.98.1.4 | ACT\_VALUE | DintIO |  |  |
| IW3.98.1.5 | ACT\_FREQ | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.FVM\_1\_DintIO.FB | KCV 01252 |
| IW3.98.1.6 | ACT\_CURRENT | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.FVM\_1\_DintIO.Amps | KCV 01252 |
| IW3.98.1.7 | ACT\_TORQUE | DintIO |  |  |
| IW3.98.1.8 | FREE\_ACT | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.FVM\_1\_DintIO.FB1 | KCV 01252 |
| QW3.98.1.9 | ID\_PAR\_WRITE | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.Parameter\_Write | KCV 01252 |
| QW3.98.1.10 | INDEX\_WRITE | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.Index\_Write | KCV 01252 |
| QW3.98.1.11 | VALUE\_PAR\_WRITE | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.Value\_Write | KCV 01252 |
| QW3.98.1.12 | CW | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.FVM\_1\_DintIO.CW | KCV 01252 |
| QW3.98.1.13 | REF\_VALUE\_WRITE | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.FVM\_1\_DintIO.SP | KCV 01252 |
| QW3.98.1.14 | REF\_FREQ\_WRITE | DintIO |  |  |
| QW3.98.1.15 | REF\_CURRENT\_WRITE | DintIO |  |  |
| QW3.98.1.16 | REF\_TORQUE\_WRITE | DintIO |  |  |
| QW3.98.1.17 | FREE\_REF\_WRITE | DintIO |  |  |
| IW3.98.1.18 | UnitStatus | HwStatus |  |  |

Table 806. Controller\_3.3.98.1 PPO Type 2 (Connections)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.98.1.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.Parameter\_Read |
| IW3.98.1.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.Index\_Read |
| IW3.98.1.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.Value\_Read |
| IW3.98.1.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.StatusWord |
| IW3.98.1.4 |  |  |  |  |
| IW3.98.1.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.FVM\_1\_DintIO.FB |
| IW3.98.1.6 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.FVM\_1\_DintIO.Amps |
| IW3.98.1.7 |  |  |  |  |
| IW3.98.1.8 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.FVM\_1\_DintIO.FB1 |
| QW3.98.1.9 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.Parameter\_Write |
| QW3.98.1.10 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.Index\_Write |
| QW3.98.1.11 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.Value\_Write |
| QW3.98.1.12 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.FVM\_1\_DintIO.CW |
| QW3.98.1.13 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.EqM15Aax3.KS.FVM\_1\_DintIO.SP |
| QW3.98.1.14 |  |  |  |  |
| QW3.98.1.15 |  |  |  |  |
| QW3.98.1.16 |  |  |  |  |
| QW3.98.1.17 |  |  |  |  |

Table 807. Controller\_3.3.98.1 PPO Type 2 (Status)

#### 99 EM 277 PROFIBUS-DP

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| I/O Offset in the V-memory | 5000 | dint |  | 0 | 10239 |

Table 808. Controller\_3 (172.16.96.153).3.99 EM 277 PROFIBUS-DP (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.99.0 | UnitStatus | HwStatus |  |  |

Table 809. Controller\_3.3.99 EM 277 PROFIBUS-DP (Connections)

##### 0 16 Byte buffer I/O -

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.99.0.0 | Valve\_Pos | RealIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.CNTValvePos |  |
| IW3.99.0.1 | Temp1 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Prod\_temp |  |
| IW3.99.0.2 | Temp2 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Nitrogen\_Temp |  |
| IW3.99.0.3 | Setpoint\_FB | DintIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Setpoint\_FB |  |
| IX3.99.0.4 | Dummy\_Bit | BoolIO |  |  |
| IX3.99.0.5 | Dummy\_Bit | BoolIO |  |  |
| IX3.99.0.6 | LOC\_Mode | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.LOC\_Mode |  |
| IX3.99.0.7 | Auto\_Mode | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Auto\_Mode |  |
| IX3.99.0.8 | Dummy\_Bit | BoolIO |  |  |
| IX3.99.0.9 | Dummy\_Bit | BoolIO |  |  |
| IX3.99.0.10 | Dummy\_Bit | BoolIO |  |  |
| IX3.99.0.11 | Dummy\_Bit | BoolIO |  |  |
| IX3.99.0.12 | ValveBlocked | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.LinValveState |  |
| IX3.99.0.13 | Dummy\_Bit | BoolIO |  |  |
| IX3.99.0.14 | Dummy\_Bit | BoolIO |  |  |
| IX3.99.0.15 | Dummy\_Bit | BoolIO |  |  |
| IX3.99.0.16 | Dummy\_Bit | BoolIO |  |  |
| IX3.99.0.17 | Dummy\_Bit | BoolIO |  |  |
| IX3.99.0.18 | Dummy\_Bit | BoolIO |  |  |
| IX3.99.0.19 | Dummy\_Bit | BoolIO |  |  |
| QW3.99.0.20 | Temp\_SP | RealIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Temp\_Sp |  |
| QW3.99.0.21 | Dummy\_Dint | DintIO |  |  |
| QW3.99.0.22 | Dummy\_Dint | DintIO |  |  |
| QW3.99.0.23 | Dummy\_Dint | DintIO |  |  |
| QW3.99.0.24 | Dummy\_Dint | DintIO |  |  |
| QX3.99.0.25 | Start | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Start\_Stop |  |
| QX3.99.0.26 | Pump\_Running | BoolIO | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Pump\_SW |  |
| QX3.99.0.27 | Dummy\_Bit | BoolIO |  |  |
| QX3.99.0.28 | Dummy\_Bit | BoolIO |  |  |
| QX3.99.0.29 | Dummy\_Bit | BoolIO |  |  |
| QX3.99.0.30 | Dummy\_Bit | BoolIO |  |  |
| QX3.99.0.31 | Dummy\_Bit | BoolIO |  |  |
| QX3.99.0.32 | Dummy\_Bit | BoolIO |  |  |
| IW3.99.0.33 | UnitStatus | HwStatus |  |  |

Table 810. Controller\_3.3.99.0 16 Byte buffer I/O - (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.99.0.0 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.CNTValvePos | 0.0 | 100.0 |  | 1 | false |
| IW3.99.0.1 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Prod\_temp |  |  |  |  |  |
| IW3.99.0.2 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Nitrogen\_Temp |  |  |  |  |  |
| IW3.99.0.3 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Setpoint\_FB |  |  |  |  |  |
| IX3.99.0.4 |  |  |  |  |  | false |
| IX3.99.0.5 |  |  |  |  |  | false |
| IX3.99.0.6 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.LOC\_Mode |  |  |  |  | false |
| IX3.99.0.7 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Auto\_Mode |  |  |  |  | false |
| IX3.99.0.8 |  |  |  |  |  | false |
| IX3.99.0.9 |  |  |  |  |  | false |
| IX3.99.0.10 |  |  |  |  |  | false |
| IX3.99.0.11 |  |  |  |  |  | false |
| IX3.99.0.12 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.LinValveState |  |  |  |  | false |
| IX3.99.0.13 |  |  |  |  |  | false |
| IX3.99.0.14 |  |  |  |  |  | false |
| IX3.99.0.15 |  |  |  |  |  | false |
| IX3.99.0.16 |  |  |  |  |  | false |
| IX3.99.0.17 |  |  |  |  |  | false |
| IX3.99.0.18 |  |  |  |  |  | false |
| IX3.99.0.19 |  |  |  |  |  | false |
| QW3.99.0.20 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Temp\_Sp | 0.0 | 100.0 |  | 1 | false |
| QW3.99.0.21 |  |  |  |  |  |  |
| QW3.99.0.22 |  |  |  |  |  |  |
| QW3.99.0.23 |  |  |  |  |  |  |
| QW3.99.0.24 |  |  |  |  |  |  |
| QX3.99.0.25 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Start\_Stop |  |  |  |  | false |
| QX3.99.0.26 | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Pump\_SW |  |  |  |  | false |
| QX3.99.0.27 |  |  |  |  |  | false |
| QX3.99.0.28 |  |  |  |  |  | false |
| QX3.99.0.29 |  |  |  |  |  | false |
| QX3.99.0.30 |  |  |  |  |  | false |
| QX3.99.0.31 |  |  |  |  |  | false |
| QX3.99.0.32 |  |  |  |  |  | false |

Table 811. Controller\_3.3.99.0 16 Byte buffer I/O - (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.99.0.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.CNTValvePos |
| IW3.99.0.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Prod\_temp |
| IW3.99.0.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Nitrogen\_Temp |
| IW3.99.0.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Setpoint\_FB |
| IX3.99.0.4 |  |  |  |  |
| IX3.99.0.5 |  |  |  |  |
| IX3.99.0.6 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.LOC\_Mode |
| IX3.99.0.7 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Auto\_Mode |
| IX3.99.0.8 |  |  |  |  |
| IX3.99.0.9 |  |  |  |  |
| IX3.99.0.10 |  |  |  |  |
| IX3.99.0.11 |  |  |  |  |
| IX3.99.0.12 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.LinValveState |
| IX3.99.0.13 |  |  |  |  |
| IX3.99.0.14 |  |  |  |  |
| IX3.99.0.15 |  |  |  |  |
| IX3.99.0.16 |  |  |  |  |
| IX3.99.0.17 |  |  |  |  |
| IX3.99.0.18 |  |  |  |  |
| IX3.99.0.19 |  |  |  |  |
| QW3.99.0.20 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Temp\_Sp |
| QW3.99.0.21 |  |  |  |  |
| QW3.99.0.22 |  |  |  |  |
| QW3.99.0.23 |  |  |  |  |
| QW3.99.0.24 |  |  |  |  |
| QX3.99.0.25 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Start\_Stop |
| QX3.99.0.26 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_15A26\_00.Messer\_PLC.Pump\_SW |
| QX3.99.0.27 |  |  |  |  |
| QX3.99.0.28 |  |  |  |  |
| QX3.99.0.29 |  |  |  |  |
| QX3.99.0.30 |  |  |  |  |
| QX3.99.0.31 |  |  |  |  |
| QX3.99.0.32 |  |  |  |  |

Table 812. Controller\_3.3.99.0 16 Byte buffer I/O - (Status)

#### 111 Ellerwerk\_S7-315-2DP\_FW11

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| Diagnostics Alarm | enable | enum |  |  |  |
| Process Alarm | enable | enum |  |  |  |

Table 813. Controller\_3 (172.16.96.153).3.111 Ellerwerk\_S7-315-2DP\_FW11 (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.111.0 | UnitStatus | HwStatus |  |  |

Table 814. Controller\_3.3.111 Ellerwerk\_S7-315-2DP\_FW11 (Connections)

##### 1 first ID in general

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.111.1.0 | UnitStatus | HwStatus |  |  |

Table 815. Controller\_3.3.111.1 first ID in general (Connections)

##### 2 second ID in general

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.111.2.0 | UnitStatus | HwStatus |  |  |

Table 816. Controller\_3.3.111.2 second ID in general (Connections)

##### 3 third ID in general

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.111.3.0 | UnitStatus | HwStatus |  |  |

Table 817. Controller\_3.3.111.3 third ID in general (Connections)

##### 4 Master\_O Slave\_I 16 wo ttl len

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QW3.111.4.0 | Output\_00 | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Output\_00 |  |
| QW3.111.4.1 | Output\_02 | DintIO |  |  |
| QW3.111.4.2 | Output\_04 | DintIO |  |  |
| QW3.111.4.3 | Output\_06 | DintIO |  |  |
| QW3.111.4.4 | Output\_08 | DintIO |  |  |
| QW3.111.4.5 | Output\_10 | DintIO |  |  |
| QW3.111.4.6 | Output\_12 | DintIO |  |  |
| QW3.111.4.7 | Output\_14 | DintIO |  |  |
| QW3.111.4.8 | Output\_16 | DintIO |  |  |
| QW3.111.4.9 | Output\_18 | DintIO |  |  |
| QW3.111.4.10 | Output\_20 | DintIO |  |  |
| QW3.111.4.11 | Output\_22 | DintIO |  |  |
| QW3.111.4.12 | Output\_24 | DintIO |  |  |
| QW3.111.4.13 | Output\_26 | DintIO |  |  |
| QW3.111.4.14 | Output\_28 | DintIO |  |  |
| QW3.111.4.15 | Output\_30 | DintIO |  |  |
| IW3.111.4.16 | UnitStatus | HwStatus |  |  |

Table 818. Controller\_3.3.111.4 Master\_O Slave\_I 16 wo ttl len (Connections)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QW3.111.4.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Output\_00 |
| QW3.111.4.1 |  |  |  |  |
| QW3.111.4.2 |  |  |  |  |
| QW3.111.4.3 |  |  |  |  |
| QW3.111.4.4 |  |  |  |  |
| QW3.111.4.5 |  |  |  |  |
| QW3.111.4.6 |  |  |  |  |
| QW3.111.4.7 |  |  |  |  |
| QW3.111.4.8 |  |  |  |  |
| QW3.111.4.9 |  |  |  |  |
| QW3.111.4.10 |  |  |  |  |
| QW3.111.4.11 |  |  |  |  |
| QW3.111.4.12 |  |  |  |  |
| QW3.111.4.13 |  |  |  |  |
| QW3.111.4.14 |  |  |  |  |
| QW3.111.4.15 |  |  |  |  |

Table 819. Controller\_3.3.111.4 Master\_O Slave\_I 16 wo ttl len (Status)

##### 5 Master\_I Slave\_O 16 wo ttl len

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.111.5.0 | Input\_00 | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Input\_00 |  |
| IW3.111.5.1 | Input\_02 | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Input\_02 |  |
| IW3.111.5.2 | Input\_04 | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Input\_04 |  |
| IW3.111.5.3 | Speed Centrifuge Act Value | RealIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Speed\_Centrifuge\_Actual\_Value |  |
| IW3.111.5.4 | Bearing Temp Drive Side | RealIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Bearing\_Temperature\_DriveSide |  |
| IW3.111.5.5 | Bearing Temp Drum Side | RealIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Bearing\_Temperature\_DrumSide |  |
| IW3.111.5.6 | Process Step | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Process\_Step |  |
| IW3.111.5.7 | Dummy\_Dint | DintIO |  |  |
| IW3.111.5.8 | Process Rem time | DintIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Process\_Remaning\_time |  |
| IW3.111.5.9 | Dummy\_Dint | DintIO |  |  |
| IW3.111.5.10 | Imbalance Value | RealIO | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Imbalance\_Value |  |
| IW3.111.5.11 | Dummy\_Dint | DintIO |  |  |
| IW3.111.5.12 | UnitStatus | HwStatus |  |  |

Table 820. Controller\_3.3.111.5 Master\_I Slave\_O 16 wo ttl len (Connections)

| **Channel** | **Variable** | **Min** | **Max** | **Unit** | **Fraction** | **Inverted** |
| --- | --- | --- | --- | --- | --- | --- |
| IW3.111.5.0 | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Input\_00 |  |  |  |  |  |
| IW3.111.5.1 | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Input\_02 |  |  |  |  |  |
| IW3.111.5.2 | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Input\_04 |  |  |  |  |  |
| IW3.111.5.3 | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Speed\_Centrifuge\_Actual\_Value | 0.0 | 100.0 |  | 1 | false |
| IW3.111.5.4 | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Bearing\_Temperature\_DriveSide | 0.0 | 100.0 |  | 1 | false |
| IW3.111.5.5 | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Bearing\_Temperature\_DrumSide | 0.0 | 100.0 |  | 1 | false |
| IW3.111.5.6 | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Process\_Step |  |  |  |  |  |
| IW3.111.5.7 |  |  |  |  |  |  |
| IW3.111.5.8 | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Process\_Remaning\_time |  |  |  |  |  |
| IW3.111.5.9 |  |  |  |  |  |  |
| IW3.111.5.10 | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Imbalance\_Value | 0.0 | 100.0 |  | 1 | false |
| IW3.111.5.11 |  |  |  |  |  |  |

Table 821. Controller\_3.3.111.5 Master\_I Slave\_O 16 wo ttl len (Properties)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.111.5.0 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Input\_00 |
| IW3.111.5.1 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Input\_02 |
| IW3.111.5.2 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Input\_04 |
| IW3.111.5.3 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Speed\_Centrifuge\_Actual\_Value |
| IW3.111.5.4 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Bearing\_Temperature\_DriveSide |
| IW3.111.5.5 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Bearing\_Temperature\_DrumSide |
| IW3.111.5.6 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Process\_Step |
| IW3.111.5.7 |  |  |  |  |
| IW3.111.5.8 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Process\_Remaning\_time |
| IW3.111.5.9 |  |  |  |  |
| IW3.111.5.10 |  |  |  | Cikloprpil\_D6\_laktol.U\_08A12\_00.Ellerwerk\_PLC.PLC\_Data.Imbalance\_Value |
| IW3.111.5.11 |  |  |  |  |

Table 822. Controller\_3.3.111.5 Master\_I Slave\_O 16 wo ttl len (Status)

#### 112 PLC\_CP342-5 Cu Slave Universal

| **Parameter** | **Value** | **Type** | **Unit** | **Min** | **Max** |
| --- | --- | --- | --- | --- | --- |
| User Parameter Data | 0x40 0x00 0x00 | string |  |  | 1250 |

Table 823. Controller\_3 (172.16.96.153).3.112 PLC\_CP342-5 Cu Slave Universal (Settings)

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.112.0 | UnitStatus | HwStatus |  |  |

Table 824. Controller\_3.3.112 PLC\_CP342-5 Cu Slave Universal (Connections)

##### 1 32word AO/constcy complete

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| QW3.112.1.0 | DW000 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Write\_ST\_01\_SET |  |
| QW3.112.1.1 | DW002 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Write\_LT\_01\_SET |  |
| QW3.112.1.2 | DW004 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Write\_IT\_M1\_SET |  |
| QW3.112.1.3 | DW006 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Write\_TT\_101\_SET |  |
| QW3.112.1.4 | DW008 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Write\_DPT\_01\_SET |  |
| QW3.112.1.5 | DW010 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Write\_PT\_01\_SET |  |
| QW3.112.1.6 | DW012 | DintIO |  |  |
| QW3.112.1.7 | DW014 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_01\_ACT |  |
| QW3.112.1.8 | DW016 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_02\_ACT |  |
| QW3.112.1.9 | DW018 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_03\_ACT |  |
| QW3.112.1.10 | DW020 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_05\_ACT |  |
| QW3.112.1.11 | DW022 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_06\_ACT |  |
| QW3.112.1.12 | DW024 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_07\_ACT |  |
| QW3.112.1.13 | DW026 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_08\_ACT |  |
| QW3.112.1.14 | DW028 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_09\_ACT |  |
| QW3.112.1.15 | DW030 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_10\_ACT |  |
| QW3.112.1.16 | DW032 | DintIO |  |  |
| QW3.112.1.17 | DW034 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.WDS\_DCS |  |
| QW3.112.1.18 | DW036 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.LOC\_EN |  |
| QW3.112.1.19 | DW038 | DintIO |  |  |
| QW3.112.1.20 | DW040 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW40\_WR |  |
| QW3.112.1.21 | DW042 | DintIO |  |  |
| QW3.112.1.22 | DW044 | DintIO |  |  |
| QW3.112.1.23 | DW046 | DintIO |  |  |
| QW3.112.1.24 | DW048 | DintIO |  |  |
| QW3.112.1.25 | DW050 | DintIO |  |  |
| QW3.112.1.26 | DW052 | DintIO |  |  |
| QW3.112.1.27 | DW054 | DintIO |  |  |
| QW3.112.1.28 | DW056 | DintIO |  |  |
| QW3.112.1.29 | DW058 | DintIO |  |  |
| QW3.112.1.30 | DW060 | DintIO |  |  |
| QW3.112.1.31 | DW062 | DintIO |  |  |
| IW3.112.1.32 | UnitStatus | HwStatus |  |  |

Table 825. Controller\_3.3.112.1 32word AO/constcy complete (Connections)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| QW3.112.1.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Write\_ST\_01\_SET |
| QW3.112.1.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Write\_LT\_01\_SET |
| QW3.112.1.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Write\_IT\_M1\_SET |
| QW3.112.1.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Write\_TT\_101\_SET |
| QW3.112.1.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Write\_DPT\_01\_SET |
| QW3.112.1.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Write\_PT\_01\_SET |
| QW3.112.1.6 |  |  |  |  |
| QW3.112.1.7 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_01\_ACT |
| QW3.112.1.8 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_02\_ACT |
| QW3.112.1.9 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_03\_ACT |
| QW3.112.1.10 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_05\_ACT |
| QW3.112.1.11 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_06\_ACT |
| QW3.112.1.12 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_07\_ACT |
| QW3.112.1.13 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_08\_ACT |
| QW3.112.1.14 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_09\_ACT |
| QW3.112.1.15 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Write\_BF\_C\_10\_ACT |
| QW3.112.1.16 |  |  |  |  |
| QW3.112.1.17 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.WDS\_DCS |
| QW3.112.1.18 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.LOC\_EN |
| QW3.112.1.19 |  |  |  |  |
| QW3.112.1.20 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW40\_WR |
| QW3.112.1.21 |  |  |  |  |
| QW3.112.1.22 |  |  |  |  |
| QW3.112.1.23 |  |  |  |  |
| QW3.112.1.24 |  |  |  |  |
| QW3.112.1.25 |  |  |  |  |
| QW3.112.1.26 |  |  |  |  |
| QW3.112.1.27 |  |  |  |  |
| QW3.112.1.28 |  |  |  |  |
| QW3.112.1.29 |  |  |  |  |
| QW3.112.1.30 |  |  |  |  |
| QW3.112.1.31 |  |  |  |  |

Table 826. Controller\_3.3.112.1 32word AO/constcy complete (Status)

##### 2 64word AI/constcy complete

| **Channel** | **Name** | **Type** | **Variable** | **I/O description** |
| --- | --- | --- | --- | --- |
| IW3.112.2.0 | DW000 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.LT\_01 |  |
| IW3.112.2.1 | DW002 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.TT\_01 |  |
| IW3.112.2.2 | DW004 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.PT\_01 |  |
| IW3.112.2.3 | DW006 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.PT\_02 |  |
| IW3.112.2.4 | DW008 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.TT\_02 |  |
| IW3.112.2.5 | DW010 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.IT\_M1 |  |
| IW3.112.2.6 | DW012 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.TT\_101 |  |
| IW3.112.2.7 | DW014 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.TT\_302 |  |
| IW3.112.2.8 | DW016 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.TT\_301 |  |
| IW3.112.2.9 | DW018 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.PT\_301 |  |
| IW3.112.2.10 | DW020 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.TT\_03 |  |
| IW3.112.2.11 | DW022 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.PT\_03 |  |
| IW3.112.2.12 | DW024 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.ST\_M1 |  |
| IW3.112.2.13 | DW026 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SC\_M1 |  |
| IW3.112.2.14 | DW028 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.TY\_101 |  |
| IW3.112.2.15 | DW030 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.PY\_01 |  |
| IW3.112.2.16 | DW032 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Read\_ST\_01\_SET |  |
| IW3.112.2.17 | DW034 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Read\_LT\_01\_SET |  |
| IW3.112.2.18 | DW036 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Read\_IT\_M1\_SET |  |
| IW3.112.2.19 | DW038 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Read\_TT\_101\_SET |  |
| IW3.112.2.20 | DW040 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Read\_DPT\_01\_SET |  |
| IW3.112.2.21 | DW042 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Read\_PT\_01\_SET |  |
| IW3.112.2.22 | DW044 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_01\_ACT |  |
| IW3.112.2.23 | DW046 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_02\_ACT |  |
| IW3.112.2.24 | DW048 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_03\_ACT |  |
| IW3.112.2.25 | DW050 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_04\_ACT |  |
| IW3.112.2.26 | DW052 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_05\_ACT |  |
| IW3.112.2.27 | DW054 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_06\_ACT |  |
| IW3.112.2.28 | DW056 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_07\_ACT |  |
| IW3.112.2.29 | DW058 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_08\_ACT |  |
| IW3.112.2.30 | DW060 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_09\_ACT |  |
| IW3.112.2.31 | DW062 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_10\_ACT |  |
| IW3.112.2.32 | DW064 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.ACTUAL\_CONTROL\_MODE |  |
| IW3.112.2.33 | DW066 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.ACTUAL\_ALARM\_LEVEL |  |
| IW3.112.2.34 | DW068 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.WDS\_PLC |  |
| IW3.112.2.35 | DW070 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW070 |  |
| IW3.112.2.36 | DW072 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW072 |  |
| IW3.112.2.37 | DW074 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW074 |  |
| IW3.112.2.38 | DW076 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW076 |  |
| IW3.112.2.39 | DW078 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW078 |  |
| IW3.112.2.40 | DW080 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW080 |  |
| IW3.112.2.41 | DW082 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW082 |  |
| IW3.112.2.42 | DW084 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW084 |  |
| IW3.112.2.43 | DW086 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW086 |  |
| IW3.112.2.44 | DW088 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW088 |  |
| IW3.112.2.45 | DW090 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW090 |  |
| IW3.112.2.46 | DW092 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW092 |  |
| IW3.112.2.47 | DW094 | DintIO |  |  |
| IW3.112.2.48 | DW096 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW096 |  |
| IW3.112.2.49 | DW098 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW098 |  |
| IW3.112.2.50 | DW100 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW100 |  |
| IW3.112.2.51 | DW102 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW102 |  |
| IW3.112.2.52 | DW104 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW104 |  |
| IW3.112.2.53 | DW106 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW106 |  |
| IW3.112.2.54 | DW108 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW108 |  |
| IW3.112.2.55 | DW110 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW110 |  |
| IW3.112.2.56 | DW112 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW112 |  |
| IW3.112.2.57 | DW114 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW114 |  |
| IW3.112.2.58 | DW116 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW116 |  |
| IW3.112.2.59 | DW118 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW118 |  |
| IW3.112.2.60 | DW120 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW120 |  |
| IW3.112.2.61 | DW122 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.OP\_ID\_FROM\_BARTEC\_TERMINAL\_2 |  |
| IW3.112.2.62 | DW124 | DintIO | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.OP\_ID\_FROM\_BARTEC\_TERMINAL\_1 |  |
| IW3.112.2.63 | DW126 | DintIO |  |  |
| IW3.112.2.64 | UnitStatus | HwStatus |  |  |

Table 827. Controller\_3.3.112.2 64word AI/constcy complete (Connections)

| **Channel** | **Channel Value** | **Forced** | **Variable value** | **Variable** |
| --- | --- | --- | --- | --- |
| IW3.112.2.0 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.LT\_01 |
| IW3.112.2.1 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.TT\_01 |
| IW3.112.2.2 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.PT\_01 |
| IW3.112.2.3 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.PT\_02 |
| IW3.112.2.4 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.TT\_02 |
| IW3.112.2.5 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.IT\_M1 |
| IW3.112.2.6 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.TT\_101 |
| IW3.112.2.7 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.TT\_302 |
| IW3.112.2.8 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.TT\_301 |
| IW3.112.2.9 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.PT\_301 |
| IW3.112.2.10 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.TT\_03 |
| IW3.112.2.11 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.PT\_03 |
| IW3.112.2.12 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.ST\_M1 |
| IW3.112.2.13 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SC\_M1 |
| IW3.112.2.14 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.TY\_101 |
| IW3.112.2.15 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.PY\_01 |
| IW3.112.2.16 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Read\_ST\_01\_SET |
| IW3.112.2.17 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Read\_LT\_01\_SET |
| IW3.112.2.18 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Read\_IT\_M1\_SET |
| IW3.112.2.19 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Read\_TT\_101\_SET |
| IW3.112.2.20 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Read\_DPT\_01\_SET |
| IW3.112.2.21 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.SP\_Read\_PT\_01\_SET |
| IW3.112.2.22 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_01\_ACT |
| IW3.112.2.23 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_02\_ACT |
| IW3.112.2.24 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_03\_ACT |
| IW3.112.2.25 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_04\_ACT |
| IW3.112.2.26 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_05\_ACT |
| IW3.112.2.27 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_06\_ACT |
| IW3.112.2.28 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_07\_ACT |
| IW3.112.2.29 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_08\_ACT |
| IW3.112.2.30 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_09\_ACT |
| IW3.112.2.31 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.BFC\_Read\_BF\_C\_10\_ACT |
| IW3.112.2.32 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.ACTUAL\_CONTROL\_MODE |
| IW3.112.2.33 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.ACTUAL\_ALARM\_LEVEL |
| IW3.112.2.34 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.WDS\_PLC |
| IW3.112.2.35 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW070 |
| IW3.112.2.36 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW072 |
| IW3.112.2.37 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW074 |
| IW3.112.2.38 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW076 |
| IW3.112.2.39 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW078 |
| IW3.112.2.40 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW080 |
| IW3.112.2.41 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW082 |
| IW3.112.2.42 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW084 |
| IW3.112.2.43 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW086 |
| IW3.112.2.44 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW088 |
| IW3.112.2.45 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW090 |
| IW3.112.2.46 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW092 |
| IW3.112.2.47 |  |  |  |  |
| IW3.112.2.48 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW096 |
| IW3.112.2.49 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW098 |
| IW3.112.2.50 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW100 |
| IW3.112.2.51 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW102 |
| IW3.112.2.52 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW104 |
| IW3.112.2.53 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW106 |
| IW3.112.2.54 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW108 |
| IW3.112.2.55 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW110 |
| IW3.112.2.56 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW112 |
| IW3.112.2.57 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW114 |
| IW3.112.2.58 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW116 |
| IW3.112.2.59 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW118 |
| IW3.112.2.60 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.Temp\_DW120 |
| IW3.112.2.61 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.OP\_ID\_FROM\_BARTEC\_TERMINAL\_2 |
| IW3.112.2.62 |  |  |  | Ciklo\_Eno\_Acetal\_3.U\_14A24\_00.COMBER\_PLC.PLC\_Data.OP\_ID\_FROM\_BARTEC\_TERMINAL\_1 |
| IW3.112.2.63 |  |  |  |  |

Table 828. Controller\_3.3.112.2 64word AI/constcy complete (Status)

## Tasks

|  | **Task name** | **Priority** | **Interval Time** | **Offset** | **Accepted latency** |
| --- | --- | --- | --- | --- | --- |
| 1 | Fast | 2 - High | 50 | 0 | N/A |
| 2 | Ellato\_3MMS | 2 - High | 200 | 0 | N/A |
| 3 | Ellato\_3 | 2 - High | 1000 | 0 | N/A |
| 4 | Ciklo\_Ad\_Eno\_3 | 2 - High | 1000 | 500 | N/A |
| 5 | Ciklo\_Eno\_Acetal\_3 | 2 - High | 1000 | 0 | N/A |
| 6 | Nyers\_Drospiren\_3 | 2 - High | 1000 | 0 | N/A |
| 7 | Normal | 3 - Normal | 250 | 60 | N/A |
| 8 | Slow | 4 - Low | 1000 | 0 | N/A |

Table 829. Task Overview - Controller\_3 (Tasks)

## Access Variables

| **Name** | **Path** | **Data Type** | **Attribute** |
| --- | --- | --- | --- |
| MMS\_KSVG04672\_Cmd | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1D.GCS\_1\_IO.Cmd.Value | bool |  |
| MMS\_KSVG04672\_FB0 | Cikloprpil\_D6\_laktol.U\_14C46\_00.EqM02E.GCS\_1D.GCS\_1\_IO.FB0 | BoolIO |  |
| MMS\_PIT04211 | Ellato\_3.U\_14C42\_Tart.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_IO | RealIO |  |
| MMS\_PIT04211\_Status | Ellato\_3.U\_14C42\_Tart.EqM01Aax3.CNT\_1.IND\_5.IND\_5\_Status | dint |  |
| MMS\_TIT04201 | Ellato\_3.U\_14C42\_Tart.IND\_1ax3.IND\_1\_IO | RealIO |  |
| MMS\_TIT04201\_Status | Ellato\_3.U\_14C42\_Tart.IND\_1ax3.IND\_1\_IO\_Status | dint |  |
| MMS\_KSVG04248\_Cmd | Ellato\_3.U\_14C42\_Tart.EqM02Bax3.GCS\_1A.GCS\_1\_IO.Cmd.Value | bool |  |
| MMS\_KSVG04248\_FB0 | Ellato\_3.U\_14C42\_Tart.EqM02Bax3.GCS\_1A.GCS\_1\_IO.FB0 | BoolIO |  |
| MMS\_FS09033 | Nyers\_Drospiren\_3.U\_19A90\_00.EqM15Iax3.FS\_IO | BoolIO |  |
| MMS\_TT09037 | Nyers\_Drospiren\_3.U\_19A90\_00.IND\_1ax3B.IND\_1\_IO | RealIO |  |

Table 830. Access Variables - Controller\_3 (MMS)