

Quartus Prime Lite Edition - D:/simple/electric - electric

File Edit View Project Assignments Processing Tools Window Help

electric

Project Navigator Hierarchy

Entity/Instance	ALMs needed (=A
Cyclone V: 5CSEMA5F31C6	
electric	9.5 (9.5)

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- Flow Log
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- Fitter
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- Assembler
- Power Analyzer
- Flow Messages
- Flow Suppressed Mess

Flow Summary

<<Filter>>

Flow Status	Successful - Thu Jul 06 14:34:28 2023
Quartus Prime Version	18.1.0 Build 625 09/12/2018 SJ Lite Edition
Revision Name	electric
Top-level Entity Name	electric
Family	Cyclone V
Device	5CSEMA5F31C6
Timing Models	Final
Logic utilization (in ALMs)	10 / 32,070 ( < 1 % )
Total registers	17
Total pins	83 / 457 ( 18 % )
Total virtual pins	0
Total block memory bits	0 / 4,065,280 ( 0 % )
Total DSP Blocks	0 / 87 ( 0 % )
Total HSSI RX PCSs	0
Total HSSI PMA RX Deserializers	0
Total HSSI TX PCSs	0
Total HSSI PMA TX Serializers	0
Total PLLs	0 / 6 ( 0 % )
Total DLLs	0 / 4 ( 0 % )

Tasks Compilation

Task

- Compile Design
  - Analysis & Synthesis
  - Fitter (Place & Route)
  - Assembler (Generate program
  - Timing Analysis
  - EDA Netlist Writer
  - Edit Settings
  - Program Device (Open Programm

IP Catalog

Installed IP

- Project Directory
  - No Selection Available
- Library
  - Basic Functions
  - DSP
  - Interface Protocols
  - Memory Interfaces and Controllers
  - Processors and Peripherals
  - University Program
- Search for Partner IP

+ Add...

Messages

Type ID Message

- 215031 Total thermal power estimate for the design is 1194.16 mW
- Quartus Prime Power Analyzer was successful. 0 errors, 6 warnings

System Processing (21)

Chat

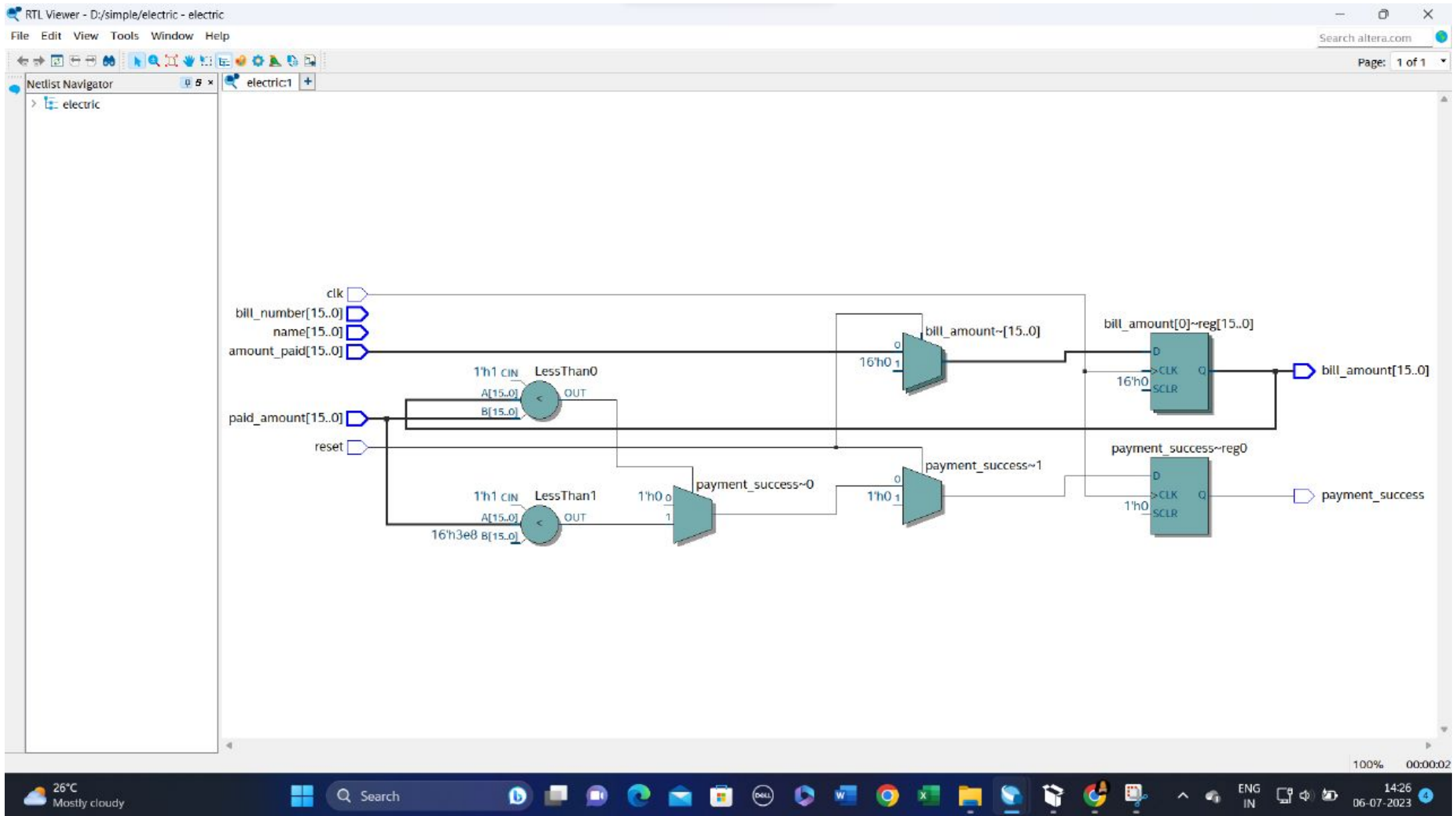
100% 00:00:06

26°C Mostly cloudy

Search

ENG IN

14:35 06-07-2023



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electric

electric.v

electric\_tb.v

Compilation Report - electric

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Entity/Instance ALMs needed [=A]

Cyclone V: 5CSEMA5F31C6

electric 9.5 (9.5)

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Incremental C

Incremental C

Pin-Out File

Resource Section

Resource Usa

Partition Stat

Input Pins

Output Pins

I/O Bank Usag

All Package P

I/O Standard

Resource Util

Filter Resource Usage Summary

<<Filter>>

	Resource	Usage	%
1	Logic utilization (ALMs needed / total ALMs on device)	10 / 32,070	< 1 %
2	ALMs needed [=A-B+C]	10	
1	A ALMs used in final placement [=a+b+c+d]	12 / 32,070	< 1 %
1	a ALMs used for LUT logic and registers	5	
2	b ALMs used for LUT logic	3	
3	c ALMs used for registers	4	
4	d ALMs used for memory (up to half of total ALMs)	0	
2	B Estimate of ALMs recoverable by dense packing	2 / 32,070	< 1 %
3	C Estimate of ALMs unavailable [=a+b+c+d]	0 / 32,070	0 %
1	a Due to location constrained logic	0	
2	b Due to LAB-wide signal conflicts	0	
3	c Due to LAB input limits	0	
4	d Due to virtual I/Os	0	
3			
4	Difficulty packing design:	Low	
5			
6	Total LABs: partially or completely used	3 / 3,207	< 1 %
1	-- Logic LABs	3	
2	-- Memory LABs (up to half of total LABs)	0	
7			
8	Combinational ALUT usage for logic	15	
1	-- 7 input functions	0	
2	-- 6 input functions	3	
3	-- 5 input functions	6	

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DSP

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Add...

Tasks Compilation

Task

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Messages

Type ID Message

334004 Delay annotation completed successfully

11801 Fitter post-fit operations ending: elapsed time is 00:00:04

11802 Generated suppressed messages file: D:/simple/electric/output\_files/11802\_fit\_suppressed.txt

System Processing (59)

26°C Mostly cloudy

Search

100% 00:01:21

14:30 06-07-2023