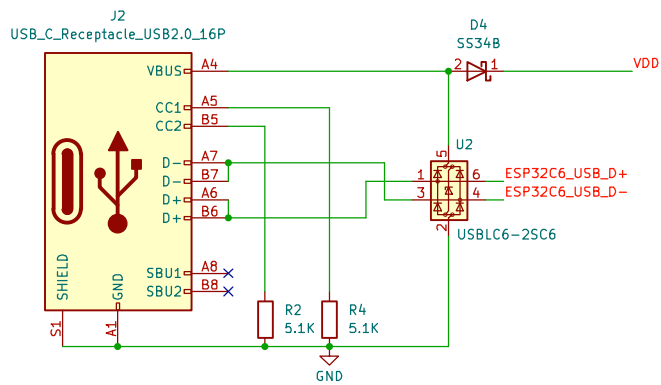
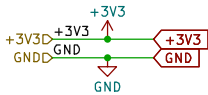
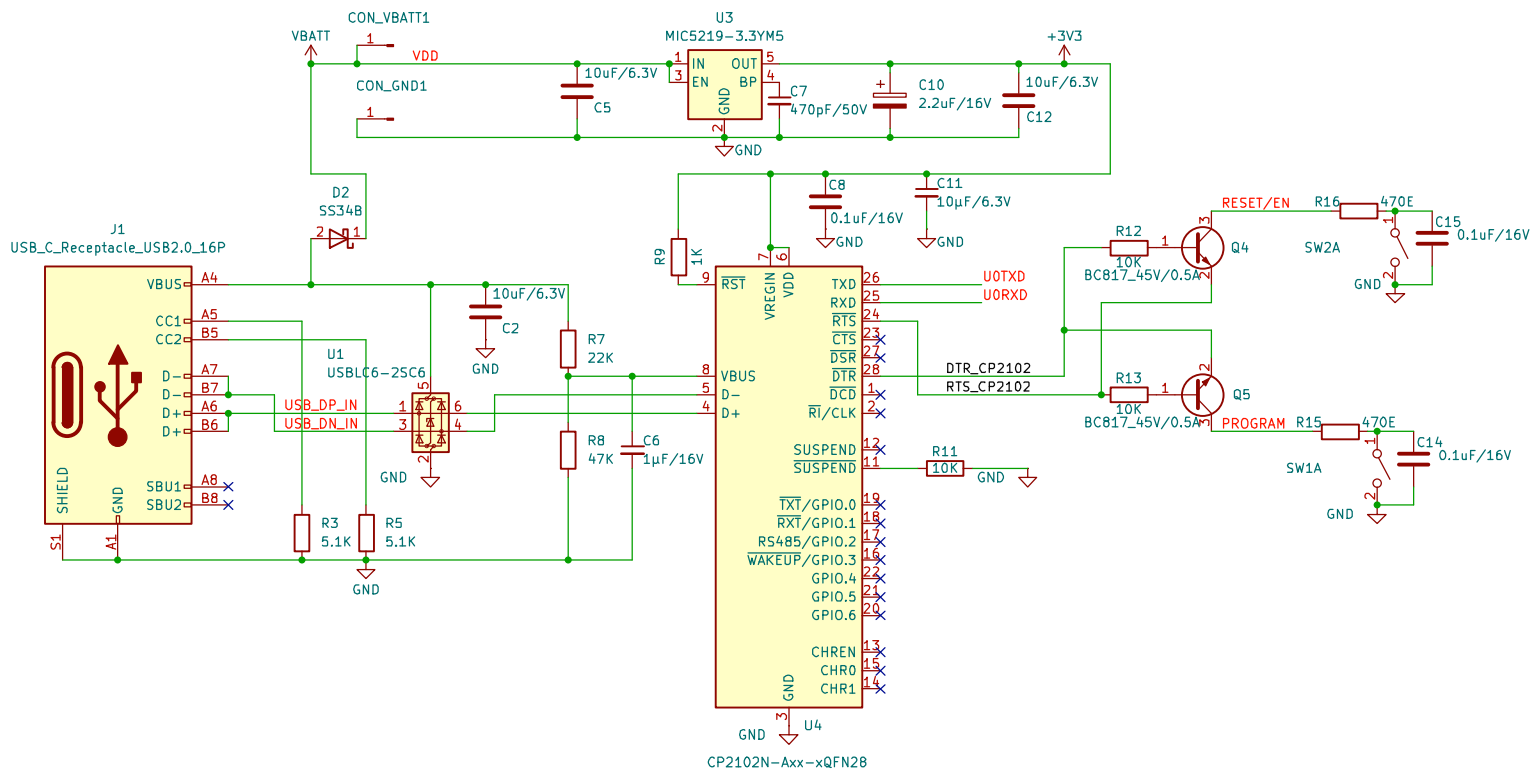


Min track/spacing = 4 mil
Min hole size = 0.2 mm



- Chip boot mode
 - Strapping pin: GPIO8 and GPIO9
- SDIO Sampling and Driving Clock Edge
 - Strapping pin: MTMS and MTDI
- ROM message printing
 - Strapping pin: GPIO8
 - eFuse parameter: EFUSE_UART_PRINT_CONTROL and EFUSE_DIS_USB_SERIAL_JTAG_ROM_PRINT
- JTAG signal source
 - Strapping pin: GPIO15
 - eFuse parameter: EFUSE_DIS_PAD_JTAG, EFUSE_DIS_USB_JTAG, and EFUSE_JTAG_SELEnable

- Strapping Pin
 - GPIO4, MTMS, LP_GPIO4, LP_UART_RXD, ADC1_CH4, FSPIDH
 - GPIO5, MTDI, LP_GPIO5, LP_UART_TXD, ADC1_CH5, FSPIMP
 - GPIO8 ROM message printing
 - GPIO9 Chip boot mode
 - GPIO15 JTAG signal source

Table 3: Pin Definitions

GPIO0, XTAL32K_P, LP_GPIO0, LP_UART_DTRN, ADC1_CH0
GPIO1, XTAL32K_N, LP_GPIO1, LP_UART_DSRN, ADC1_CH1
GPIO2, LP_GPIO2, LP_UART_RTSN, ADC1_CH2, FSPIQ
GPIO3, LP_GPIO3, LP_UART_CTSN, ADC1_CH3
GPIO4, MTMS, LP_GPIO4, LP_UART_RXD, ADC1_CH4, FSPIDH
GPIO5, MTDI, LP_GPIO5, LP_UART_TXD, ADC1_CH5, FSPIMP
GPIO6, MTCK, LP_GPIO6, LP_I2C_SDA, ADC1_CH6, FSPICLK
GPIO7, MTDO, LP_GPIO7, LP_I2C_SCL, FSPID
GPIO8
GPIO9
GPIO10
GPIO11
GPIO12, USB_D-
GPIO13, USB_D+

GPIO15
GPIO16, U0TXD, FSPICS0
GPIO17, U0RXD, FSPICS1
GPIO18, SDIO_CMD, FSPICS2
GPIO19, SDIO_CLK, FSPICS3
GPIO20, SDIO_DATA0, FSPICS4
GPIO21, SDIO_DATA1, FSPICS5
GPIO22, SDIO_DATA2
GPIO23, SDIO_DATA3

