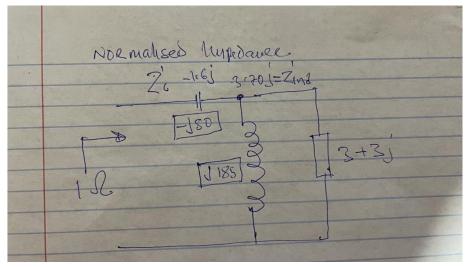
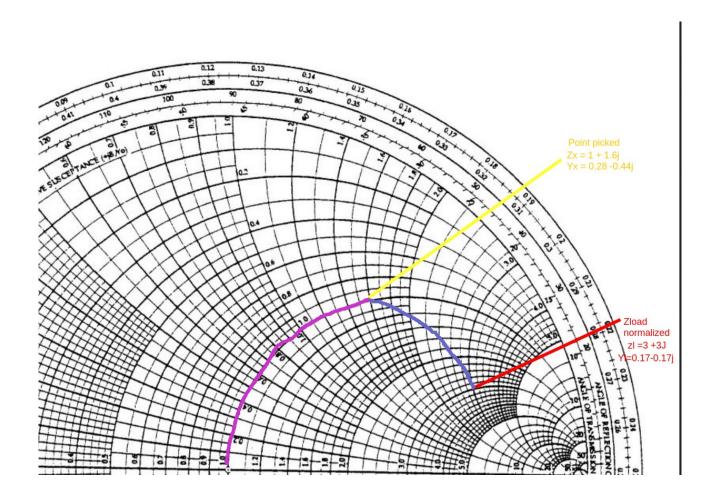
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Normalizing Load Impedance Zl $Zl = (150 \ +150j)/50 = 3 \ +3j\Omega$

Normalizing Input impedance

 $Zin = 50/50 = 1 \Omega$





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smith chart sketch

= 0.28 – j0.44.....ii

Admittance for the Y_i inductance is obtained by getting the difference btn Y_i and Y_i for equation I and I is above

Impedance of the capacitor in the Marched network. From eqn k and 1 ohms

the difference is -j1.6 which is equivalent capacitor impedance Zc

After de-normalizing

$$Z_{c} = -1.6 *50 = -j80\Omega$$

Zind = -3.7 *50 = -j185 Ω

Converting inductor into stub line equivalent

reading form the chart for j3.7 is equivalent to 0.21λ

