## **SEMESTER BREAK TRAINING**

## ELECTRONIC CIRCUIT DESIGN AND TESTING

## THE OBJECTIVE OF TRAINING IS TO DEVELOP SKILLS NEEDED IN AN ELECTRONICS ENGINEER.

SISTec Department of Electronics and Communication Engineering conducted training on "Electronic Circuit Design and Testing" for first year students of ECE from 1

Jan to 3 Jan 2020.

On day two Prof. Sharad Shrivastava, started session with introduction to basic electronic components, their types, color coding, their series and parallel connections, in the other half of the day, Prof. Siddharth Singh along with Prof. Kavita Kamerikar took hand-on session on analog and digital circuits.

On day three Prof. Rahul
Shrivastava exposed students to
embedded system, its components
and characteristics. next students
performed many hand-on
exercises on an #Arduino board,
later the students learned how to
read sensor and display the
information to a #smart\_phone
using #bluetooth. At the end of the
day, Prof. Shrivastava
demonstrated data uploading to
#ThingSpeak cloud platform.









SISTec Department of Electronics and Communication Engineering

## **INDUSTRIAL VISIT**

MSME(Micro, Small and Medium Enterprises), Bhopal.

SISTec Department of Electronics and Communication Engineering organised industrial visit for their second year and third year students to MSME(Micro, Small and Medium Enterprises), Bhopal. The visit started with the session of Ms. Shashi Karautiya, she discussed about the importance of embedded systems, PLC and SCADA in an industrial environment.

In the second session hand-on session was conducted by Mr.
Ankur on ARM processors. Also session were conducted on PLC and SCADA over TIA portal.
The objective of visit is to develop understanding on opportunities provided by MSME, and use of PLC and SCADA in an industrial environment.









#### FLDec Systems



Congratulations to

- 1. Ms. Rama Malviya
- 2. Mr. Ashish Pandey

for getting placed in FLDec Systems at the profile of VLSI Engineer.

Best wishes to all your future endeavors.

Sagar Group of Institutions-SISTec: An Ultimate Gateway to Placement and The Best Place for Learning, where the students are provided with Customized Training based on their skill-set in order to get that extra edge over their competitors.

**#VLSI #Engineer #Placed** 

#### TCS (NINJA DEVELOPER)



Congratulations to all for getting placed in TCS as NINJA DEVELOPER at 3.5 LPA.

#BestWishes for all your future endeavors.
Sagar Group of Institutions-SISTec: An Ultimate Gateway to
Placement and The Best Place for Learning, where the students are
provided with Customized Training based on their skill-set in order to
get that extra edge over their competitors.

# LEAP LEARNER (CODING TRAINEE)



SISTecECE congratulates Shweta Malviya, for getting placed as Coding Trainee (Research - Robotics) in Leap Learner.
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## PLASTECH SOLUTIONS (GRADUATE ENGINEER TRAINEE)



Congratulations to

1. Mr. Prateek Shekhar

2. Mr. Ashish Pandey

for getting placed in Plastech Solutions, Pune at the profile of Graduate Engineer Trainee.

#BestWishes to all your future endeavors.

Sagar Group of Institutions-SISTec: An Ultimate Gateway to Placement and The Best Place for Learning, where the students are provided with Customized Training based on their skill-set in order to get that extra edge over their competitors.

JAN-MAY 2020 3 Feb 2020

#### **EXPERT TALK**

## Cyber Security and Ethical Hacking

SISTec Department of **Electronics and Communication** Engineering organized an expert talk on "Cyber Security and Ethical Hacking" for second year and third year students of **#SISTecECE** and SISTec Department of Computer Science & Engineering on 3 Feb 2020. The talk was delivered by Mr. Ajinkya Lohakare, CTO of Social-Spectra & Dreamssofts, He is very young energetic and enthusiastic person, has certifications in Ethical Hacking, Certified Hacking Forensic Investigator, Google Certified Application Developer, Block Chain & Crypto currency Expert.



He discussed about various methods and tools available for ethical hacking and also the methods, tools and practices to be followed over internet for safe transactions and data security. He also motivated students to understand the underlying processes and architecture of various internet products.

#Internet #CyberSecurity
#EthicalHacking #Social\_Spectra



JAN-MAY 2020 9 Feb 2020

## **INDUSTRIAL VISIT**

Telecommunication Department, Western Central Railways, Bhopal (M.P)

Industrial Visits offer a great source to gain practical knowledge. Students can observe and learn as to how theoretical concepts are put into action, thereby adding their practical learning. Students are exposed to real working environment and are shown how things are done in an organisation. With this view in mind #SISTecECE organized industrial visit to Signal and Telecommunication Department, Western Central Railways, Bhopal (M.P)







Mr. Manish Choudhary, Senior Section Engineer, S&T Bhopal Division along with his team provided a visit to the Railway Telephone Exchange, OFC Station, Passenger Reservation System and the Integrated Security System at Bhopal Junction Railway station and explained the work flow in detail.

## BIG BASKET (QUALITY ANALYST)



A Gateway to Placements...







Profile: Quality Analyst Package: 3.2 LPA

















Best wishes for your bright future

B. Tech | M. Tech | B. Pharma | M. Pharma | MBA

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## **INDUSTRIAL VISIT**

Telecommunication Department, Western Central Railways, Bhopal (M.P)



SISTec Department of
Mechanical Engineering
organized 2 Days Nirmaan 2020
on 28th Jan 20 - 29th Jan 20.
This year Nirmaan 2020 has
brought an Amazing Electrical
Vehicle Startup Conclave,
sponsored by RGPV under
TEQIP-III, in association with
ARAI (Automotive Research
Association of India).



A team from SISTec Department of Electronics and Communication Engineering, second year, won a cash prize of Rs. 10000/- under best innovation category. The idea is #DriverLess #car, many sensors are deployed with a microcontroller unit in this car so that the car can reach the specified location safely. Team members are: Richa Soni, Ashutosh Dubey, Rahul Kumar, Anjali Mishra, Kareena Verma, Deepak k Shah, Vikas Mewada and Aman Mansoori. The team is guided by Dr. Paresh Rawat. Many facilities are to be added, #SISTecECE wishes them for their future endevours!!

## **Technical Talk VISIT**

## Control and Instrumentation for Thermal Power Plants



Sagar Group of Institutions-SISTec

SISTec Organizes Technical Talk On- Control and Instrumentation for Thermal Power Plants in Association with Rajiv Gandhi Proudyogiki Vishwavidyalaya (#RGPV) under the Third phase of Technical Education Quality Improvement Programme (referred to as #TEQIP-III).

This program mainly deals with#Industrial Automation and its types#
#Control system use in automation#
#Power generation process in TPP#
#Water and steam path in TPP#
#PLC & Programming of PLC#

# SPATEZ TECHNOLOGIES (EMBEDDED ENGINEER TRAINEE)

Congratulations to Mr. Ronak Chitlangya, Ms Aditi Khare, and Mr Ashish Pandey for getting placed in Spatez Technologies as Embedded Engineer Trainee.





