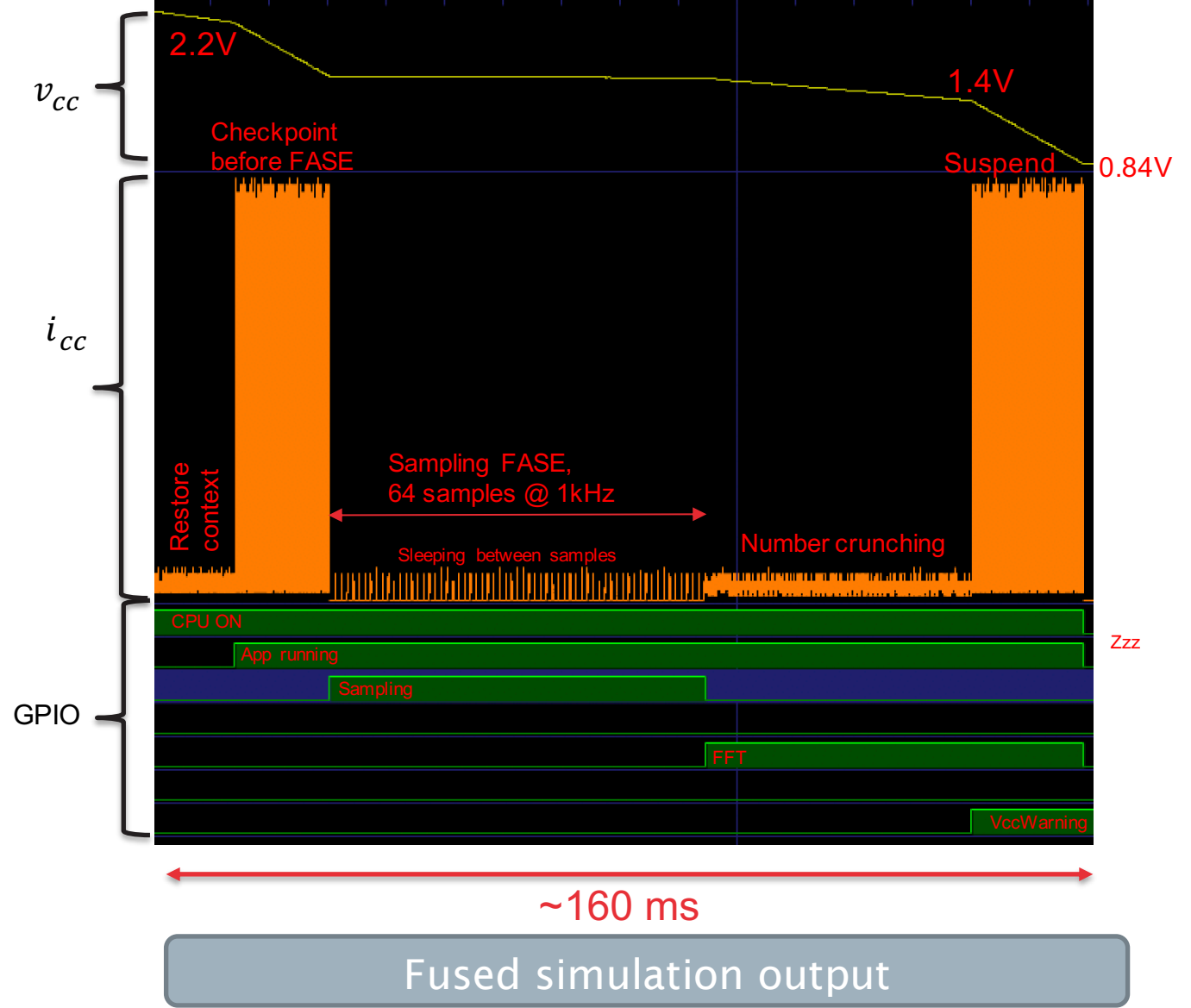


- Writing entire data & stack to NVM causes costly checkpoints:
 - ~20 ms
 - ~0.56V vcc drop
- Restore/booting entire data & stack takes significant time & energy

Experimental setup:

- Cortex M0
- 2 μ W power supply
- 2 μ F capacitor
- fclk = 1MHz
- 64 kB instruction MRAM
- 8 kB SRAM



*ps. absolute icc values are a bit meaningless (needs LP filtering), look at vcc drop instead