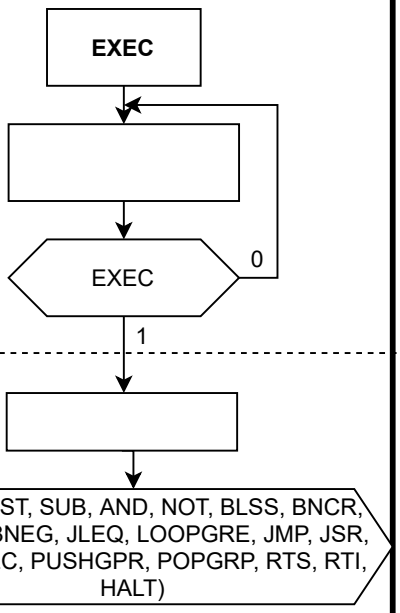
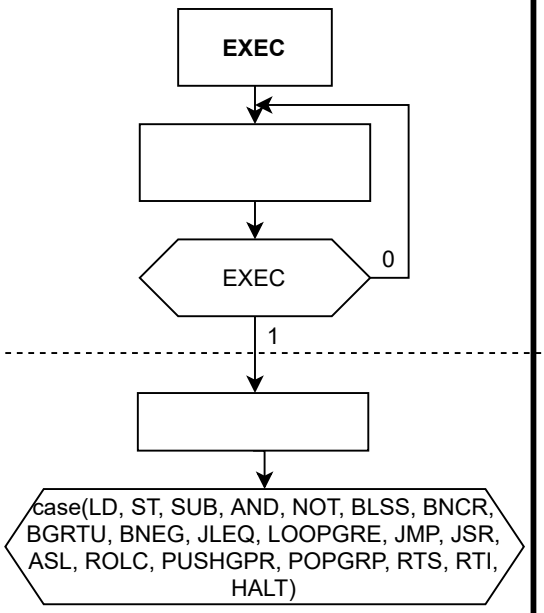
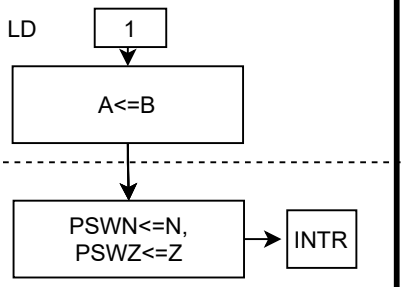
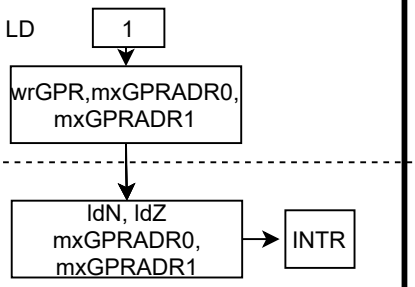
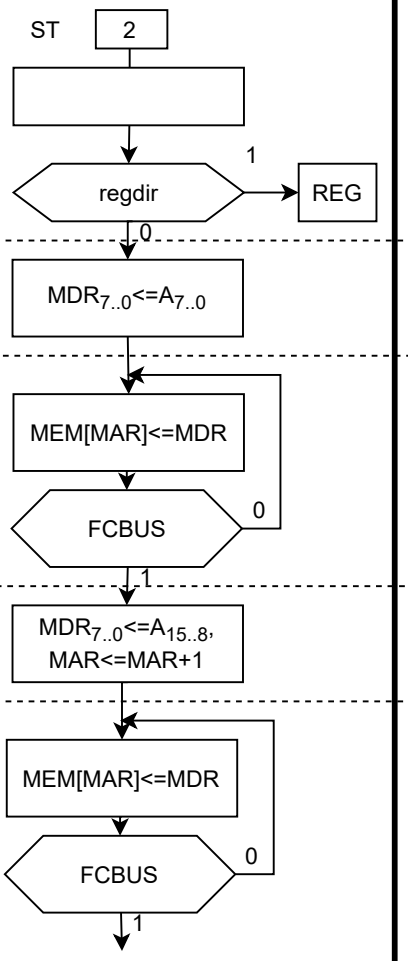
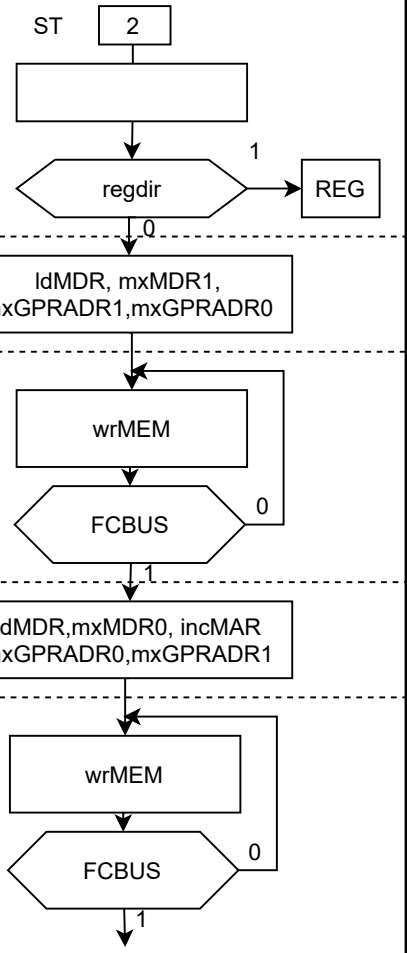
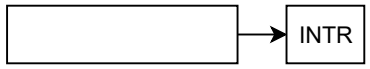
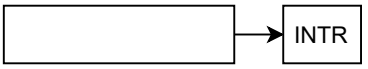
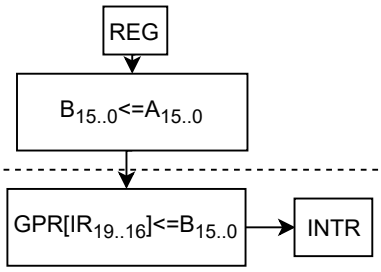
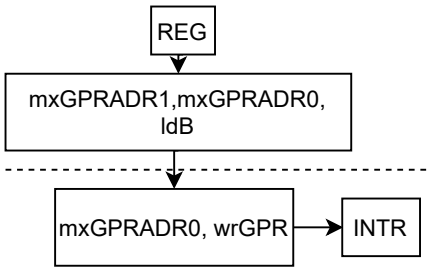
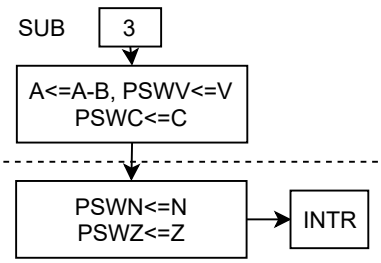
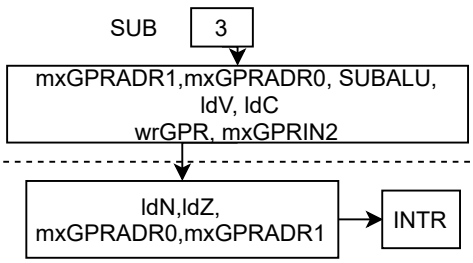
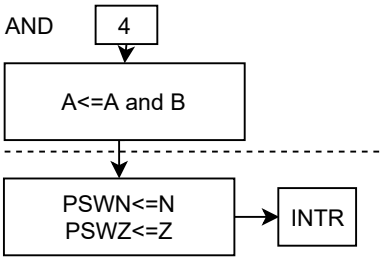
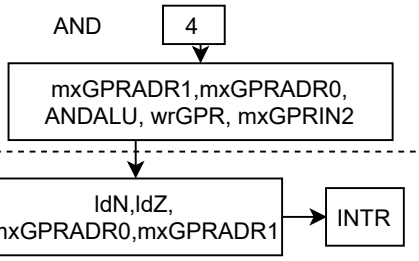
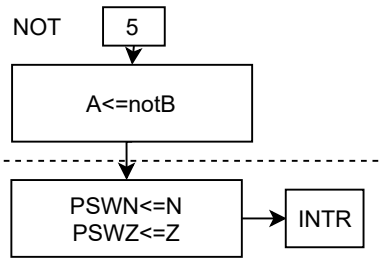
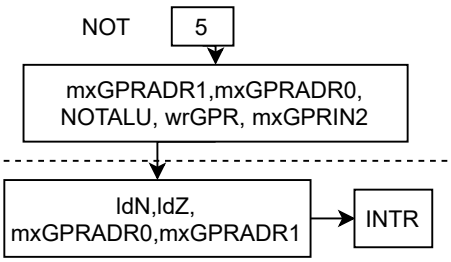
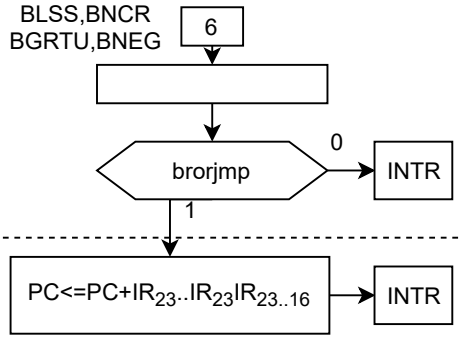
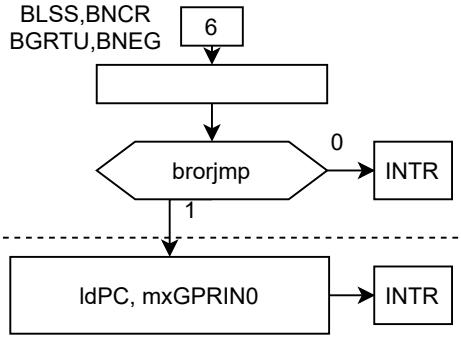
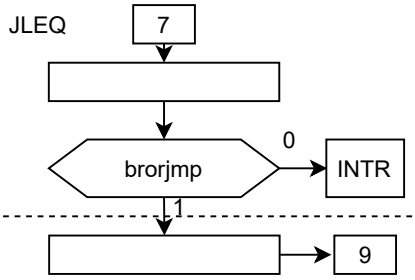
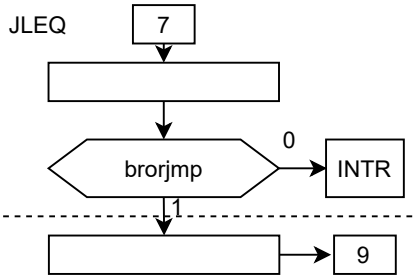
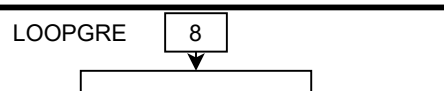
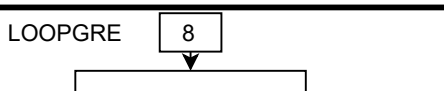
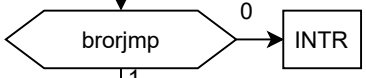
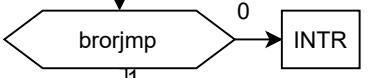
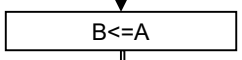
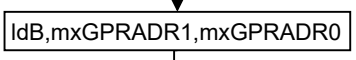
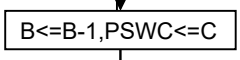
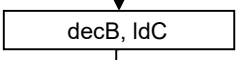
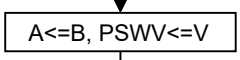
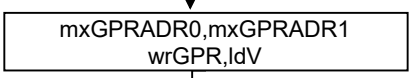
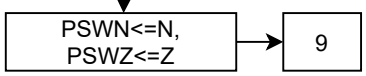
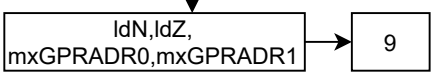
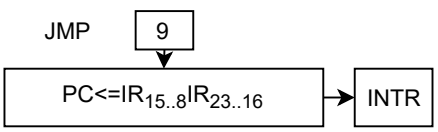
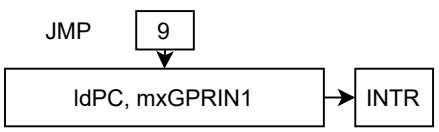
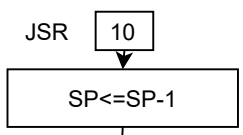
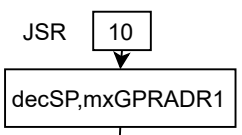
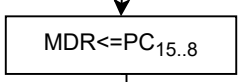
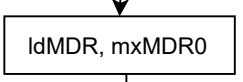
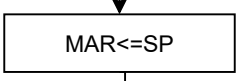
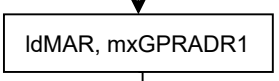
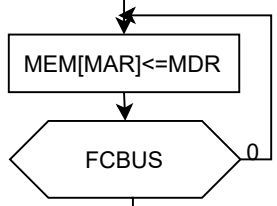
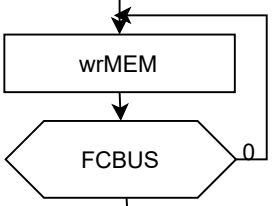
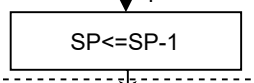
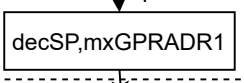
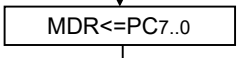
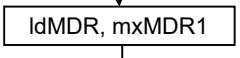
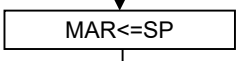
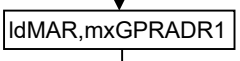
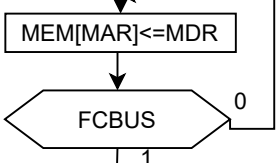
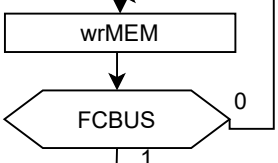
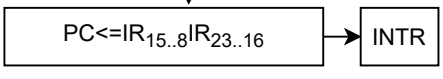
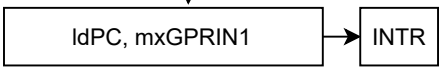
Дијаграм тока микрооперација	Дијаграм тока управљачких сигнала	Секвенца управљачких сигнала
		step00 => br(if notEXEC then step00) step01 => br(case(LD, ST, SUB, AND, NOT, BLSS, BNCR, BGRTU, BNEG, JLEQ, LOOPGRE, JMP, JSR,ASL, ROLC, PUSHGPR, POPGRP, RTS, RTI, HALT) then (LD,step02),(ST,step04),(SUB,step0C),(AND,step0E), (NOT,step10),(BLSS,BNCR,BGRTU,BNEG, step12), (JLEQ, step14),(LOOPGRE, step16),(JMP, step1B), (JSR, step1C),(ASL,ROLC,step25),(RTS,step29), (RTI, step2F),(PUSHGPR, step3C),(POPGPR,step47), (HALT,step50))
		step02 => wrGPR,mxGPRADR0, mxGPRADR1 step03 => ldN, ldZ mxGPRADR0, mxGPRADR1 br step51
		step04 => br(if regdir then step0A) step05 => ldMDR, mxMDR1, mxGPRADR1,mxGPRADR0 step06 => wrMEM, br(if notFCBUS then step06) step07 => ldMDR,mxMDR0, incMAR mxGPRADR0,mxGPRADR1 step08 => wrMEM, br(if notFCBUS then step08)

Име, презиме и број индекса	Иван Цветић 2019/0183 Славица Митровић 2019/0324	Потпис	**Назив:** Основи рачунарске технике 2 **Датум:** 27.02.2021. **Задатак 19:** Фаза извршавања операција, страна 1/7

Дијаграм тока микрооперација	Дијаграм тока управљачких сигнала	Секвенца управљачких сигнала
		step09 => br step51
		step0A => mxGPRADR1,mxGPRADR0, ldB step0B => mxGPRADR0, wrGPR, br step51
		step0C => mxGPRADR1,mxGPRADR0, SUBALU, ldV, ldC wrGPR, mxGPRIN2 step0D => ldN,ldZ, mxGPRADR0,mxGPRADR1, br step51
		step0E => mxGPRADR1,mxGPRADR0, ANDALU, wrGPR, mxGPRIN2 step0F => ldN,ldZ, mxGPRADR0,mxGPRADR1, br step51
		step10 => mxGPRADR1,mxGPRADR0, NOTALU, wrGPR, mxGPRIN2 step11 => ldN,ldZ, mxGPRADR0,mxGPRADR1, br step51
		step12 => br(if notbrorjmp then step51)
		step13 => ldPC, mxGPRIN0, br step51
		step14 => br(if notbrorjmp then step51) step15 => br step1B

Име, презиме и број индекса	Потпис	
Иван Цветић 2019/0183 Славица Митровић 2019/0324		**Назив:** Основи рачунарске технике 2 **Датум:** 27.02.2021. **Задатак 19:** Фаза извршавања операција, страна 2/7

Дијаграм тока микрооперација	Дијаграм тока управљачких сигнала	Секвенца управљачких сигнала
		step16 => br(if notbrorjmp then step51)
		
		step17 => ldB,mxGPRADR1,mxGPRADR0
		step18 => decB, ldC
		step19 => mxGPRADR0,mxGPRADR1 wrGPR,ldV
		step1A => ldN,ldZ, mxGPRADR0,mxGPRADR1 (br step1B)
		step1B => ldPC, mxGPRIN1, br step51
		step1C => decSP,mxGPRADR1
		step1D => ldMDR, mxMDR0
		step1E => ldMAR, mxGPRADR1
		step1F => wrMEM, br (if notFCBUS then step1F)
		step20 => decSP,mxGPRADR1
		step21 => ldMDR, mxMDR1
		step22 => ldMAR,mxGPRADR1
		step23 => wrMEM, br (if notFCBUS then step23)
		step24 => ldPC, mxGPRIN1, br step51

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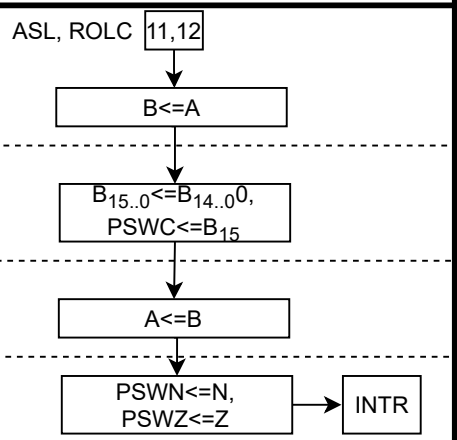
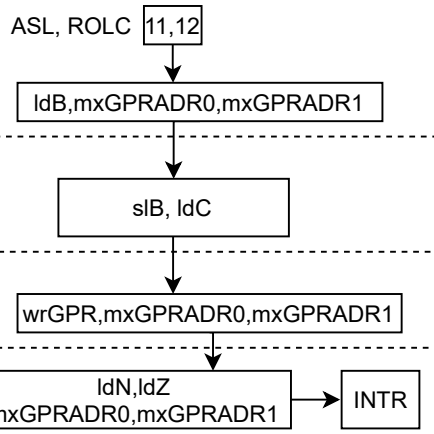
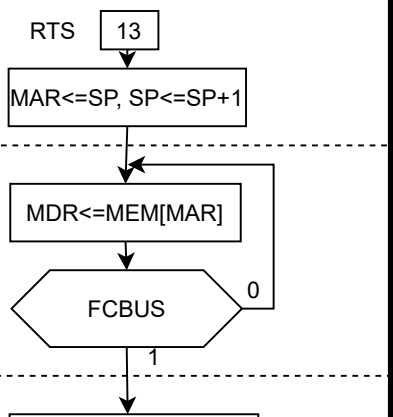
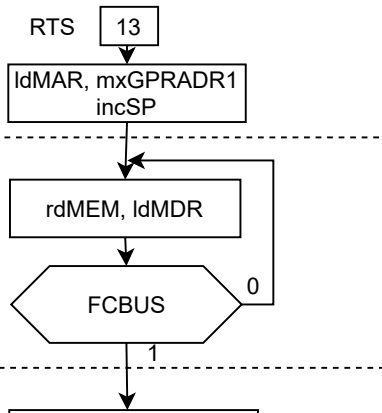
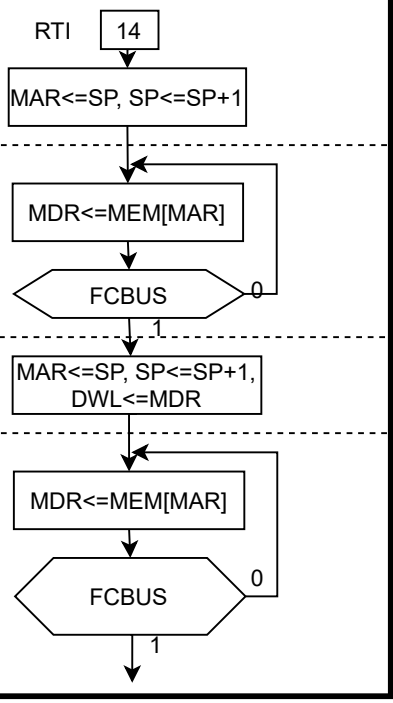
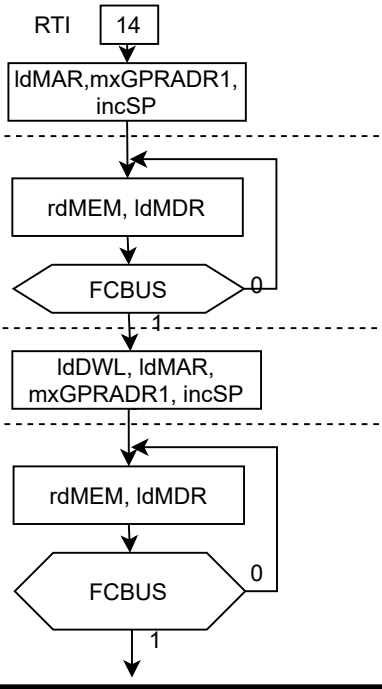
Дијаграм тока микрооперација	Дијаграм тока управљачких сигнала	Секвенца управљачких сигнала
		step25 => ldB,mxGPRADR0,mxGPRADR1 step26 => slB, ldC step27 => wrGPR,mxGPRADR0, mxGPRADR1 step28 => ldN,ldZ mxGPRADR0,mxGPRADR1, br step51
		step29 => ldMAR, mxGPRADR1 incSP step2A => rdMEM, ldMDR, br(if notFCBUS then step2A) step2B => ldDWL, ldMAR, mxGPRADR1, incSP step2C => rdMEM, ldMDR, br(if notFCBUS then step2C) step2D => ldDWH step2E => ldPC, mxGPRIN0, mxGPRIN1 br step51
		step2F => ldMAR,mxGPRADR1, incSP step30 => rdMEM, ldMDR, br(if notFCBUS then step30) step31 => ldDWL, ldMAR, mxGPRADR1, incSP step32 => rdMEM, ldMDR, br(if notFCBUS then step32)

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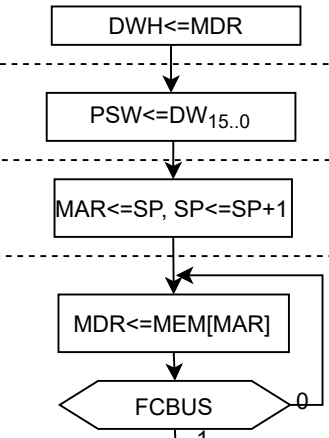
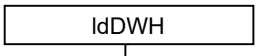
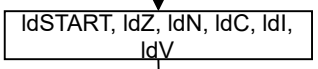
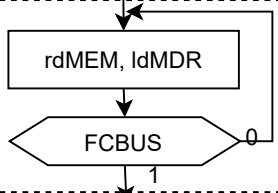
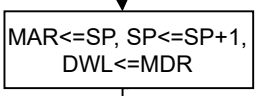
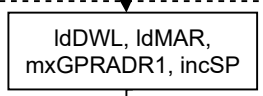
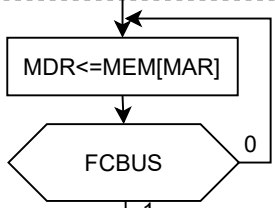
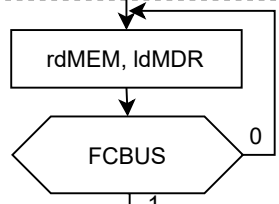
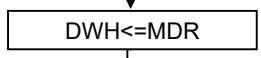
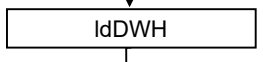
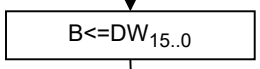
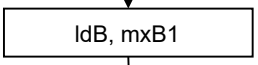
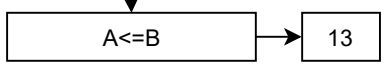
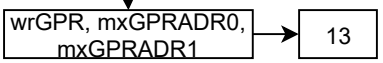
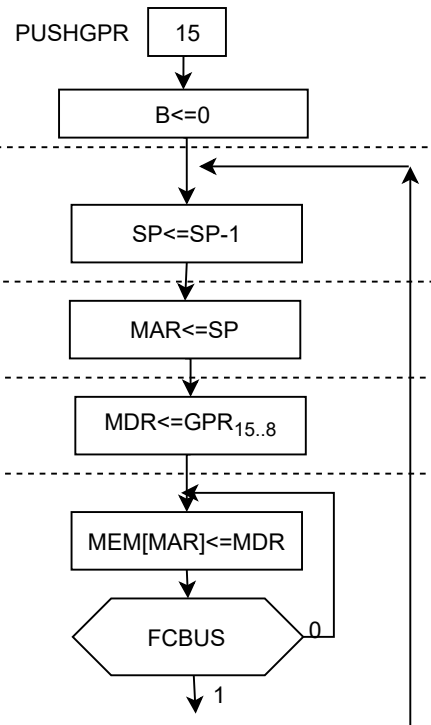
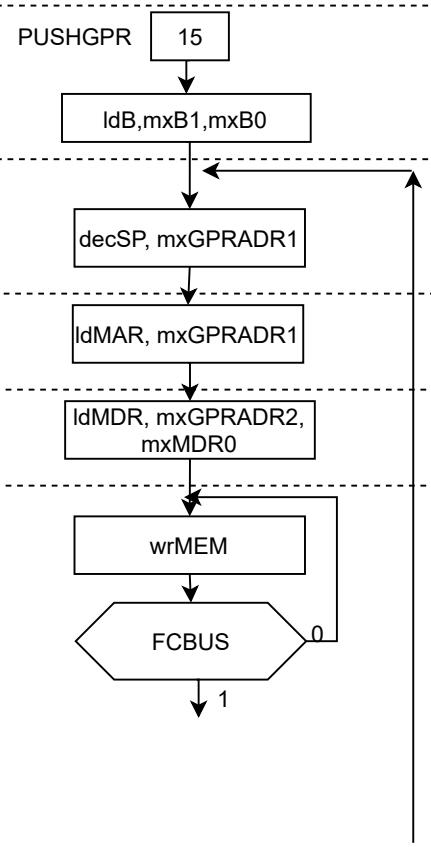
Дијаграм тока микрооперација	Дијаграм тока управљачких сигнала	Секвенца управљачких сигнала
		step33 => ldDWH
		step34 => ldSTART, ldZ, ldN, ldC, ldI, ldV
		step35 => ldMAR, mxGPRADR1, incSP
		step36 => rdMEM, ldMDR, br (if notFCBUS then step36)
		step37 => ldDWL, ldMAR, mxGPRADR1, incSP
		step38 => rdMEM, ldMDR, br (if notFCBUS then step38)
		step39 => ldDWH
		step3A => ldB, mxB1
		step3B => wrGPR, mxGPRADR0, mxGPRADR1, br step29
		step3C => ldB,mxB1,mxB0 step3D => decSP, mxGPRADR1 step3E => ldMAR, mxGPRADR1 step3F => ldMDR, mxGPRADR2, mxMDR0 step40 => wrMEM, br (if notFCBUS then step40)

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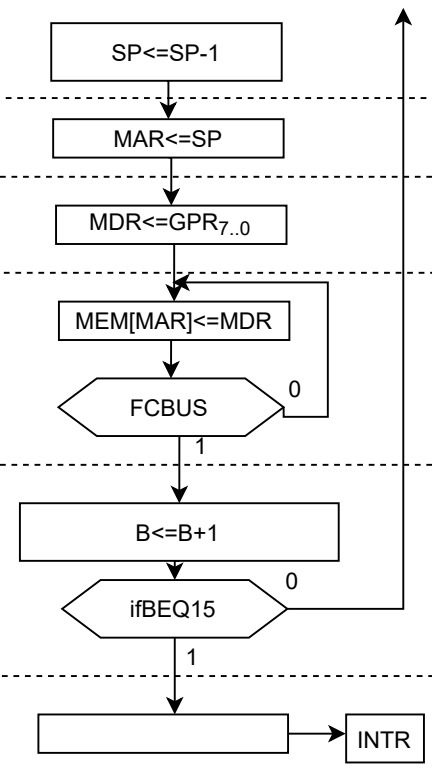
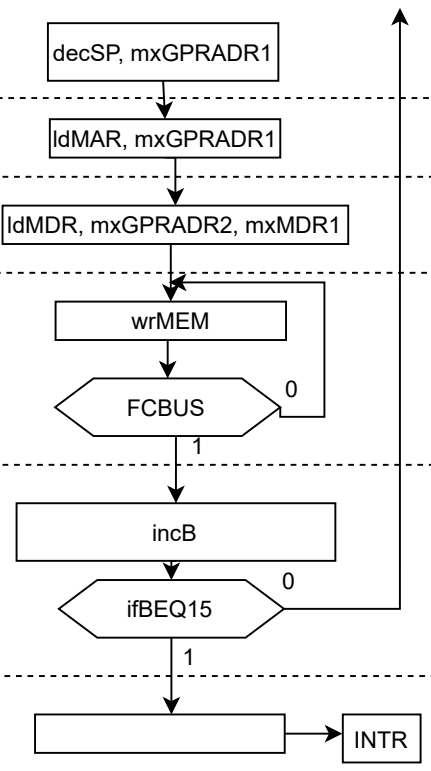
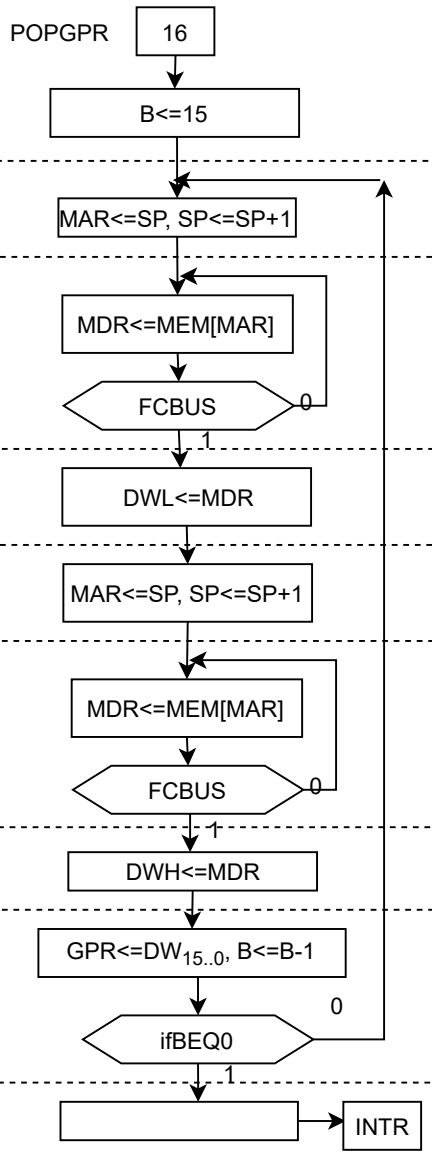
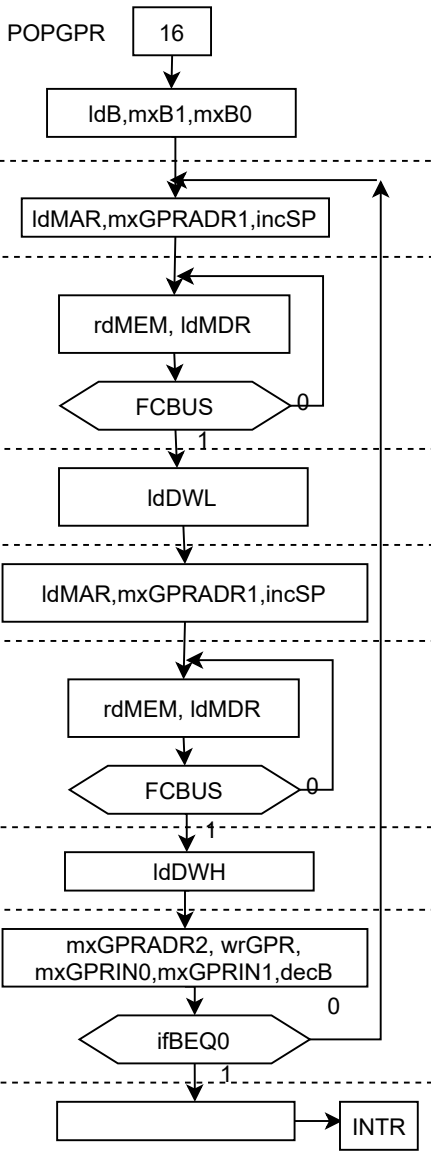
Дијаграм тока микрооперација	Дијаграм тока управљачких сигнала	Секвенца управљачких сигнала
		step41 => decSP, mxGPRADR1 step42 => ldMAR, mxGPRADR1 step43 => ldMDR, mxGPRADR2, mxMDR1 step44 => wrMEM, br(if notFCBUS then step44) step45 => incB br(if notifBEQ15 then step3D) step46 => br step51
		step47 => ldB,mxB1,mxB0 step48 => ldMAR,mxGPRADR1,incSP step49 => rdMEM, ldMDR, br(if notFCBUS then step49) step4A => ldDWL step4B => ldMAR,mxGPRADR1,incSP step4C => rdMEM, ldMDR, br(if notFCBUS then step4C) step4D => ldDWH step4E => mxGPRADR2, wrGPR, mxGPRIN0,mxGPRIN1,decB, br(if notifBEQ0 then step48) step4F => br step4A

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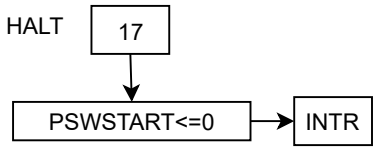
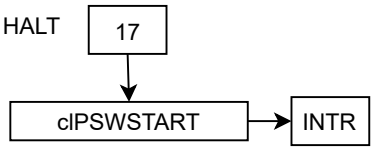
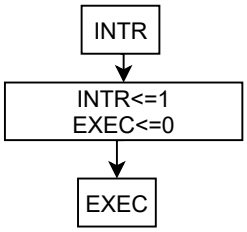
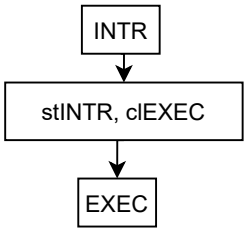
Дијаграм тока микрооперација	Дијаграм тока управљачких сигнала	Секвенца управљачких сигнала
		step50 => clPSWSTART, (br step51)
		step51 => stINTR, clEXEC, br step00

Име, презиме и број индекса	Иван Цветић 2019/0183 Славица Митровић 2019/0324	Потпис	**Назив:** Основи рачунарске технике 2 **Датум:** 27.02.2021. **Задатак 19:** Фаза извршавања операција, страна 7/7