

Schematic: Seeed SoM - STM32MP157C

Revision History

SHEET	SHEET NAME
01	Title/Revision History
02	MPU_1
03	MPU_2
04	DDR3
05	eMMC
06	PMU
07	PINOUT/LED

DATE	REVISION	DESCRIPTION
2019/05/21	Seeed SoM - STM32MP157C_v1.0_SCH_190521	Initial Release
2019/09/04	Seeed SoM - STM32MP157C_v1.01_SCH_190904	
2019/12/13	Seeed SoM - STM32MP157C_v1.01_SCH_191213	Change R5,R6 from 1k to 2k





