

## Siyuan Chai

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CONTACT INFORMATION	<a href="mailto:siyuanc3@illinois.edu">siyuanc3@illinois.edu</a> <a href="https://schai.me">https://schai.me</a>
EDUCATION	<b>University of Illinois</b> , Urbana Champaign, IL Computer Science Ph.D. Start Aug. 2021 Advisor: <a href="#">Prof. Tianyin Xu</a> <b>Northwestern University</b> , Evanston, IL M.S. Computer Science, B.S. Electrical Engineering Graduated June 2021 GPA: 4.0/4.0 (Summa Cum Laude)
SKILLS	<b>Programming languages:</b> C/C++, Python, Java, Go, JavaScript, MATLAB, MySQL, Racket <b>System-level Development:</b> Unix/Linux, QEMU, LLVM, OpenMP, Multi-threading, GNU Make, GDB <b>Artificial Intelligence:</b> Image Processing, Computer Vision, CUDA, Docker, PyTorch, Tensorflow, Keras
RESEARCH EXPERIENCE	<b>UIUC Xlab</b> , <a href="#">Prof. Tianyin Xu</a> Aug. 2021 to Present <i>Support Linux Kernel for Elastic Cuckoo Page Table</i> <ul style="list-style-type: none"><li>Adapting Linux kernel to support <a href="#">Elastic Cuckoo Page Table</a> (ECPT), a hash page table that aims to replace paging by enabling memory-level parallelism</li><li>Exploring the design space like page management, allocation and cache policy in linux running on ECPT</li></ul> <b>NU Compilers Group</b> , <a href="#">Prof. Simone Campanoni</a> Jan. 2021 to July 2021 <i>Enhance Parallelism by Utilizing Commutative Loop iterations</i> <ul style="list-style-type: none"><li>Programmed with LLVM to tell if a strongly connected component within a loop iteration in program dependence graph is commutative</li><li>Extend the idea to develop tools for loop iteration commutativity for further utilization of parallelism</li></ul> <b>NU Parallelism Group</b> , <a href="#">Prof. Peter Dinda</a> June 2020 to May 2021 <i>CARAT CAKE: Replacing Paging via Compiler/Kernel Cooperation</i> <ul style="list-style-type: none"><li>Designed and implemented CARAT CAKE, an allocation level address space which aims to replace virtual memory and paging with protection checks inserted at compile time and allocations tracked in runtime</li><li>Implemented a competitive paging address space with support for red black tree and splay tree data structures to track VA-PA mapping, Transparent Huge Pages, and PCID; performance measured with Performance Monitoring Counter</li><li>Designed runtime protection check with address mapping data structures</li></ul> <b>Image &amp; Video Processing Lab</b> , <a href="#">Prof. Aggelos Katsaggelos</a> June 2019 to July 2021 <i>DeepCOVID-XR</i> <ul style="list-style-type: none"><li>Designed and implemented a CNN model to flag out positive COVID cases based on patients' chest X-ray images</li><li>Outperformed experienced radiologists with an accuracy of 85% compared to 76 - 82% and AUC of 0.935 compared to 0.819 - 0.856</li></ul>
PUBLICATIONS AND WORKING PAPERS	1. Ramsey M Wehbe, Jiayue Sheng, Shinjan Dutta, <b>Siyuan Chai</b> , Amil Dravid, Semih Barutcu, Yunan Wu, Donald R. Cantrell, Nicholas Xiao, Hatice Savas, Rishi Agrawal, Nishant Parekh, Aggelos K. Katsaggelos. "Deepcovid-xr: An artificial intelligence algorithm to detect covid-19 on chest radiographs trained and tested

	<p>on a large us clinical dataset.” <i>Radiological Society of North America</i>. [Online]. Available: <a href="https://doi.org/10.1148/radiol.2020203511">https://doi.org/10.1148/radiol.2020203511</a>.</p> <p>2. Brian Suchy, Souradip Ghosh, Aaron Nelson, Zhen Huang, Drew Kersnar, <b>Siyuan Chai</b>, Michael Cuevas, Gaurav Chaudhary, Alex Bernat, Nikos Hardavellas, Simone Campanoni, Peter Dinda. ”CARAT CAKE: Replacing Paging via Compiler/Kernel Cooperation.” <i>Submitted for ASPLOS 2022</i>.</p>																			
WORK EXPERIENCE	<p><b>Research Intern</b>, Tencent Network Group</p> <p><i>Service Driven Network Verification tool</i></p> <ul style="list-style-type: none"> <li>Contributed to design a scalable network verification that supports quantitative query and covers all data plane with global formal modeling and local simulation</li> <li>Designed easy-to-use geo-based intent language for network verification</li> </ul>	June 2021 to Aug. 2021																		
PROJECTS	<p><b>C-style Language Compiler</b>, CS 322 Compiler Construction</p> <ul style="list-style-type: none"> <li>Created, from scratch, a compiler to translate C-style language to x86_64 assembly</li> <li>Implemented features including graph-coloring register allocation, liveness analysis, instruction selection with tiling, control flow graph, and memory access checking</li> </ul> <p><b>Middle End Analysis for a C-based API</b>, CS 323 Code Analysis &amp; Transformation</p> <ul style="list-style-type: none"> <li>Coded a LLVM pass to optimize program in a custom C-based API</li> <li>Implemented analysis including reaching-definition, constant propagation and folding, alias analysis for the specific API, function inlining, and dead code elimination</li> </ul>																			
PROFESSIONAL ACTIVITIES	<b>SOSP 2021</b> : Artifact Evaluation Committee, Slack Co-chair																			
TEACHING EXPERIENCE	<p><b>Peer Mentor (Undergraduate TA)</b> - Northwestern University</p> <table> <tr> <td>Spring 2021</td><td>CS 336 - Design &amp; Analysis of Algorithms</td><td>with Prof. Jason Hartline</td></tr> <tr> <td>Winter 2021</td><td>CS 343 - Operating Systems</td><td>with Prof. Peter Dinda</td></tr> <tr> <td>Winter 2020</td><td>CS 336 - Design &amp; Analysis of Algorithms</td><td>with Prof. Konstantin Makarychev</td></tr> <tr> <td>Fall 2019</td><td>CS 336 - Design &amp; Analysis of Algorithms</td><td>with Prof. Jason Hartline</td></tr> <tr> <td>Spring 2019</td><td>CS 336 - Design &amp; Analysis of Algorithms</td><td>with Prof. Jason Hartline</td></tr> </table> <p><b>Teaching Assistant</b> - Washington University in St. Louis</p> <table> <tr> <td>Spring 2018</td><td>ESE 205 Introduction to Engineering Design</td><td>with Prof. James Feher</td></tr> </table>	Spring 2021	CS 336 - Design & Analysis of Algorithms	with Prof. Jason Hartline	Winter 2021	CS 343 - Operating Systems	with Prof. Peter Dinda	Winter 2020	CS 336 - Design & Analysis of Algorithms	with Prof. Konstantin Makarychev	Fall 2019	CS 336 - Design & Analysis of Algorithms	with Prof. Jason Hartline	Spring 2019	CS 336 - Design & Analysis of Algorithms	with Prof. Jason Hartline	Spring 2018	ESE 205 Introduction to Engineering Design	with Prof. James Feher	
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AWARDS AND HONORS	<p><b>Dean’s List</b>, all quarters</p> <p>ACM-ICPC, Mid-Central Regional, <b>Top 20%</b></p> <p>VEX Robotics International Championship, <b>Top 4 Alliance</b></p>	<p>2017 - 2021</p> <p>2018</p> <p>2016</p>																		