ERIC ZHAO

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EDUCATION

May 2021(Expected)	UNIVERSITY OF SAN FRANCISCO (San Francisco, CA) Master of Science in Computer Science (Cumulative GPA: **3.79**/4.00)
May 2016	BRANDEIS UNIVERSITY (Waltham, MA) BA Economics (Cumulative GPA: **3.55**/4.00)

PROFICIENCIES

Technical skills: Java, React, MATLAB, C, C++, HTML, CSS, JavaScript, Python, Stata

RELATED COURSES

Programming in Java and C, Data Structures and the Fundamentals of Computing (Grade A), Human–Computer Interaction, Scientific Data Process in MATLAB (Grade A), Programming Language Paradigms (Grade A+), Modern Parallel Programming (Grade A+), Principle of Software Development, Algorithms, Machine Learning(Grade A+), Computer Architecture(Grade A), Operating System(Grade A)

PROFESSIONAL EXPERIENCE

June to August 2019
Inzyme Information Technology (China)
<u>Software Engineer Intern</u>

- Participated in the Spatial-Temporal Big Data Project wherein the team implemented a Data Change Monitoring function using Java and SpringBoot + Mybatis + Dubbo development framework
- Came up with constructive ideas during the development process of the tile automatic update module, which significantly contributed to the efficiency of the module

July to September 2017
WORLDQUANT LLC (Old Greenwich CT)
<u>Staff Accountant Intern</u>

- Analyzed historical accounting data and performed audit procedures, including creating and supporting schedule and roll-forwards; Assisted in intercompany reconciliation and monitored unallocated travel expenses

RELEVANT PROGRAMMING EXPERIENCE

September 2020 **PNC BANK INTEREST RATE RISK FORCASTING PLATFORM**

- Developed the front-end of a web application that performs a series of financial scenario analysis for PNC bank manager to assist them in decision making
- Designed and implemented the structure of data flow between components using React.js
- Designed the layout of the front-end using HTML, CSS, Ant Design, Bootstrap, Material-UI

March 2020 **ARM SINGLE-CYCLE PROCESSOR IMPLEMENTTATION PROJECT**

- Implemented a single-cycle processor implementation of a subset of the ARMv7 instruction set using digital logic in Digital
- Wrote a C program that can execute ARM machine code by emulating the register state of an ARM CPU and emulating the execution of ARM instructions
- Built necessary components such as Program Counter, Instruction memory, Control Unit that allow the implementation to be able to execute machine code version of programs such as Quick Sort, Fibonacci

October 2019 **HOTEL RECOMMENDER PROJECT**

- Developed a server-side web-based application that allows users to access hotel information (general hotel info, attractions nearby, and reviews of the hotel)
- Designed Website with features including User Registration, Login/Logout (Session), Search Reviews etc.
- Designed front-end using HTML, JavaScript, Bootstrap, and Ajax, thus made the web page user friendly and easy to access
- Implemented relational database using MySQL, thus realized the data transfer between front and back end

December 2018 **MOVIE RECOMMENDER PROJECT**

- Developed a web-based movie recommender that allows users to get a series of recommendations depending on their preferences, and the recommender uses Model-View-Controller (MVC) pattern
- Implemented back end using JAVA and allowed data sync and interaction between the front and back end