0^16

||2

MUX2

ADD8

MUX1

||

PC

Address

IMEM

NPC

(ADD)

MUX4

Rd Rs

Rdc

Regfile

Rsc

Rtc Rt

Data

Adress

DMEM

MUX

3

3

A

ALU F

Z

C

B N

O

EXT5

EXT16

EXT18

A

ADD

B

MUX5

imm25:0||00

M2

PC31:28

IMEM10:6 32bit

4

SA

M3

M1

PC\_CLK IM\_R

imm16 RF\_W CS DM\_R

offset DM\_W

IMEM15:0 RF\_CLK

M4

32bit ALU2 ALU1 ALU0

32bit M5

8