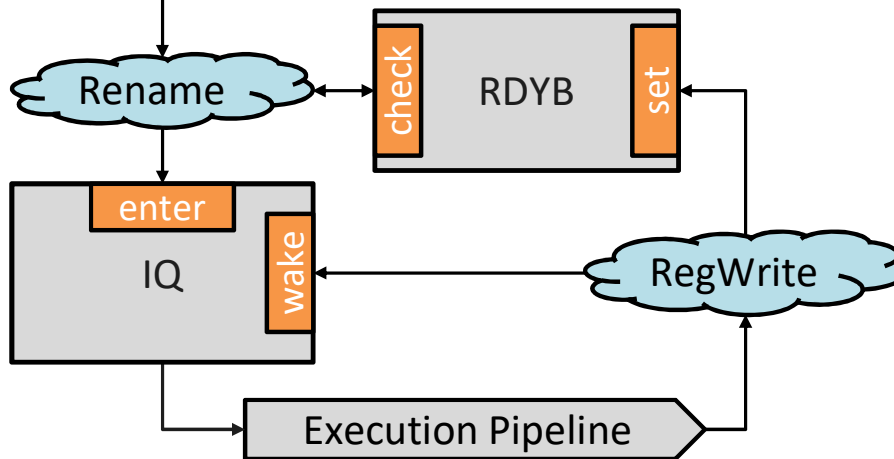


I : P10=**P3**+1

① Rename checks **P3** not ready in RDYB



③ Rename enters **I** into IQ with **P3** not ready. **I** will never be waken up → deadlock

② RegWrite sets **P3** ready in RDYB but finds no instruction to wake up in IQ