Semester Planning: Autumn Semester 2017 (P8a)

Week	38	39	40	41 4	12 43	3 44	45	46 4	7 48	49	50	51 5	52	1 2	3	4	5	6	7	8 - 37								
	S	ер.	(Octob	er		Noven	nber		De	ecem	ber		Ja	nuary	/	Fel	brua	ry		M	SE	Days	s off	Pro	ject	IME	
	18.09 - 24.09	- 01.	10 -	- 15.	16.10 - 22.10 23.10 - 29.10	10 - 05.		.11 - 19.1	20.11 - 26.11 27.11 - 03.12	12 - 10.	1	12 - 24.	1	01.01 - 07.01	- 1	- 1	.01 - 04	05.02 - 11.02	12.02 - 18.02	19.02 - 16.09	Total Hours	Total Days						
MSE (Lesson, EV, Exams,)	17	17	27	27 2	27 17	17	17	17 1	7 17			17				30		32		210	600	71.4					•	
Holiday, Public Holiday												4	42 4	12					42	210			336	40.0				
Project SS-OCT	6	6	13	10	14	19	23	23 2	23 23	23	18	19		40	20	10				770					1060	126.2		
IME, CoachING, Wilda,	19	19	2	5 1	5 11	6	2	2	2 2	2	7	6		2	2	2	2	10		70							188	22.
Total Hours per Week	42	42	42	42 4	12 42	42	42	42 4	2 42	42	42	42	42 4	12 42	2 42	42	42	42	42	1260	60	0.0	336	6.0	106	0.0	188	3.0
Diffe	renz zu 42 h	0	0	0	0 (0 0	0	0	0 () (0	0	0	0 (0 0	0	0	0	0	0					P8a		29)0
																									P8b		77	' 0
																									Total		24	0.4

Projec	Project Planning: FFT for OCT - Matlab Model																											
Week	38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 1 2 3 4 5 6 7 8-37																											
		Se	p.		Octo	ber		No	November			December				January			F	February			Zeit					
		18.09 - 24.09	25.09 - 01.10	02.10 - 08.10		16.10 - 22.10	23.10 - 29.10	30.10 - 05.11	13.11 - 19.11	20.11 - 26.11	27.11 - 03.12	04.12 - 10.12			25.12 - 31.12	01.01 - 07.01		15.01 - 21.01		05.02 - 11.02	12.02 - 18.02	19.02 - 16.09	Total in WP	Total Hours	Total Days		Date:	Milestone:
WP3.0	Introductory phase																											
i	Transfer	6	6																					12	1			
ii	Understand Structure			13																			69	13	2			
iii	Prepare existing Matlab-Files						14																09	14	2			
iii	Introduction to VHDL/Quartus																					30		30	4			
WP3.1	Analysis of FFT structure																											
i	Analysis Bitwidth															10	0						10	10	1			
ii	Comparison Generateable Code																						10	0	0			
WP3.2	Development of a model																											
i	Implementation 16K FFT-Core							19 2	3 23	10	_													75	9			
ii	Expansion to 32K FFT											23											164	64	8			
iii	Verification									10				15)									25	3	•	22.12.17	Golden Model of FFT Core
WP3.3	Implementation on FPGA																											
i	Implementation 16K/32K FFT-Core																					350	500	350	42			
ii	Verification																					150		150	18			
WP3.4	Administratives																											
i	Project Clarification P8a				10	,																		10	1	•	13.10.17	Delivery Project Clarification
ii	Presentation P8a																		o †				57	10	1	•	25.01.18	Presentation
iii	Documentation P8a									3				4		20	0 1	0	•					37	4	•	26.01.18	Delivery Tech. Report
iv	Administratives P8b																					115	115	115	14			
Х	Reserve/Buffer) 1					125		145	17			
Total Hou	rs per Week	6	6	13	10	0	14	19 2	3 23	23	23	23	18	19	0	0 40	2	0 10	0	0	0	770		1060	126.2			

P. Studer, 16.10.2017