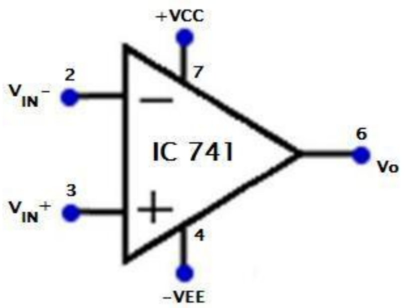
EXPERIMENT #1 BASICS OF OPERATIONAL AMPLIFIER

I OBJECTIVES

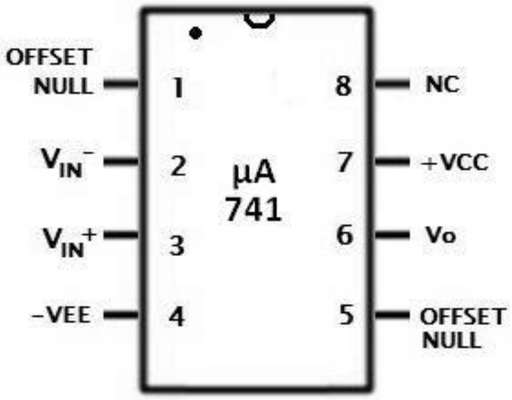
The primary objective of this experiment is to familiarize the student with the basic properties and applications of the integrated-circuit operational amplifier, (the “op-amp” in short), one of the most versatile building blocks available to electronic-circuit designers. The emphasis will be primarily on the nearly ideal op-amp.

II COMPONENTS AND INSTRUMENTATION

The focus will be on the 741-type op amp provided one per IC, in an 8-pin dual-in-line (DIP) package whose schematic connection diagram and packaging are shown in Fig. 1.1. For power, you will use two supplies, ±15 V for short. As well, you need a variety of resistors and capacitors, with emphasis on ones simply specified: 1kΩ, 10kΩ, 100kΩ, 1MΩ, 10MΩ and 0.1µF, 0.01µF, 1nF, and the like. Note that it is important to bypass the two power supplies directly on your prototyping board, using, for each supply, a parallel combination of a 100µF tantalum or electrolyte capacitors, and or 0.1 µF low inductance ceramic capacitor. For measurement, you will use a bench multimeter, a two channel oscilloscope with probes and a waveform generator.



µA 741 Symbol



PIN Diagram

Fig. 1.1 Circuit symbol of op-Amp and the pin diagram

III PREPARATION

Preparation is intended to help familiarize you with the experimental work to follow, ideally, by raising questions about the specific circuits you will later explore, it will help you in thinking about the experiments you will perform and the results you will obtain, **as you proceed.** We will refer directly to the steps of the experimental work to follow, using the numbering of procedures and circuit figures found there. Unless otherwise specified, in what follows, assume all op amps to be ideal. (You may write the answers on the reverse of this page.)

Question1.

In Fig. Q1 a practical voltage source v_s with internal resistance R_s feeds a load R_l through an amplifier with input and output resistances R_i and R_o respectively. Find v_2/v_1 .

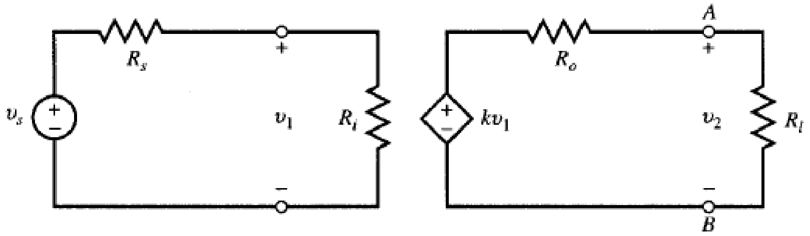


Fig. Q1

Question2.

In the inverting amplifier circuit below, compute v_1 and v_2 and sketch v_s, v_1 and v_2 if $v_s = \sin100t$.

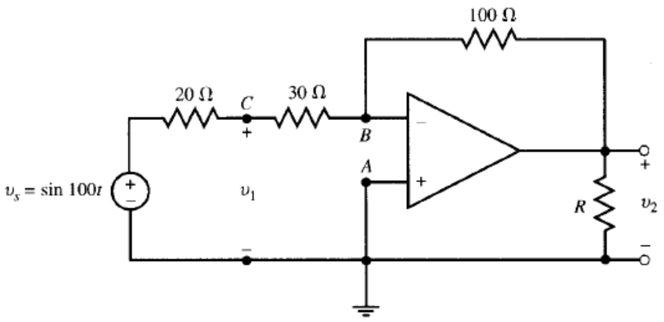


Fig. Q2

Question 3

Design an op-amp circuit with an input resistance of 2.2 kΩ and a gain of -10 V/V. What are the values of R_1 and R_2 you have chosen?

Question 4

Using an ideal op-amp, design a non-inverting amplifier with gain of +11 V/V having low currents in the associated resistor network, but with no resistor larger than 10 kΩ.

Question 5

For the op-amp with V_{cc} = 5V, $A=10^5$, $v^- = 0$ and $v^+ = 100\sin2\pi t$ (μV). Find and sketch the open-loop output v_o .

Question 6

The output voltage of an op-amp decreases by 10% when a 5 kΩ load is connected at the output terminals. Compute the output resistance R_{out}.

Question 7

An op-Amp has a common mode amplification A_{cm}=0.0002. Find the magnitude of the output voltage if the input voltages are $V^- = V^+ = 2$V.

IV EXPERIMENTATION

4.1– *Inverting Amplifier*

4.1.1 – Inverting Amplifier – AC Gain

Draw the basic inverting amplifier circuit (using µA 741) with appropriate power supply connections, input signal source etc (denote input and feedback resistors using R_1 and R_2 respectively).

Measurements
Table 1

R_1 $(K\Omega)$	R_2 $(K\Omega)$	V_{inpp} (V)	V_{outpp} (V)	Experimental Gain	Theoretical Gain
1	10	0.020			
1	100	0.020			
1	1000	0.020			

Using the resistors specified in the table , wire the circuit drawn. Use your signal generator to apply a sine wave voltage at 1000 Hz (as V_{in}) and channel 1 of the scope to monitor it. Use channel 2 to observe the output. Measure the output voltage for the various combinations of resistors shown in the table above. Use the formulae to calculate the theoretical gain in the last column. **Comment on the agreement of two sets of gains**.

4.1.2 – Inverting Amplifier - Gain (dc and ac)

Use the DC offset capability of the signal generator to vary the DC offset of the input signal. Observe the behaviour of the output. Check that your scope inputs are in DC coupling mode. Set R_1=10kΩ, R_2=1kΩ.

Table 2

V_{inpp} (V)	DC offset in V_{in} (V)	V_{out} (V) dc component	ac (pp)	ac gain (theoretical)	ac gain (measured)	dc gain (theoretical)	dc gain (measured)
1.0	0.5						
1.0	-0.5						

Comment on your findings:

4.1.3 – Inverting Amplifier - Linearity

Set the DC offset back to zero. Keep R_1 at 1k, R_2 at 100kΩ and the frequency at 1000 Hz. Measure V_{out} for the various values of V_{in} shown in the table below. (Do not fail to observe the waveforms).

Table 3

V_{inpp} (V)	V_{outpp} (V)	Experimental Gain
0.020		
0.100		
0.150		
0.200		

Extend the table until you get clipping at the top and bottom of the waveform. Determine the output voltage where clipping occurs on both the top and bottom. Measure the actual voltage of the +15 and -15 power supplies and calculate the differences between the clipping voltage and the power supply voltage. Explain why the Op-Amp is unable to output voltages near the power supply levels. **Make a graph of V_{out} Vs V_{in} from your data. Determine the circuit gain from the slope of the graph. Is your amplifier linear? Comment on the linearity.**

4.1.4 – Inverting Amplifier - Frequency Response

Continue to use R_1 of 1kΩ and R_2 of 100kΩ. Set the input voltage (sine wave) at 0.1 V_{pp}. Measure the output voltage for the frequencies given in the table below and determine the experimental gain.

Table 4

Frequency (Hz)	V_{outpp} (V)	Experimental Gain	Gain (dB)
100			
1000			
3000			
10K			
20K			
40K			
50K			
100K			
300K			
1M			

Make a log-log plot of your results with gain on the vertical axis and frequency on the horizontal axis.
Comment on the frequency response of the Op-Amp circuit compared to that of the transistor amplifier.

4.2 – The Non-inverting Amplifier

4.2.1 – The Non-inverting Amplifier - Gain

Draw the circuit diagram and construct the basic non- inverting amplifier circuit (using µA741) with appropriate power supply connections and input signal source etc.

Connect a signal generator to V_{in} and set it to a frequency of 1000 Hz. Check that the DC offset is zero. For the values of R1 and R2 in the table below, set an appropriate input voltage and measure the corresponding V_{out}. **Adjust V_{in}** as you change the resistors to get easily measured voltages **with no clipping**. Use your data and the theory to calculate the data for the remaining columns in the following table.

Table 5

R_1 $(K\Omega)$	R_2 $(K\Omega)$	V_{inpp} (V)	V_{outpp} (V)	Experimental Gain	Theoretical Gain
1	10				
1	100				

Comment on the differences you found between the theoretical and measured gain for the non-inverting amplifier circuit.

4.3 - The Difference Amplifier - Common Mode Rejection Ratio (CMRR)

The quality of the amplifier is measured (in part) by the common-mode rejection ratio (CMRR), based on the ratio of the differential voltage gain and common-mode voltage gain. The common-mode voltage gain is determined by applying identical signals to both inputs and observing the output voltage: Theoretically, the common mode gain of such a circuit should be very close to zero. In practice, in this basic circuit 1.4, using resistors of 1% tolerance, the ratios $R_2/R_1 \neq R_4/R_3$. This resistance ratio unbalance is the main cause for the common mode gain being significant, therefore easily measurable.

Construct the circuit shown at the right. Connect the inputs, V_A and V_B, together and connect to one signal generator. Set the generator to 1000 Hz (sine) and 15 V_{pp} amplitude. Measure and record the input and output voltages (peak to peak). Calculate the common-mode gain that results.

$A_{CM} = V_{outCM}/V_{inCM}$ where $V_{inCM} = V_A = V_B = 15\ V_{pp}$.

$A_{CM} =$

To measure the differential voltage gain, ground one input while applying a 1 kHz sine wave of amplitude 1 V (pp) at the other input. Measure the differential voltage gain. Compute

$A_{diff} = V_{outdiff}/V_{indiff}$

$A_{diff} =$

CMRR = $20\log_{10}(A_{diff}/A_{CM})$.

It is customary to give the CMRR in decibels

CMRR ______________

CMRR (dB) ______________

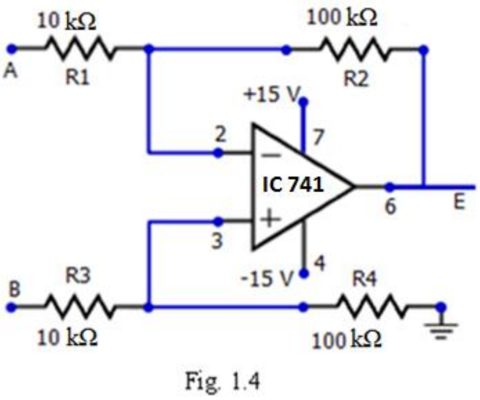


Fig. 1.4

V. INFERENCE/CONCLUSIONS