

DESCRIPTION

The CBM-II cartridge slot is designed for addressing 24K of memory in three 8K "Slots": \$2000-3FFF, \$4000-5FFF, and \$6000-7FFF which are all located in the CPU's BANK 15. Each slot has a dedicated chip select line

One 256K PLCC ROM chip provides 8 ROM SETS selectable via DIP switch. Each set consists of 24K ROM to fill the 3 cartridge slots. You can not control each slot separately.

There is an additional CHIP SELECT line on a header allowing one external (to the cartridge, ie: In the computer) memory range to included as well. This would normally be either the \$1xxx (2K DOS ROM) ranger or the \$C000-CFFF (4K) range (Not available on P500).

Updated: 2024-04-12



Steve J. Gray Sheet: / File: cbm2romcart.kicad_sch Title: CBM-II ROM CART Size: A4 Date: 2024-04-10 Rev: KiCad E.D.A. 9.0.0 Id: 1/1

STATUS: Working