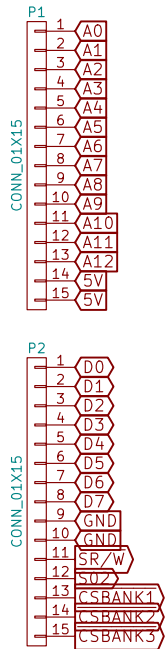
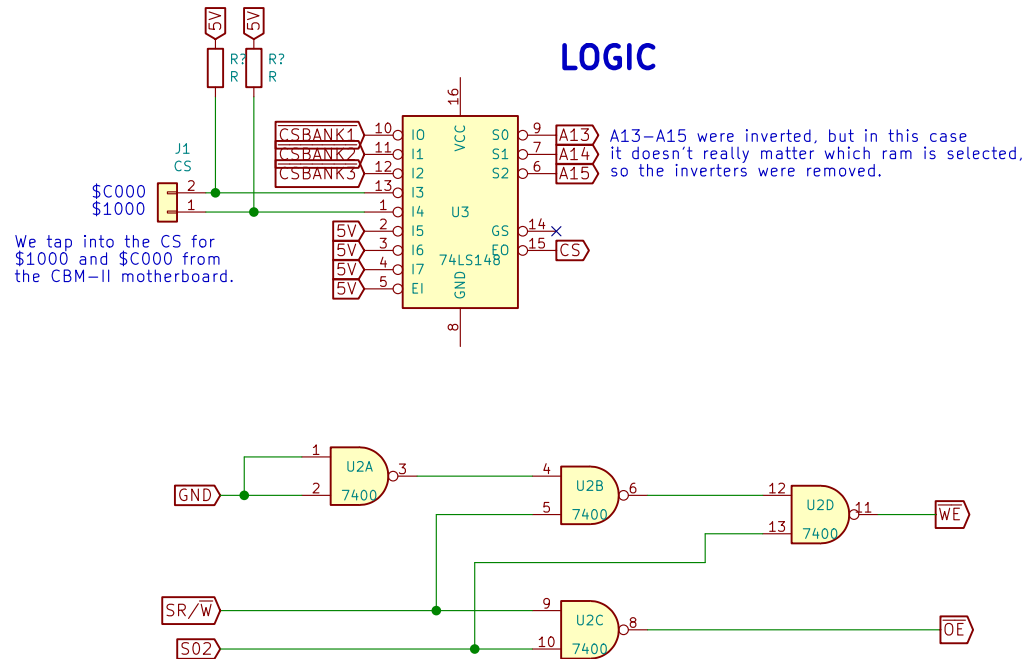


CBM-II CART CONNECTOR

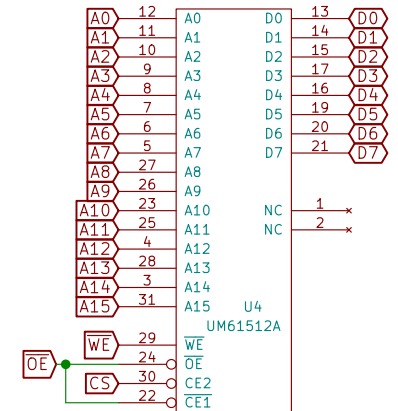


CBM-II cart is designed to support 24K RAM via 3 8K banks. Only addresses A0-A12 (8K) are available on the cart.

LOGIC

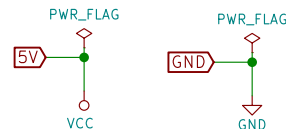


RAM



DESCRIPTION

The CBM-II cartridge slot is designed for addressing 24K of memory in 3 "BANKS": \$2000-3FFF, \$4000-5FFF, and \$6000-7FFF. Each BANK has a dedicated chip select labelled CSBANKx. There are also two memory areas designated for expansion internally at \$1000-1FFF and \$C000-CFFF. The priority encoder takes these CS lines and converts them to a 3 bit binary value which is used as the upper address lines to the RAM chip. A 74LS00 takes the R/W and S02 lines to generate separate READ and WRITE lines for the RAM.



CBM-II RAM CART

Sheet: /
File: cbm2ramcart.sch

Title:

Size: A4 Date: 2017-07-24
KiCad E.D.A. kicad (5.0.0)

Rev:
Id: 1/1

STATUS: Untested Prototype