

STM32H7xxx-Disco

MB1381

Revision B-02: correct pin name on CN6 on Arduino connector sheet.

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Top
TOP.SchDoc



Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

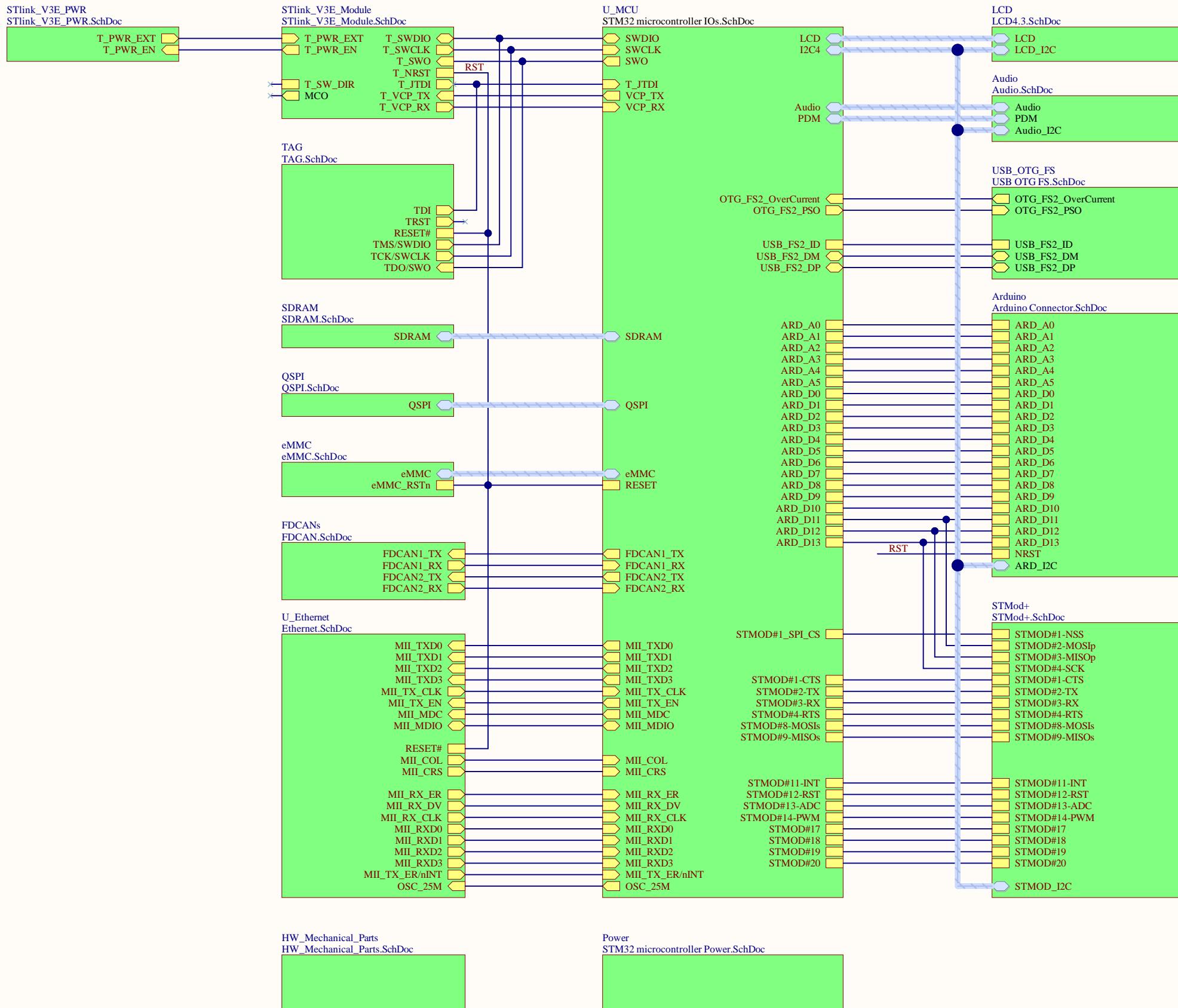
▲ Notes to generate the board layout.

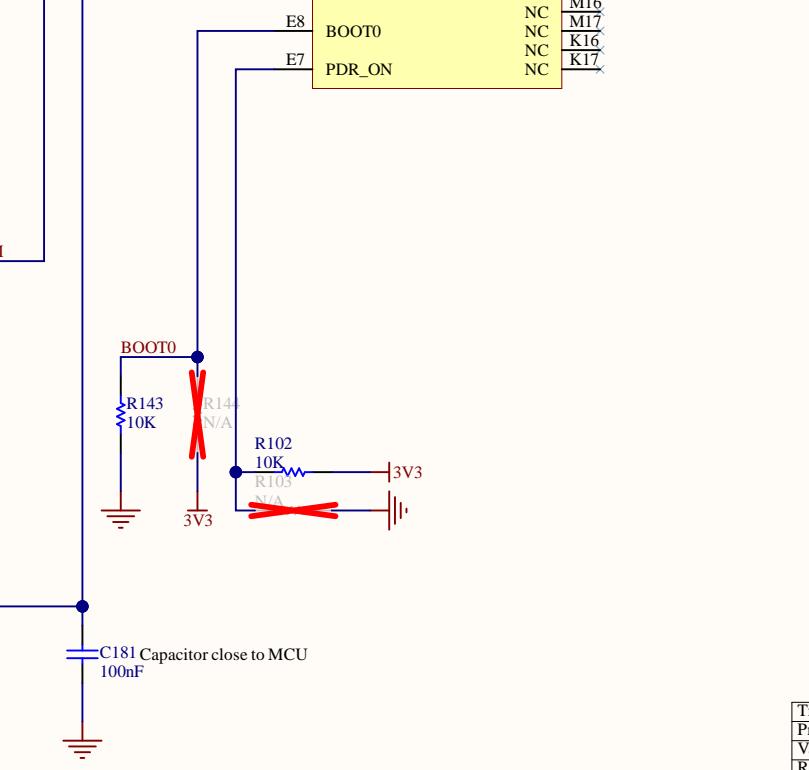
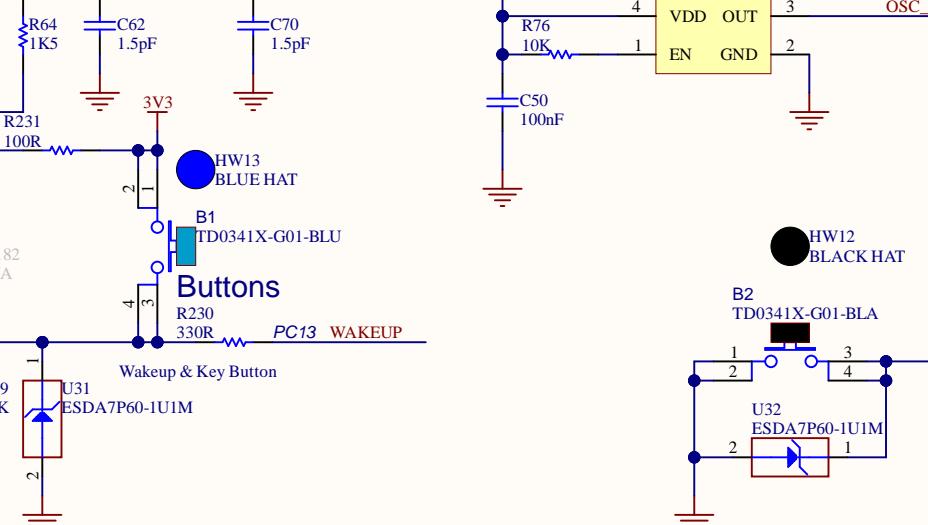
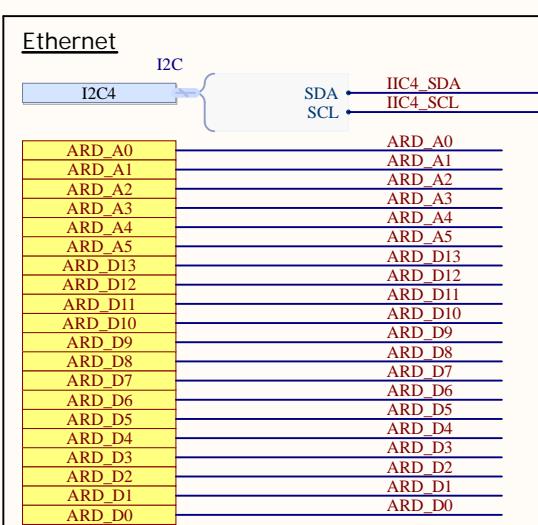
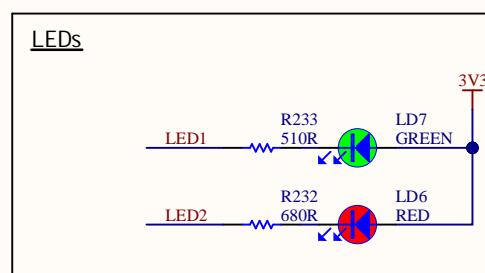
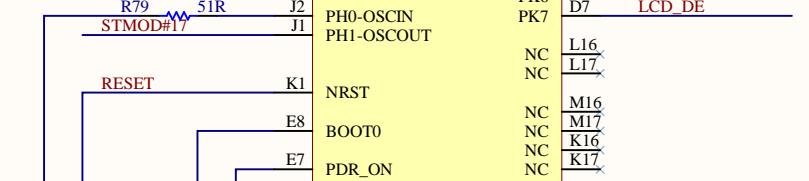
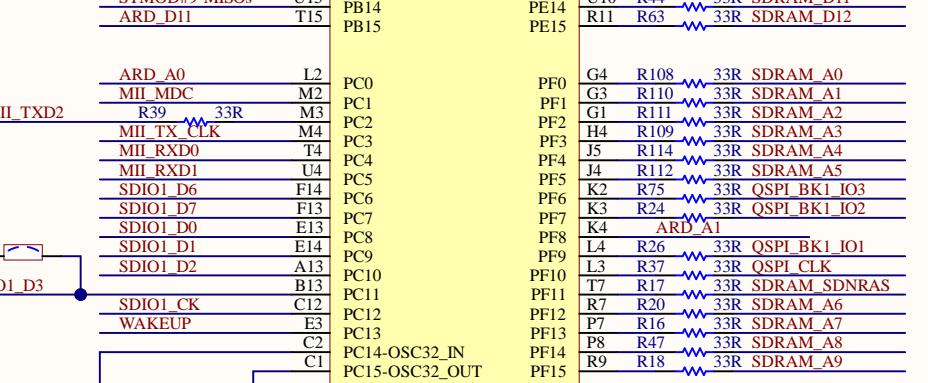
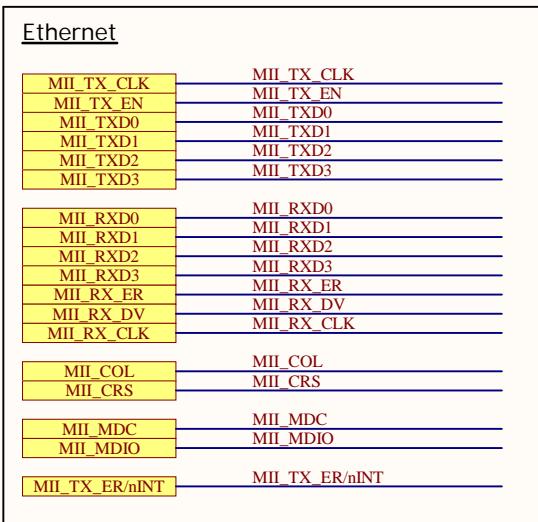
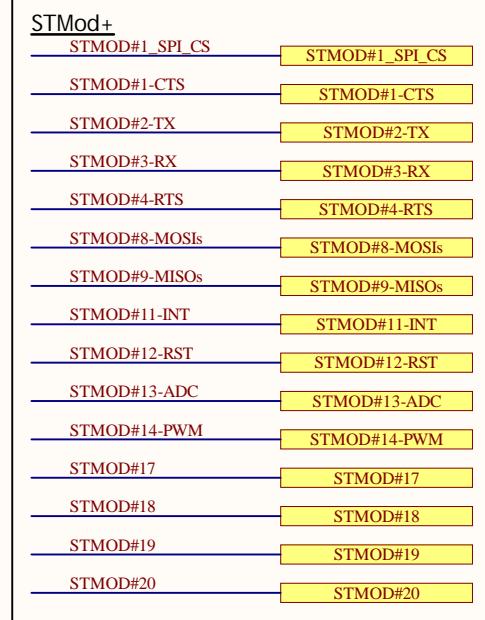
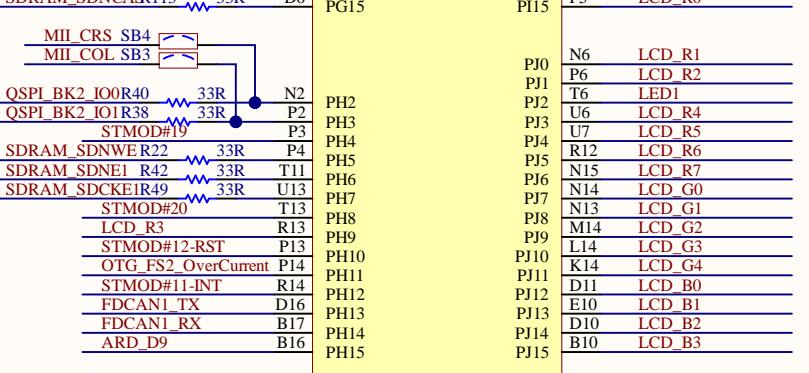
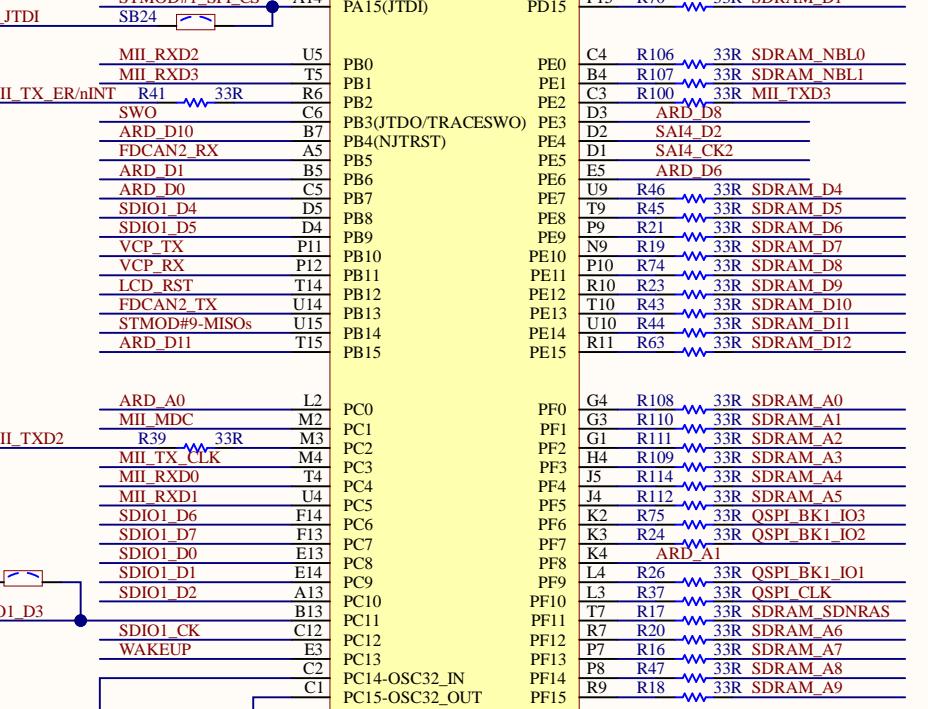
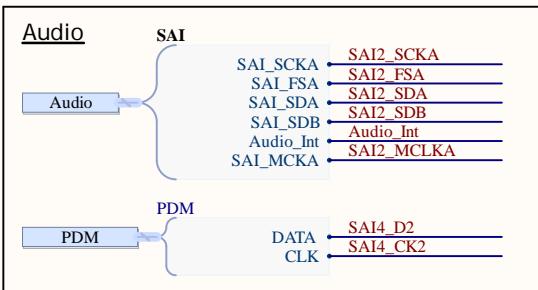
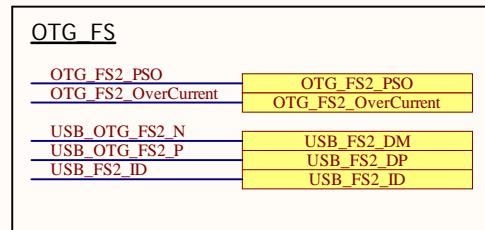
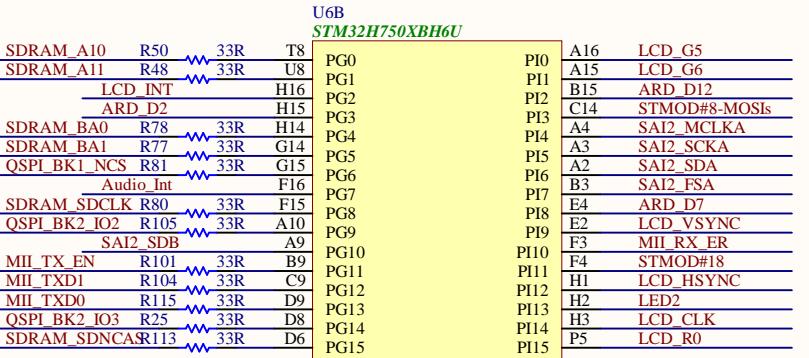
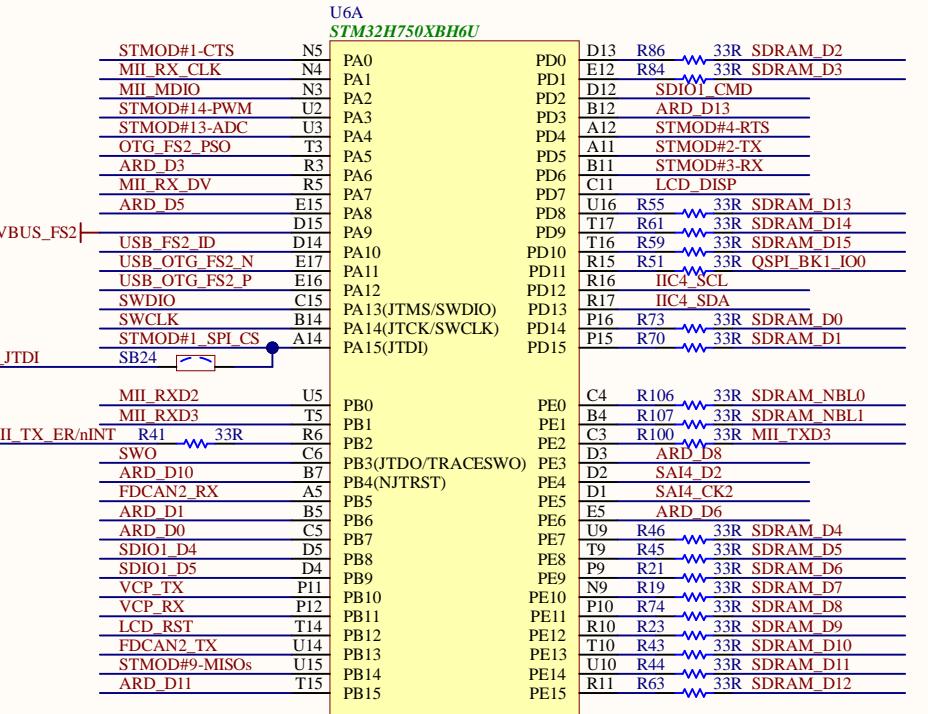
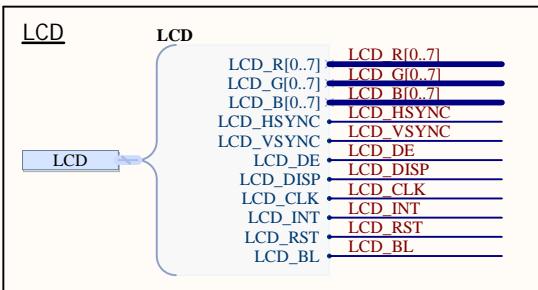
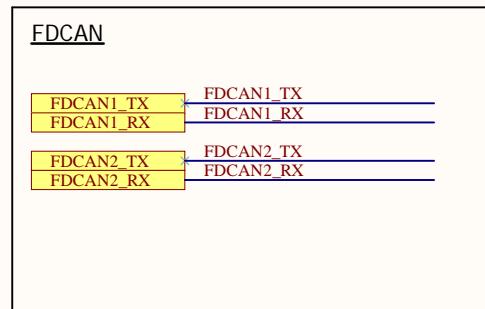
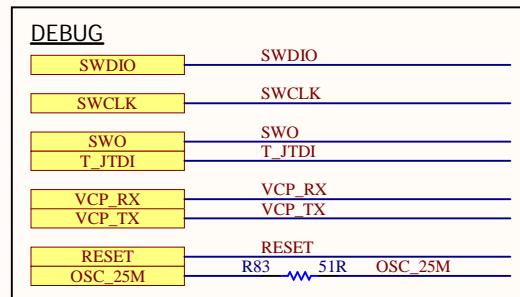
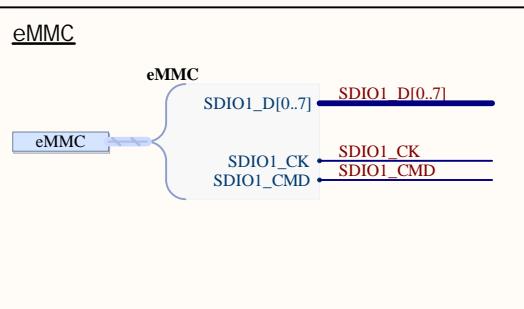
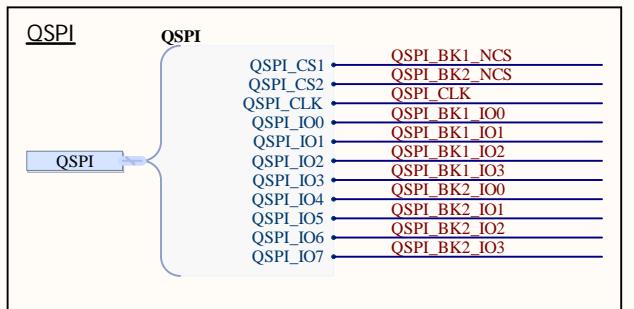
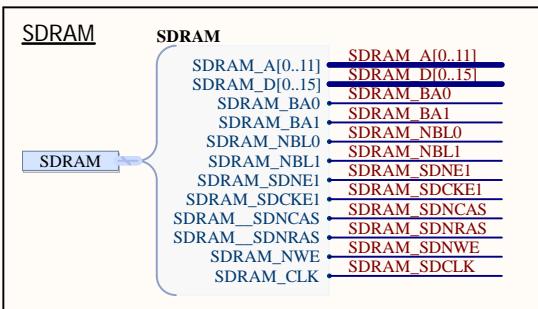
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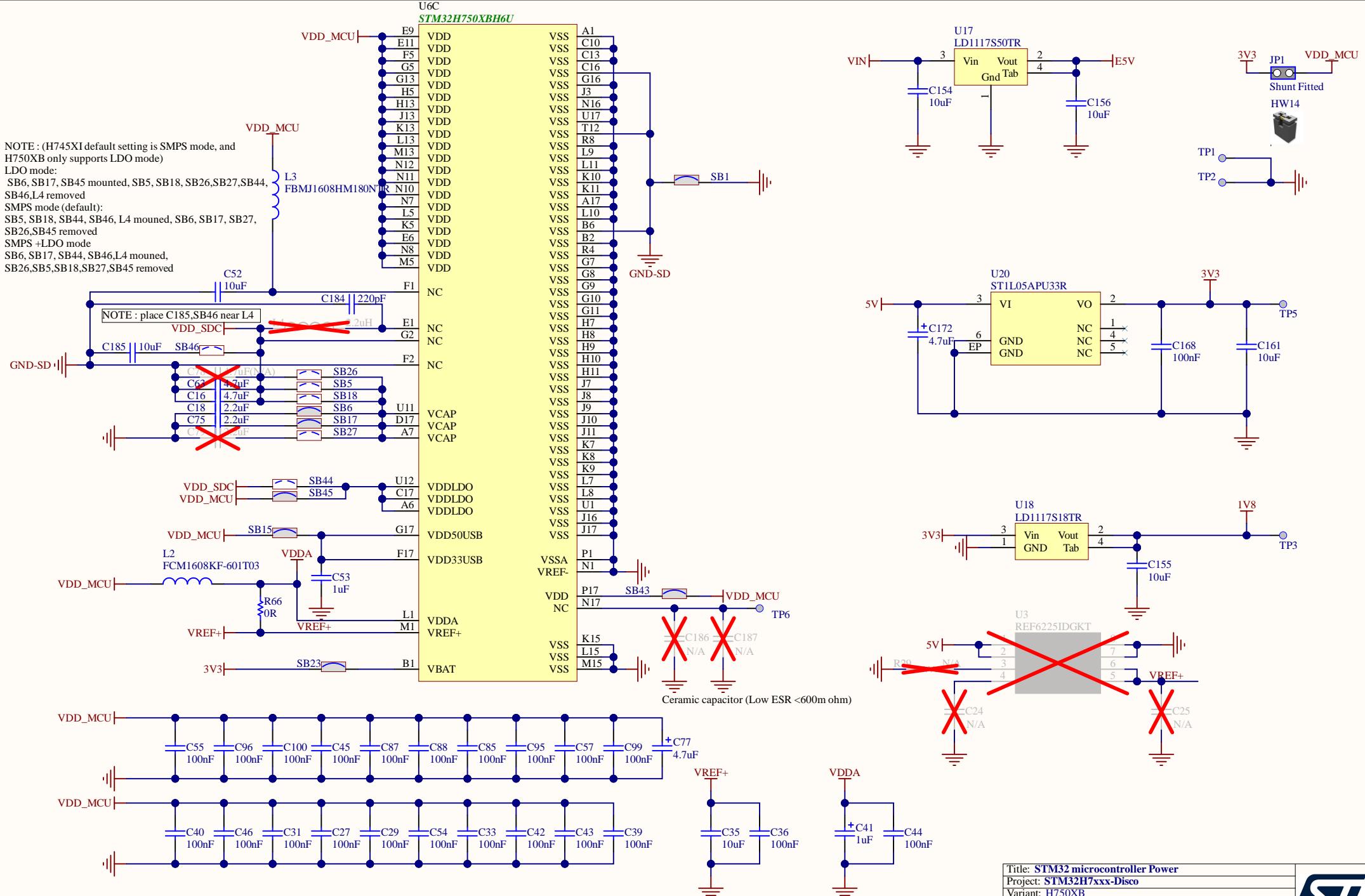
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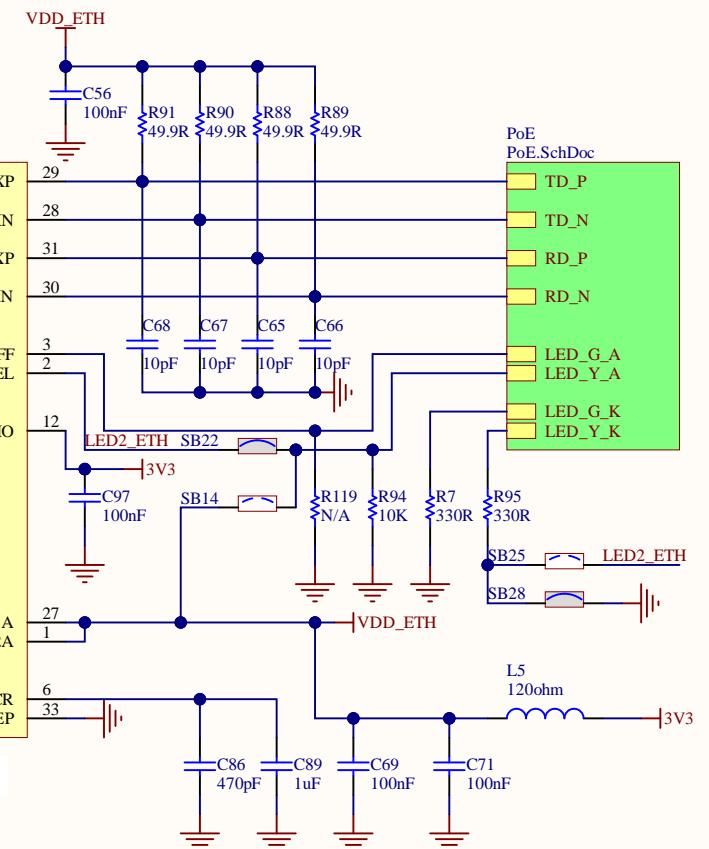
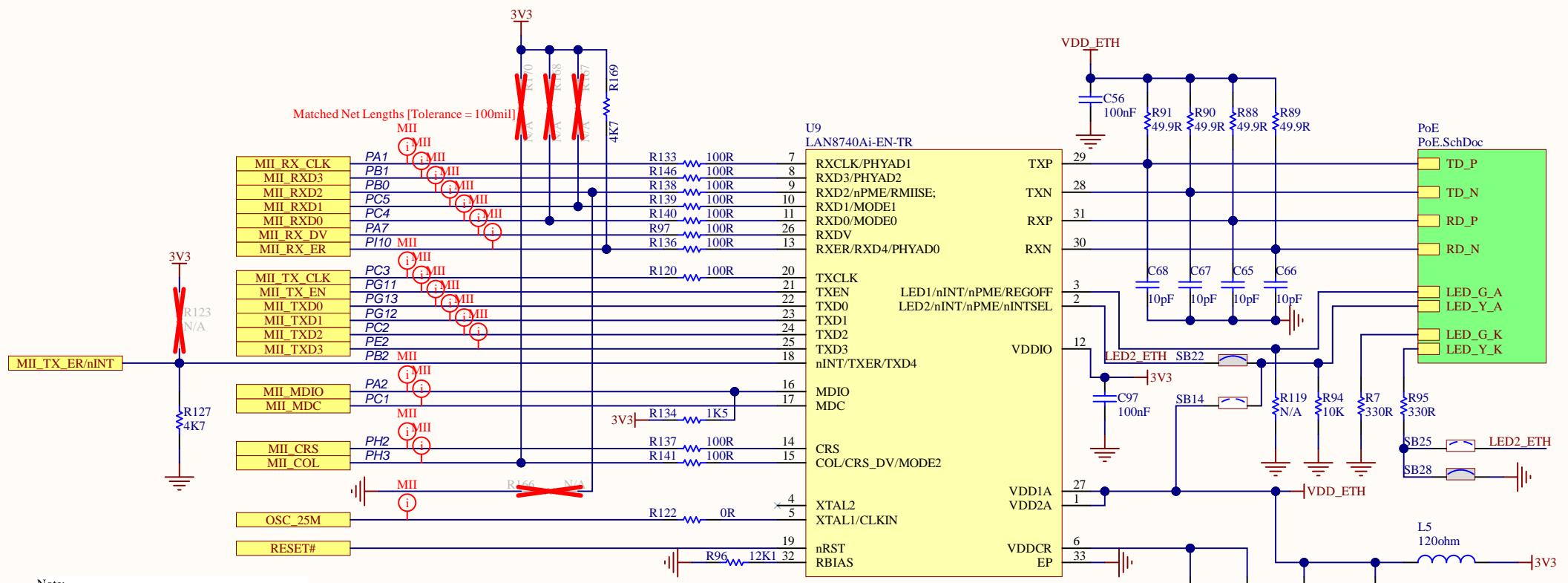
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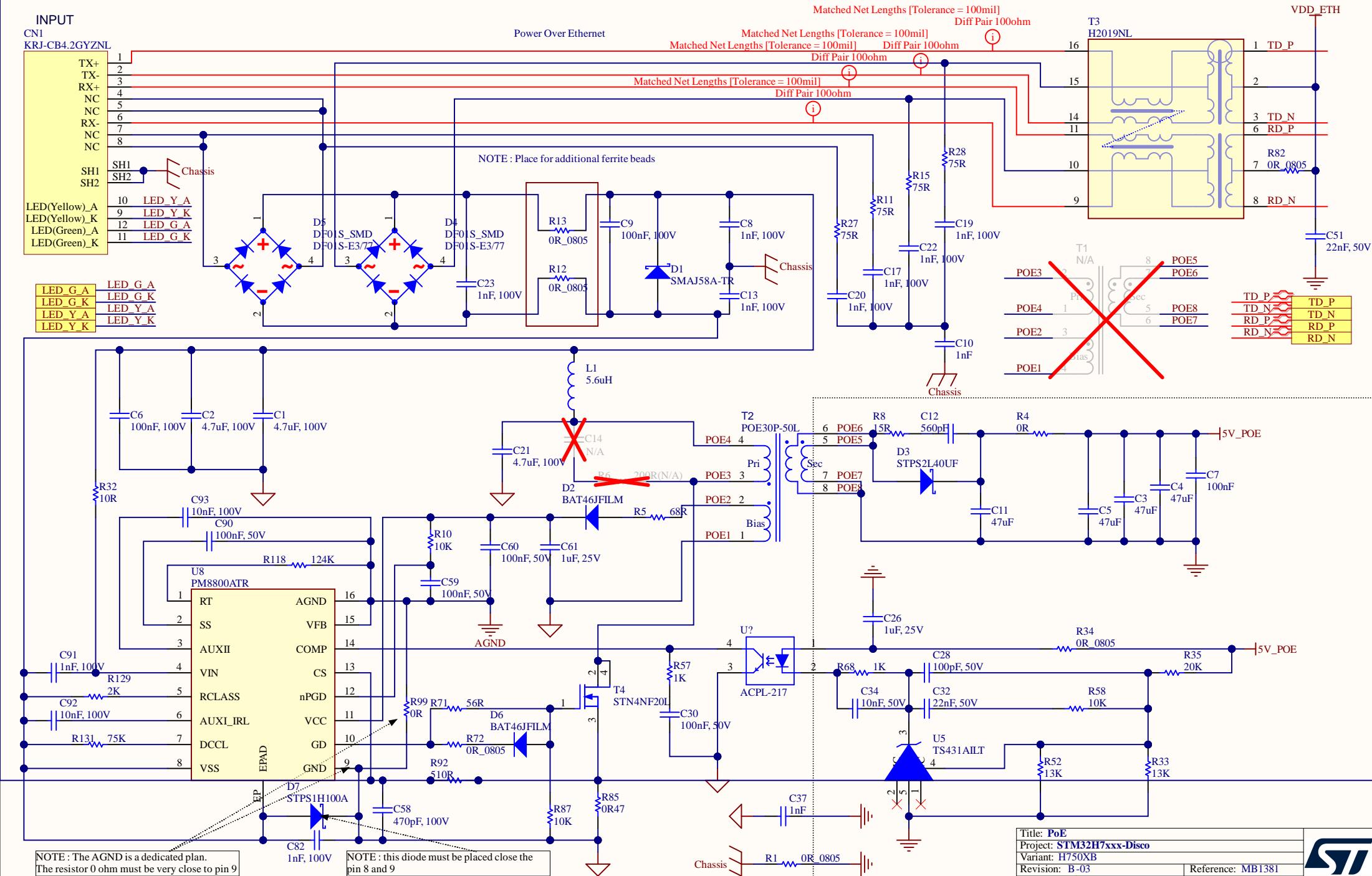
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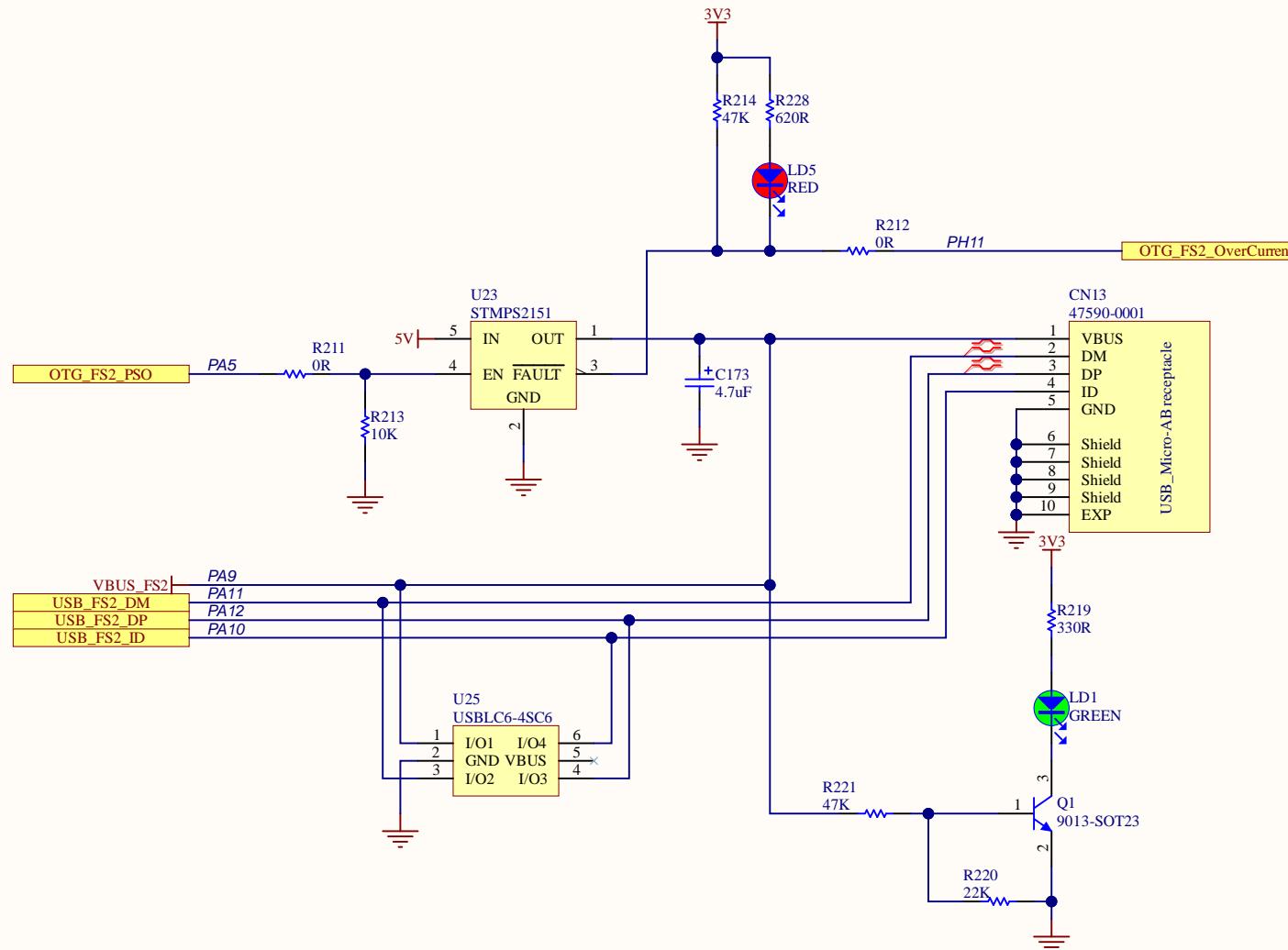


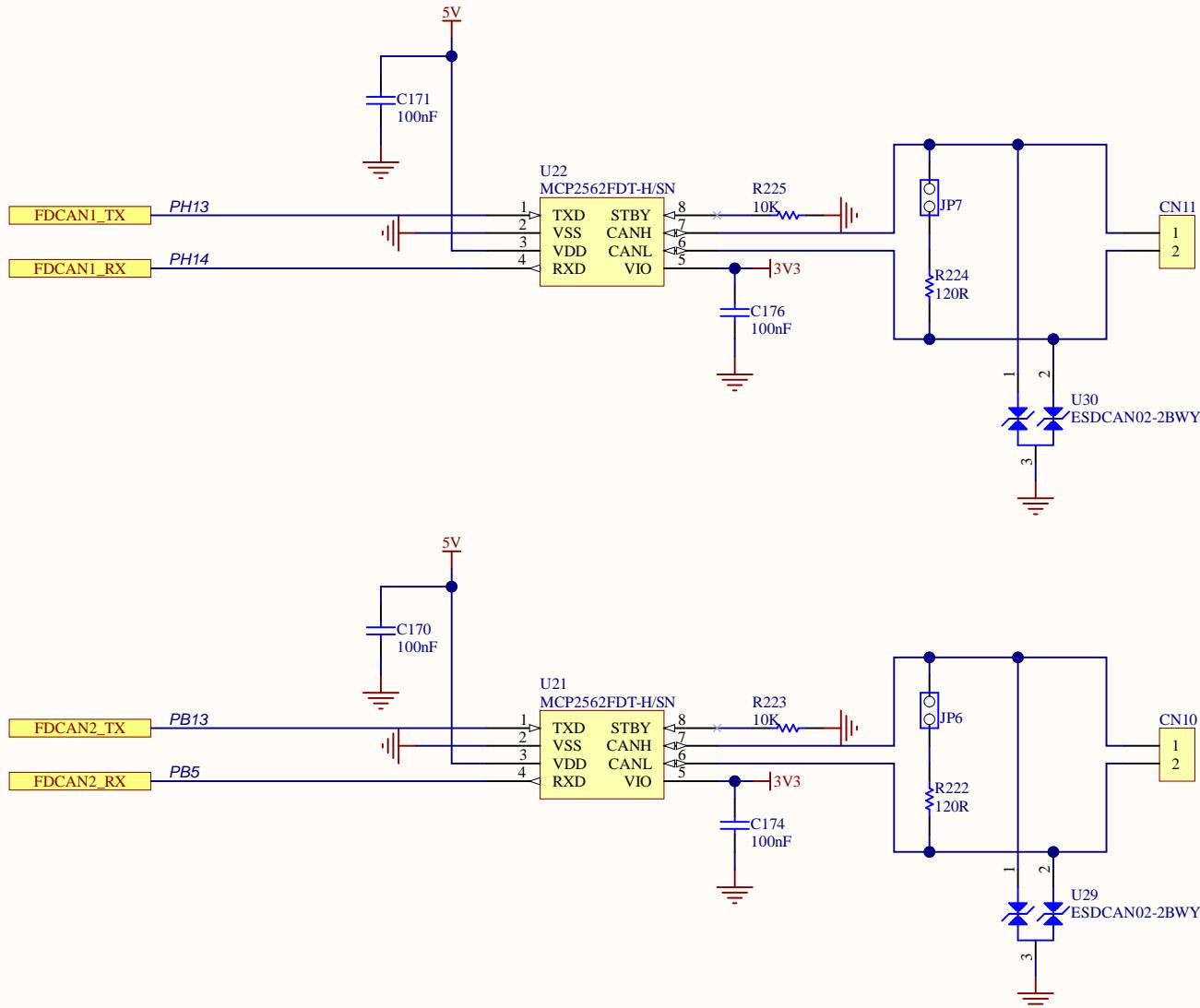


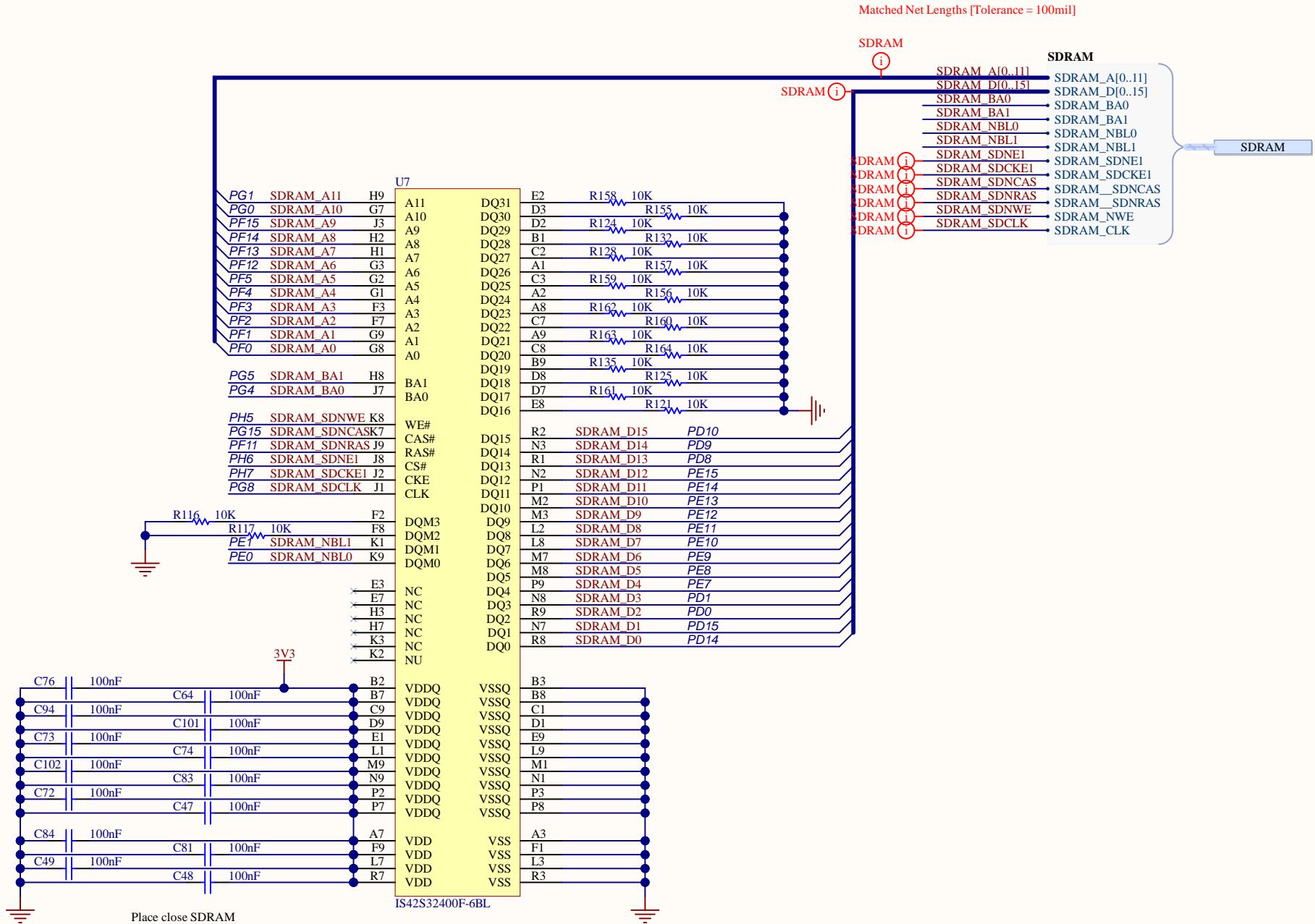


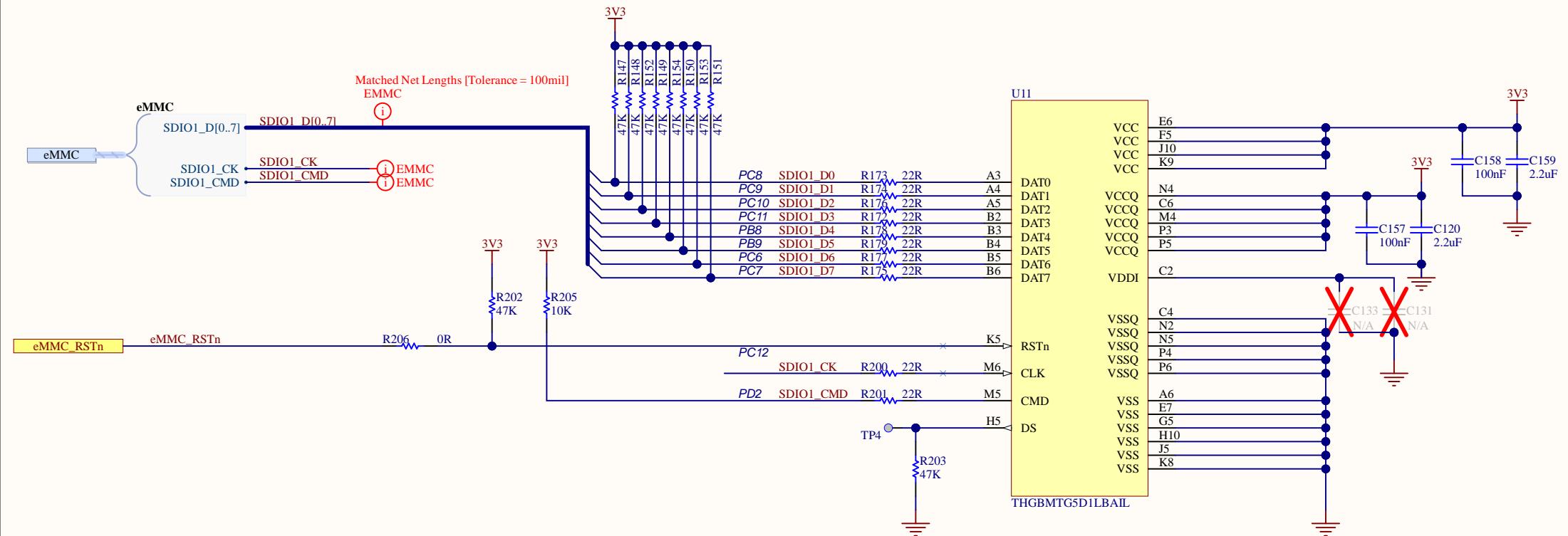










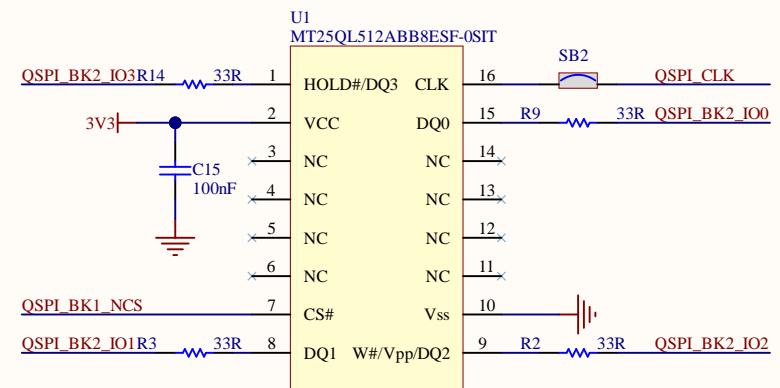
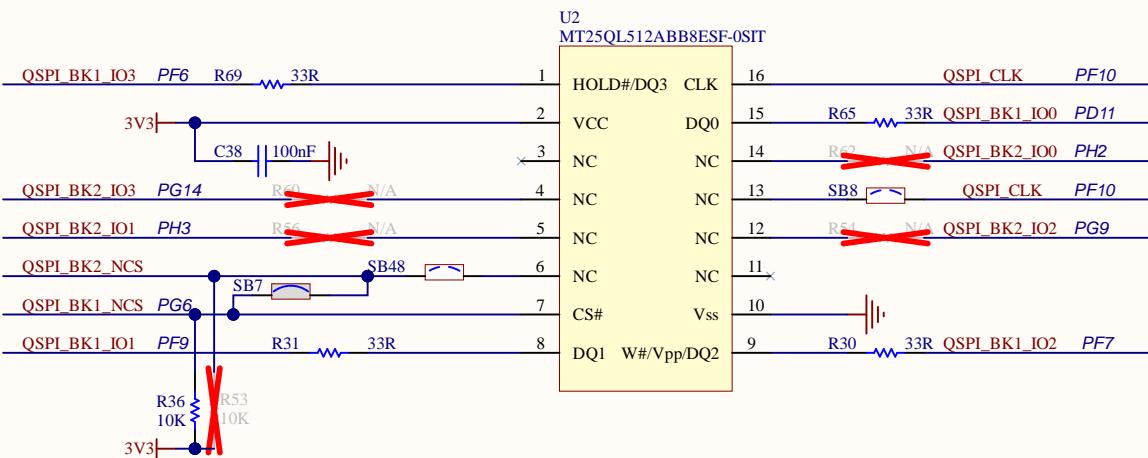
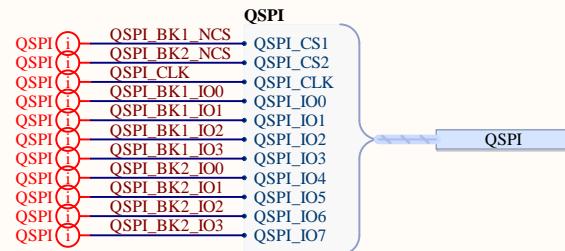


When change to THGBMDG5D1LBAIL, mount C131/C133

Title: eMMC	
Project: STM32H7xxx-Disco	
Variant: H750XB	
Revision: B-03	
Size: A4	Date: 02-JUN-22
Reference: MB1381	Sheet: 10 of 19



Matched Net Lengths [Tolerance = 100mil]

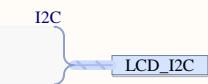


Twin Quad SPI Flash solutions:

One Twin Quad SPI Fllash	U2:MT25TL01GHBB8ESF-0SIT	U1:NA	R53/R62/R54/R56/R60/SB8/SB48 ON	R14/R3/R2/R9./SB2./C15 OFF
Two Quad SPI Fllash	U1:MT25QL512ABB8ESF-0SIT	U2:MT25QL512ABB8ESF-0SIT	R14/R3/R2/R9./SB2./C15 ON	R53/R62/R54/R56/R60/SB8/SB48 OFF

Matched Net Lengths [Tolerance = 100mil]

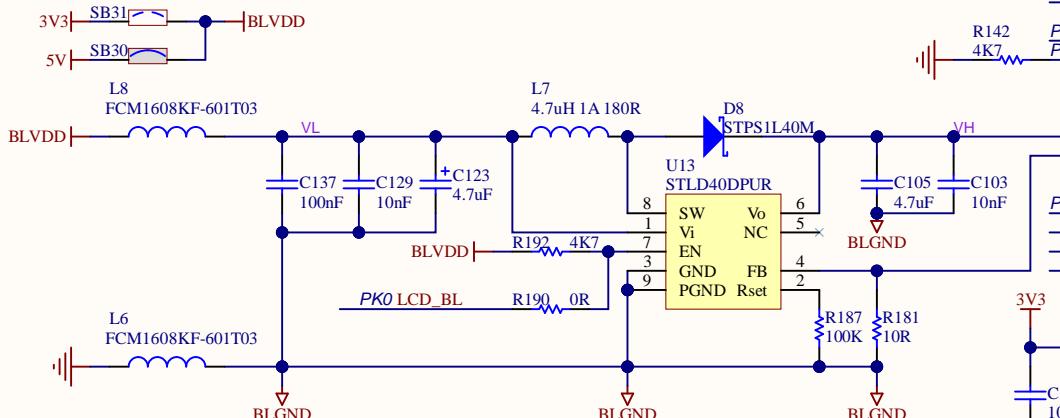
LCD i	LCD_R[0..7] LCD_G[0..7] LCD_B[0..7] LCD_HSYNC LCD_VSYNC LCD_DE LCD_DISP LCD_CLK LCD_INT LCD_RST LCD_BL	LCD_R[0..7] LCD_G[0..7] LCD_B[0..7] LCD_HSYNC LCD_VSYNC LCD_DISP LCD_CLK LCD_INT LCD_RST LCD_BL
--------------	--	--



U33	
<u>P14</u> LCD_CLK 30	CLK
<u>P12</u> LCD_HSYNC	HSYNC
<u>P9</u> LCD_VSYNC	VSYNC
DE	
DISP	
R0	LCD_R0 <u>P15</u>
R1	LCD_R1 <u>PJ0</u>
R2	LCD_R2 <u>PJ1</u>
R3	LCD_R3 <u>PH9</u>
R4	LCD_R4 <u>PJ3</u>
R5	LCD_R5 <u>PJ4</u>
R6	LCD_R6 <u>PJ5</u>
R7	LCD_R7 <u>PJ6</u>
G0	LCD_G0 <u>PJ7</u>
G1	LCD_G1 <u>PJ8</u>
G2	LCD_G2 <u>PJ9</u>
G3	LCD_G3 <u>PJ10</u>
G4	LCD_G4 <u>PJ11</u>
G5	LCD_G5 <u>PJ10</u>
G6	LCD_G6 <u>PJ1</u>
G7	LCD_G7 <u>PK2</u>
B0	LCD_B0 <u>PJ12</u>
B1	LCD_B1 <u>PJ13</u>
B2	LCD_B2 <u>PJ14</u>
B3	LCD_B3 <u>PJ15</u>
B4	LCD_B4 <u>PK3</u>
B5	LCD_B5 <u>PK4</u>
B6	LCD_B6 <u>PK5</u>
B7	LCD_B7 <u>PK6</u>

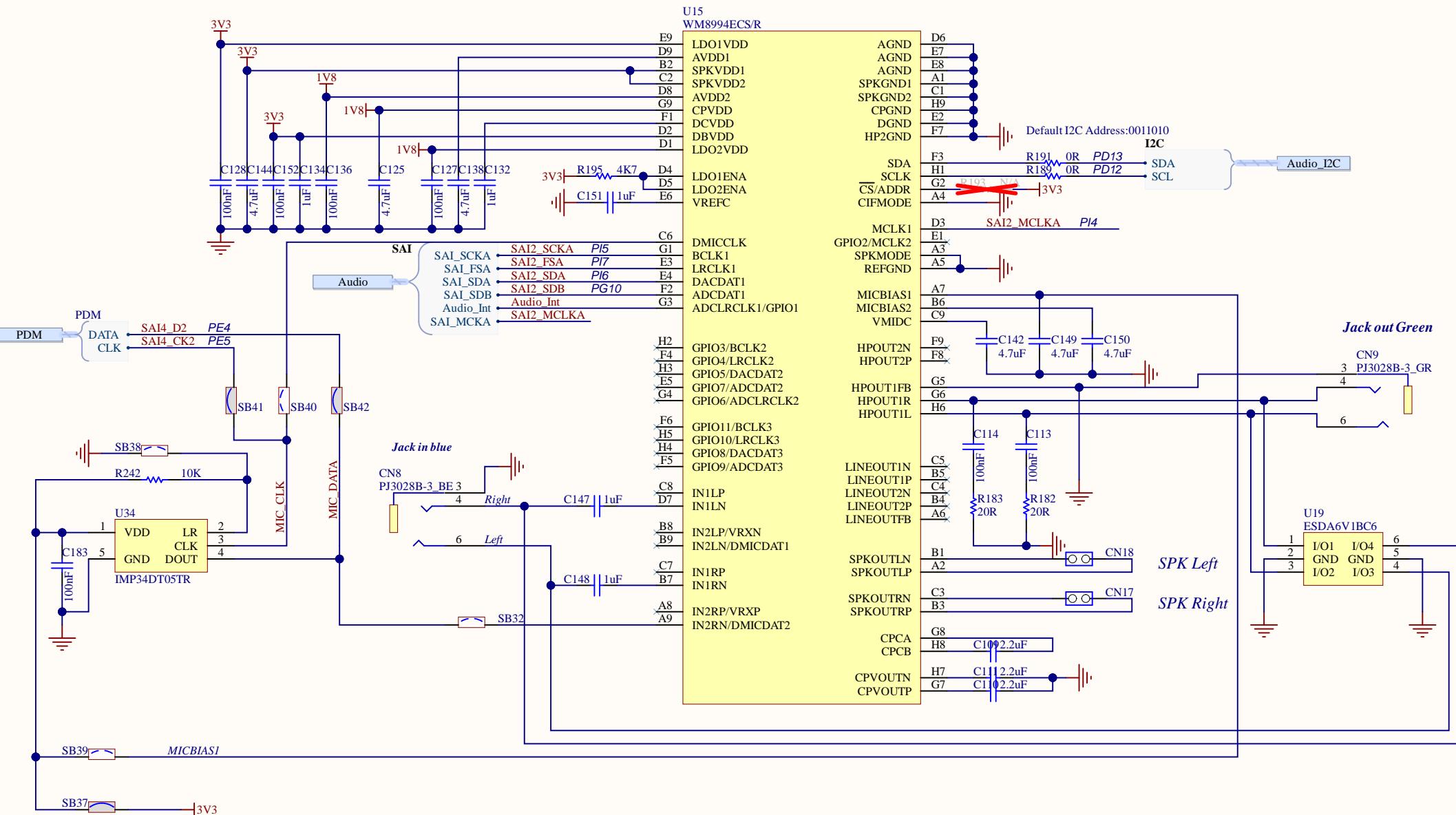
RK043FN78H-CT661C
Default I2C Address : 01110000

Backlight driver & PFC of LCD panel

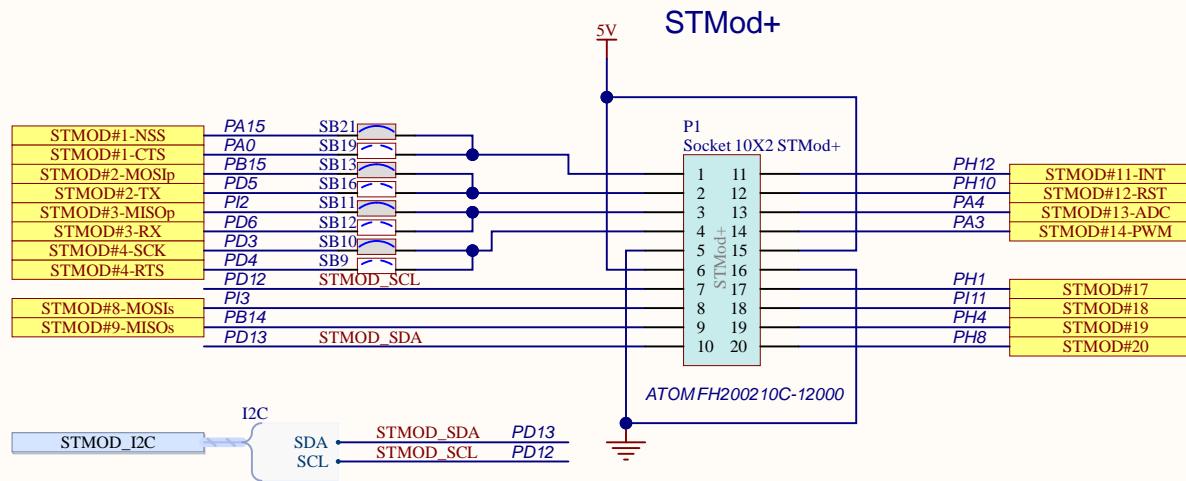


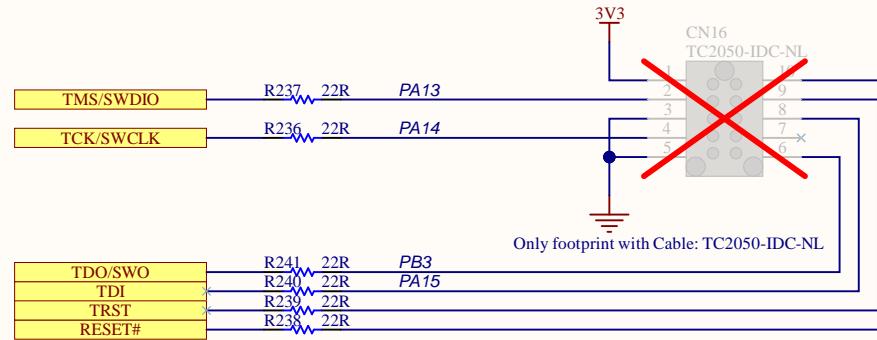
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Project: STM32H7xxx-Disco		
Variant: H750XB		
Revision: B-03		
Size: A4	Date: 02-JUN-22	Reference: MB1381



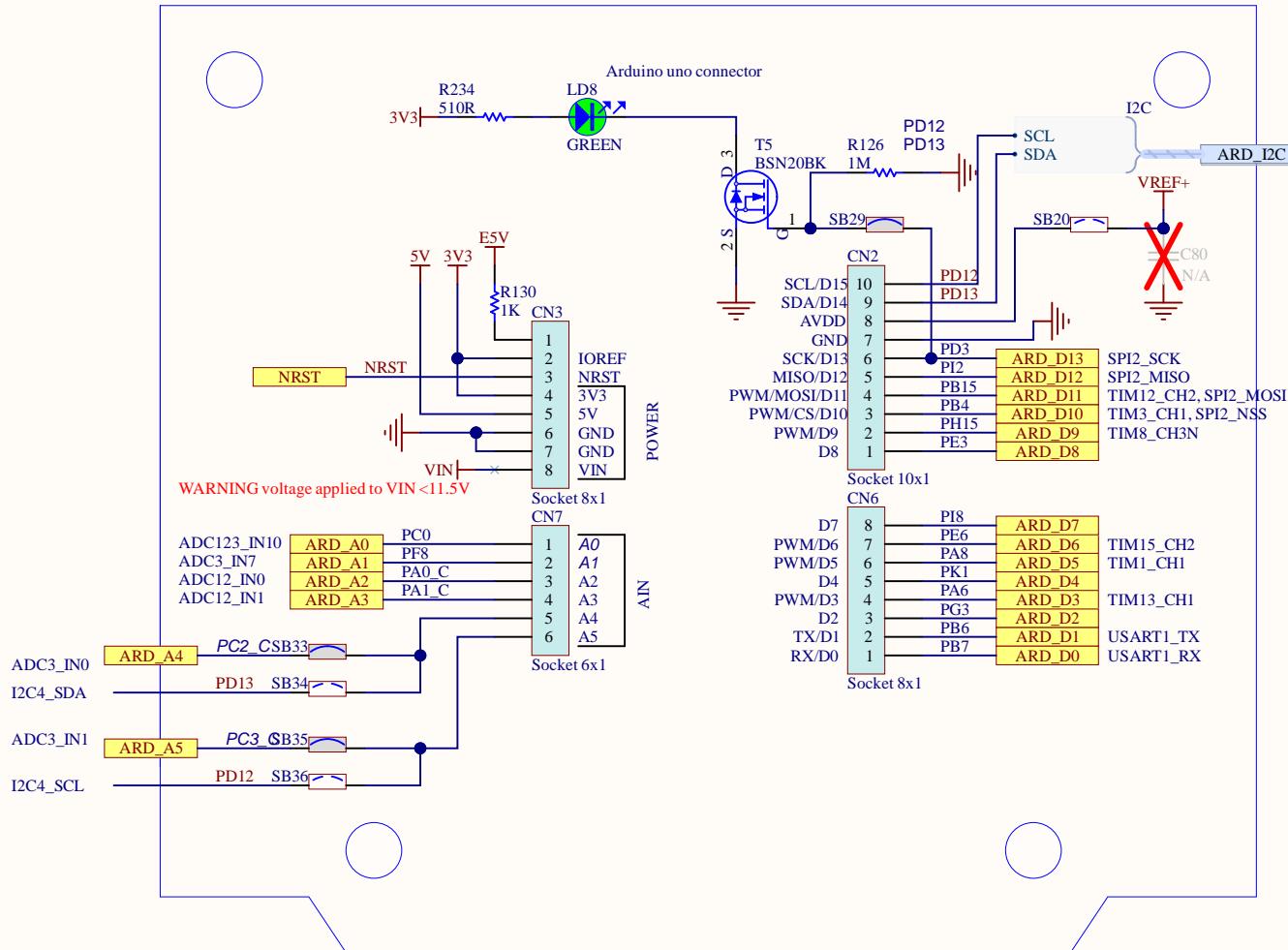


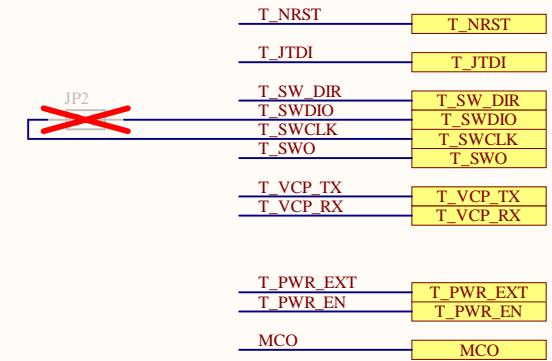
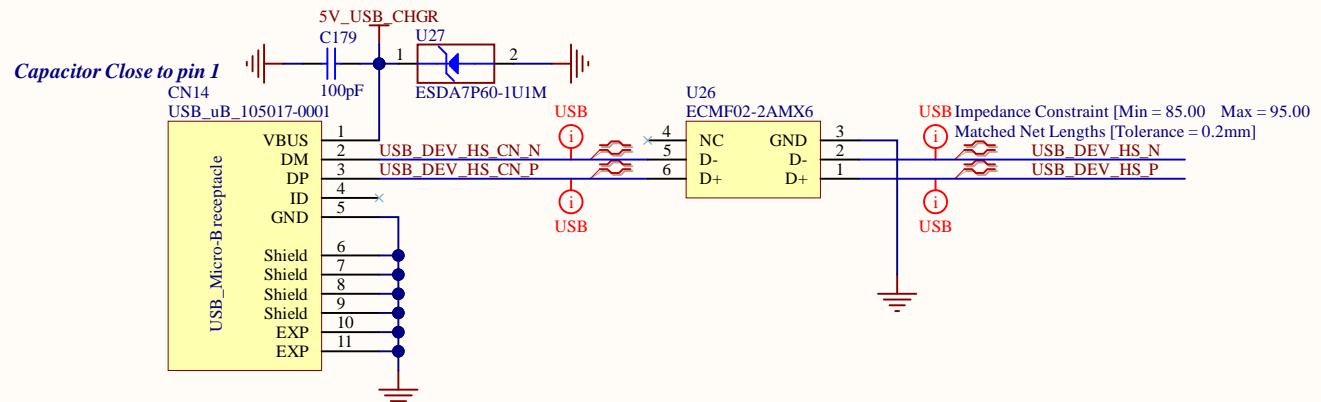
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Project: STM32H7xxx-Disco	
Variant: H750XB	
Revision: B-03	Reference: MB1381
Size: A4	Date: 02-JUN-22
	Sheet: 13 of 19





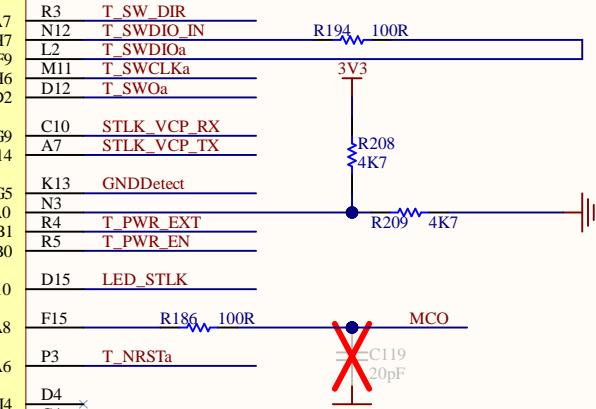
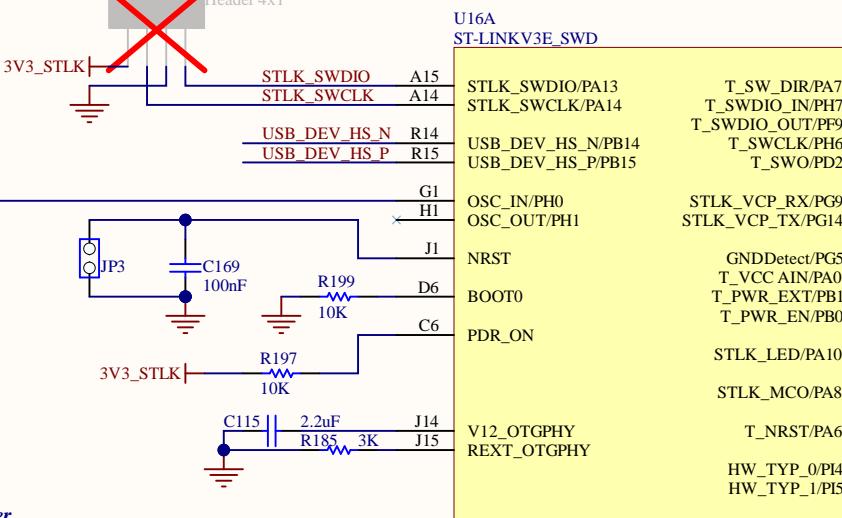
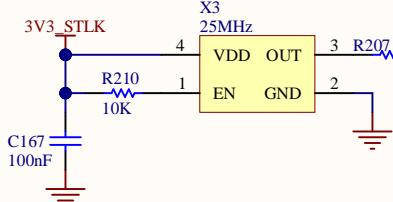
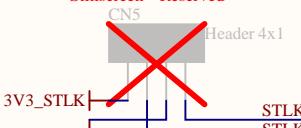
Place this connector close to STDC, they will share the same ESD components





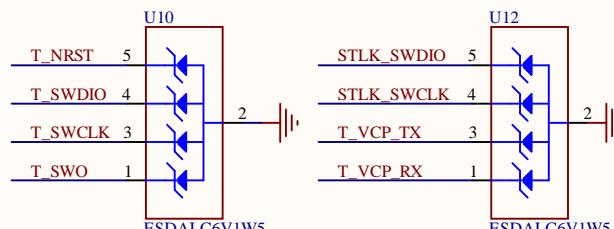
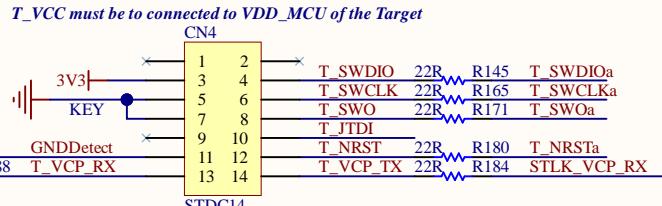
Must be on a border or the PCB.

Silkscreen = Reserved



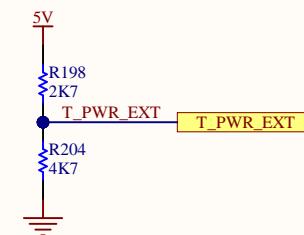
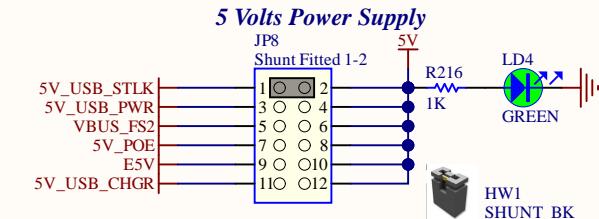
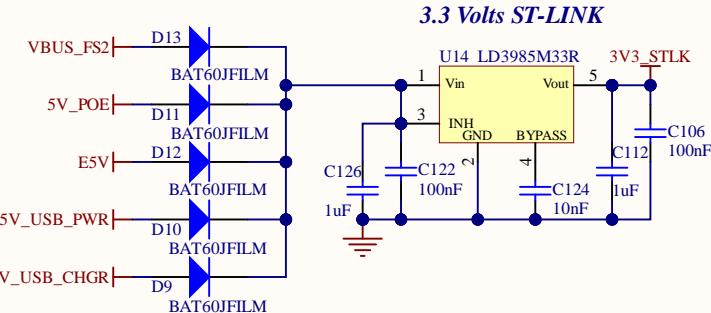
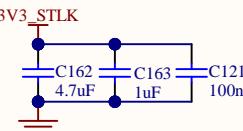
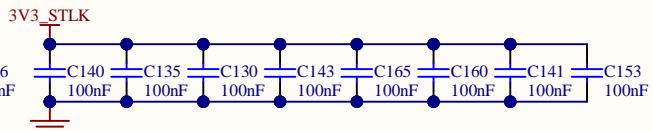
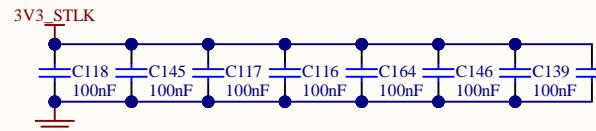
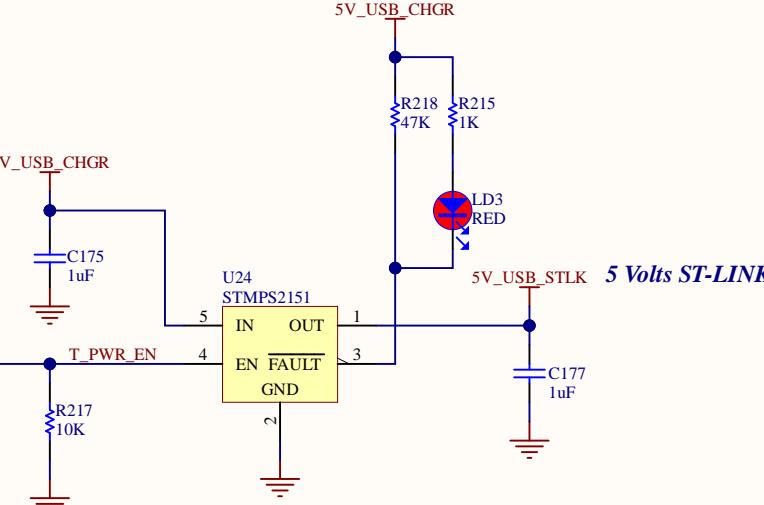
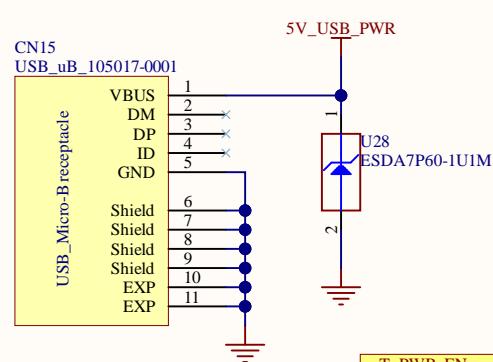
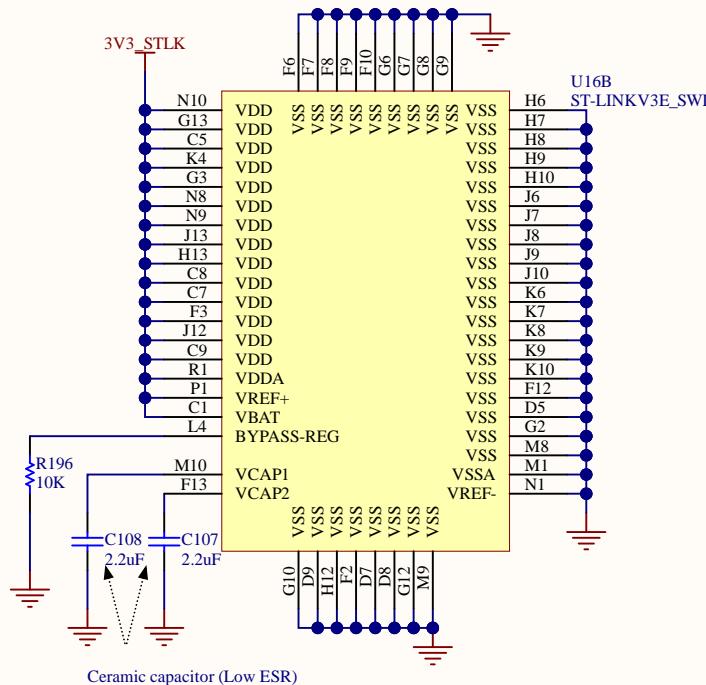
STDC14 Receiver

T_VCC must be connected to VDD MCU of the Target



Title: ST-LINK/V3E-SWD Module	
Project: STM32H7xxx-Disco	
Variant: H750XB	
Revision: B-03	
Size: A4	Date: 02-JUN-22
Reference: MB1381	Sheet: 17 of 19





Title: ST-LINK/V3E-SWD Power Module	
Project: STM32H7xxx-Disco	
Variant: H750XB	
Revision: B-03	Reference: MB1381
Size: A4	Date: 02-JUN-22
	Sheet: 18 of 18



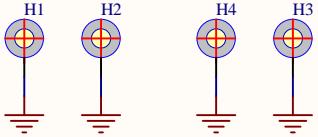
HW Mechanical parts

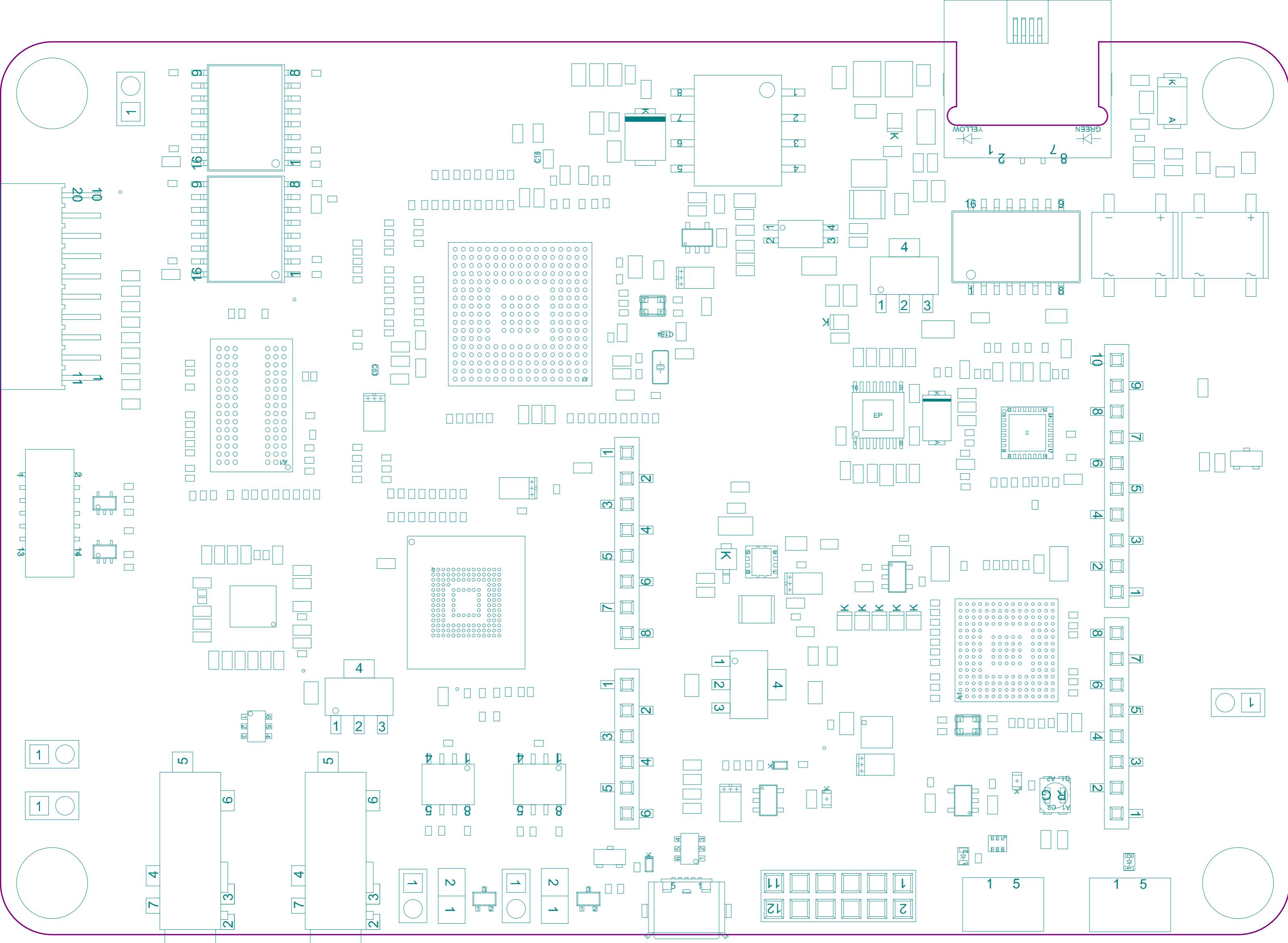
HW6
MB1381A
PCB

HW7
MB1381A-01 XXXX
BOARD REF

HW8
STM32H745I-Disco
BOARD CPN

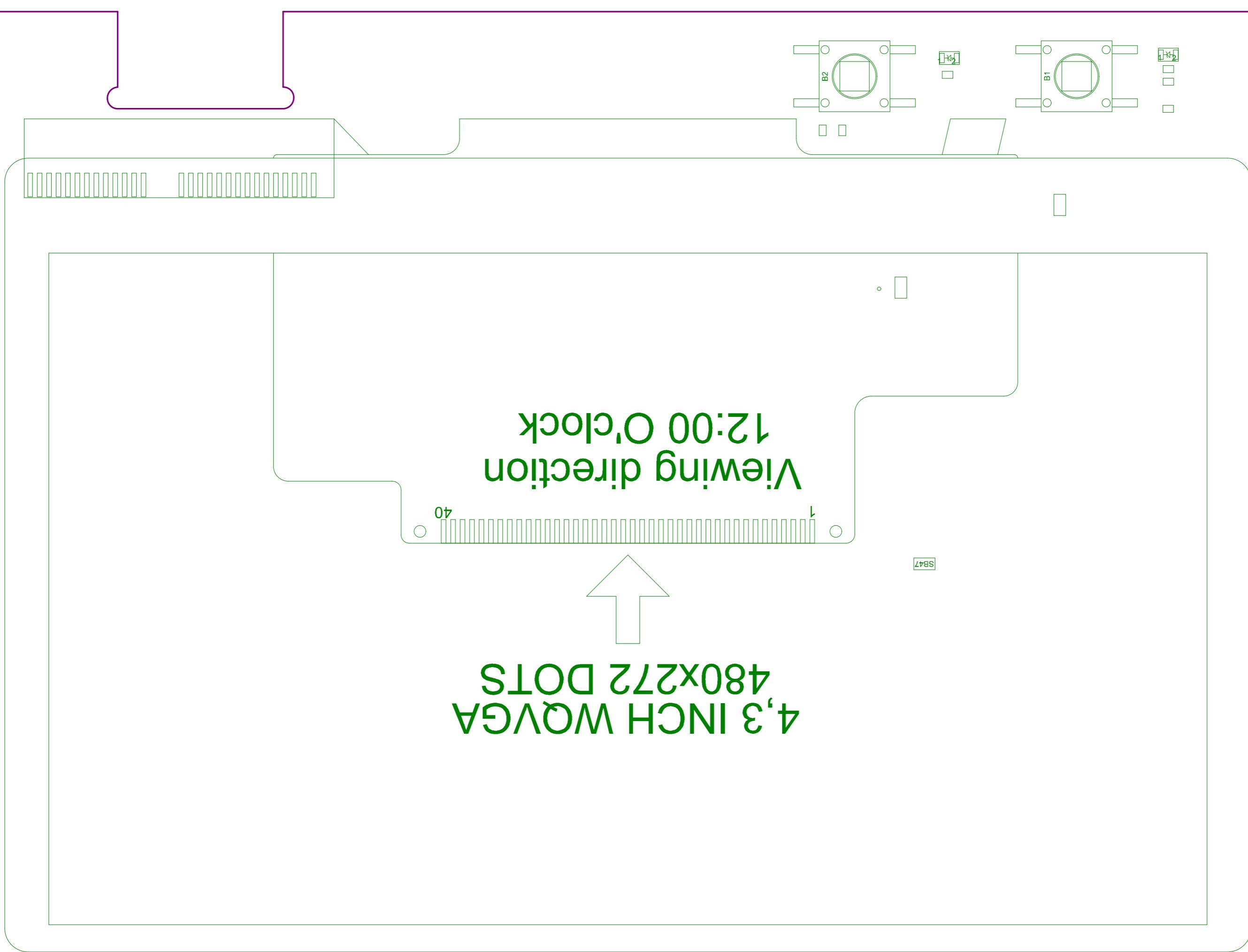
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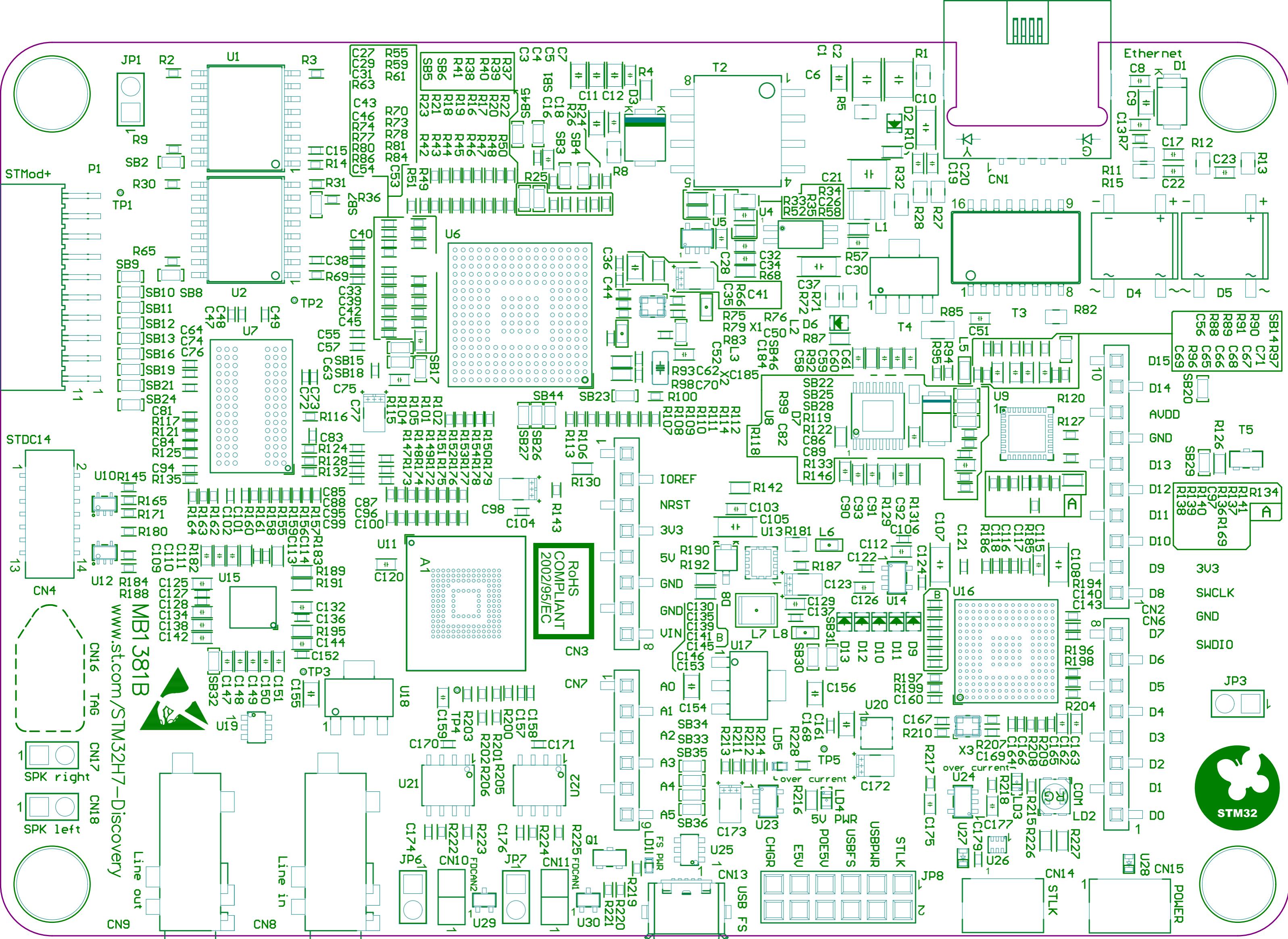




2

LAYER 2 (GND PLANE)





2

LAYER 2 (GND PLANE)

SILKSCREEN (PRIMARY) LEGEND



The diagram illustrates a 480x272 pixel resolution grid. The top section contains the text "Viewing direction" and "12:00 O'clock". Below this, a horizontal line with vertical tick marks spans the width of the grid. A large green arrow points upwards from the center of the grid, indicating the "Viewing direction".

FABRICARMING& DRILLS