Project1 Environment

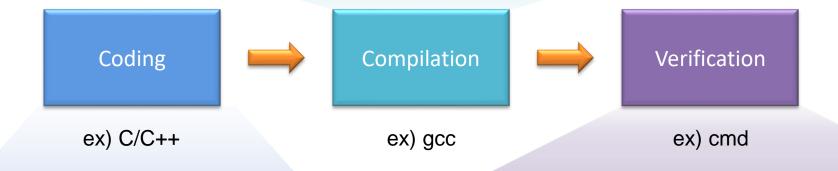








◆ C Simulation Procedure



```
void process(unsigned int cmd, unsigned int inst) {
  printf("\rite your ISS program here\rith");
  exit(0);
  /*cmd 0: A process when a command 's' is entered*/
  /*cmd 1: A process when a command 'r' is entered*/
}
```







♦ Tool Environment

- Linux
- GNU compiler collection (GCC)





◆ IP Address:

- Assigned IP
 - Check the file below
 - Computer_architecture_server.xlsx

♦ ID:

Your student number

◆ Default password:

- **0000**
- Change it as soon as possible.







◆ Server access

- If your computer is connected to network outside KAIST, it has to be connected to the server through KVPN.
 - For more details, visit KVPN.kaist.ac.kr
- Install MobaXterm
 - It provide terminals for various protocols including SSH and SSH file transfer.



- https://mobaxterm.mobatek.net/download-home-edition.html
- Any edition is okay.



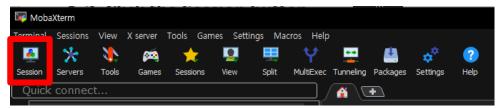




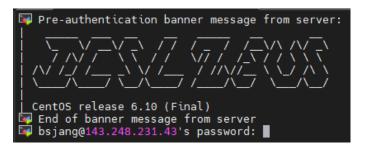
How to access server?

- ◆ 1. Execute MobaXterm
- ◆ 2. Click the Session button
- ◆ 3.Session Setting → OK
- ◆ 4. Log in (init PW : 0000)















- ◆ You can do the simulations on your own server.
- **◆** But we will compile and evaluate your codes on the notified server.
 - Make sure your compiled output works well on the notified server.
 - Claims for errors due to environmental differences will not be accepted.
- **◆** Make a directory for your code submission on the server.
 - ~/project1_submission
 - You must also upload your zip file on KLMS.
 - You can create a directory you want.







♦ krpassembler

- Assembler for KRP 2.0
- Translate assembly language to machine language format



- Input files:
 - You can make your own assembly language.
 - test_codes (provided)
 - example, test1, and test2
- Output files:
 - output_log
 - Log of assembly language
 - vectors.bin → input file of ISS
 - Binary file of instructions







Assembly language

◆ Execution example

```
ADDI r1, r2, #0x10000

ADDI r1, r2, SHL(#0x200, #0x10)

ANDI r1, r2, #2

ANDI r1, r2, LSR(#4, #0x8)
```

```
1. ADD| type 1 → Instruction type
ra: 1, rb: 2, imm: 65536 → Register and immediate information
insn: 00450000 → Instruction
2. ADD| type 2
ra: 1, rb: 2, imm: 512, mode: 0, shamt: 16
insn: 08450010
```

This is for your easy debugging





◆ Execution example

```
sckim@dell2:~/assembler$ |s
kass test_vectors
sckim@dell2:~/assembler$ ./kass ./test_vectors/example
sckim@dell2:~/assembler$ |s
kass output_log test_vectors vectors.bin Output files
sckim@dell2:~/assembler$ |
```

Address₍₁₆₎

```
@^@E^@^P^@E^H^B^@D (^BE
-
-
-:%!xxd
```

Can be translated in hex format by vim editor

```
00000000: 0000 4500 1000 4508 0200 4420 2802 4428 00000010: 0b00 9612 4414 961a 0b00 4032 e706 003a 00000020: 0030 4440 0040 8648 0000 015b 0070 8055 00000030: 0030 4468 00c0 9662 0060 8770 0400 4480 00000040: 2030 4480 0400 4478 2030 4478 0400 4488 00000050: 2030 4488 0400 4490 2030 4490 0100 0298 00000060: 0260 0898 0100 44a0 04b0 0ca1 0000 20a8 00000070: 1400 40b0 e803 7eb8 0800 e2b8 0c00 40c0 00000080: 0400 7ec8 0400 88c8 2800 40d0 1400 40d8 00000090: 0000 00e8 0000 00f0 0000 00f8 0a
```

This is for ISS input_file

Stored in little-endian

Instruction: 08450010

Instructions₍₁₆₎







ISS EXECUTION

◆ Example

or

Insert a command \$./run.sh







♦ test_codes

test1

```
r0, ROR(#-9, #4)
 r1, #90
 r2, #15
 r4, SHL(#2, #1)
 r5, ASR(#-8, #1)
 r6, #-12
 r7, #-100
 r9, #-33
 r11, r8, #-22
r12, r4, r5
                      12 = 00_00_00_08 8
r14, r9, r7
r15, r5<u>,</u> r6
```

Assembly language

Expected result







- **♦** test_codes
 - test2

```
r0, #0
  VI r1, #131071
                        @r1 = FF_FF_FF_FF, -1
                        @r2 = 00_00_00_FF, 255
@r3 = FF_FF_FF_00, -256
   r2, r2, #255
    r3, r2
                        @r4 = FF_00_00_00, -16777216
SHL r4, r3, #16
                        @r5 = 3F_C0_00_0, 1069547520
 SR r5, r4, #2
                        @r6 = FF_C0_00_00, -4194304
@r7 = 00_3F_FF_FF, 4194303
ASR r6, r4, #2
XOR r7, r6, r1
                               00_3F_FF_00, 4194048
AND r8, r3, r7
                                   FF FF 00, -25<mark>6</mark>
  r9, r3, r8
```

Assembly language

Expected result



