

```
# KERNEL: ----- TEST COMPLETE -----
# RUNTIME: Info: RUNTIME_0068 testbench.sv (146): $finish called.
# KERNEL: Time: 685 ns, Iteration: 0, Instance: /tb, Process: @INITIAL#61_3@.
# KERNEL: stopped at time: 685 ns
# VSIM: Simulation has finished. There are no more test vectors to simulate.
# VSIM: Simulation has finished.
Finding VCD file...
./wave.vcd
```
