



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
HYDERABAD - 500 085, TELANGANA STATE, INDIA.

**CONSOLIDATED MARKS MEMO / CREDIT SHEET**

B.Tech. ELECTRICAL & ELECTRONICS ENGINEERING

CMM. No.: **C0865334**

Serial No.: 21277027718

Name: **JANDHYALA SRAVANA KALYANI**

Hall Ticket No.: **14WH1A0215**

Year of Admission: 2014-2015

Name of the College: WH-BVRIT, BACHUPALLY

Month & Year of Final Exam: April, 2018

Class Awarded: **FIRST CLASS WITH DISTINCTION**



S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory						Maximum Marks in Lab				
		25	75	100				25	50	75	
I YEAR											
1	ENGLISH	21	48	69	4	2	MATHEMATICS - I	21	35	56	6
3	MATHEMATICAL METHODS	23	44	67	6	4	ENGINEERING PHYSICS	24	54	78	6
5	ENGINEERING CHEMISTRY	22	58	80	6	6	COMPUTER PROGRAMMING	25	62	87	6
7	ENGINEERING DRAWING	25	75	100	6	8	COMPUTER PROGRAMMING LAB.	25	50	75	4
9	ENGINEERING PHYSICS & ENGINEERING CHEMISTRY LA	25	50	75	4	10	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB.	22	42	64	4
11	ENGINEERING WORKSHOP / IT WORKSHOP	24	48	72	4						
I SEMESTER II YEAR II SEMESTER											
1	MATHEMATICS - III	21	56	77	4	1	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	20	35	55	4
2	FLUID MECHANICS AND HYDRAULIC MACHINERY	22	51	73	4	2	POWER SYSTEMS - I	24	44	68	4
3	ELECTRONIC DEVICES & CIRCUITS	21	54	75	4	3	ELECTRONIC CIRCUITS	20	48	68	4
4	ELECTRICAL CIRCUITS	23	58	81	4	4	SWITCHING THEORY AND LOGIC DESIGN	20	54	74	4
5	ELECTROMAGNETIC FIELDS	23	32	55	4	5	NETWORK THEORY	24	47	71	4
6	ELECTRICAL MACHINES - I	22	52	74	4	6	ELECTRICAL MACHINES - II	24	61	85	4
7	FLUID MECHANICS AND HYDRAULIC MACHINERY LAB	25	46	71	2	7	ELECTRICAL MACHINES LAB - I	25	50	75	2
8	ELECTRONIC DEVICES AND CIRCUITS LAB	25	50	75	2	8	ELECTRICAL CIRCUITS AND SIMULATION LAB	25	50	75	2
						9	GENDER SENSITIZATION* ^	24	46	70	2
I SEMESTER III YEAR II SEMESTER											
1	IC APPLICATIONS	23	56	79	4	1	ELECTRICAL AND ELECTRONICS INSTRUMENTATION	23	59	82	4
2	MANAGEMENT SCIENCE	23	26	49*	4	2	STATIC DRIVES	25	38	63	4
3	POWER SYSTEMS - II	24	61	85	4	3	COMPUTER METHODS IN POWER SYSTEMS	22	35	57	4
4	CONTROL SYSTEMS	22	64	86	4	4	MICROPROCESSORS AND INTERFACING DEVICES	23	49	72	4
5	POWER ELECTRONICS	25	43	68	4	5	ENVIRONMENTAL STUDIES	22	37	59	4
6	ELECTRICAL MACHINES - III	22	26	48*	4	6	HUMAN VALUES AND PROFESSIONAL ETHICS	22	47	69	4
7	ELECTRICAL MACHINES LAB - II	25	49	74	2	7	CONTROL SYSTEMS AND SIMULATION LAB	25	50	75	2
8	ADVANCED COMMUNICATION SKILLS LAB	24	48	72	2	8	POWER ELECTRONICS AND SIMULATION LAB	25	50	75	2
I SEMESTER IV YEAR II SEMESTER											
1	SWITCH GEAR AND PROTECTION	22	46	68	4	1	FUNDAMENTALS OF HVDC AND FACTS DEVICES	19	40	59	4
2	UTILIZATION OF ELECTRICAL ENERGY	19	33	52	4	2	RENEWABLE ENERGY SOURCES	25	38	63	4
3	DIGITAL SIGNAL PROCESSING	22	58	80	4	3	ADVANCED CONTROL SYSTEMS	19	34	53	4
4	POWER SYSTEM OPERATION AND CONTROL	23	45	68	4	4	INDUSTRY ORIENTED MINI PROJECT	-	49	49	2
5	HIGH VOLTAGE ENGINEERING	21	43	64	4	5	SEMINAR	50	-	50	2
6	ELECTRICAL DISTRIBUTION SYSTEMS	17	36	53	4	6	PROJECT WORK	49	145	194	10
7	MICROPROCESSORS AND INTERFACING DEVICES LAB	23	48	71	2	7	COMPREHENSIVE VIVA-VOCE	-	98	98	2
8	ELECTRICAL MEASUREMENTS LAB	25	48	73	2						
(# Project Internal=50, External=150)											

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Number of Credits registered for : 226 Aggregate Marks Secured for best: 218

Aggregate Marks Secured : 4086 OUT OF 5250 (77.83%)

Date of Issue : 29 May, 2018

(see overleaf for Rules concerned to award of class)

(^ Credits considered for award of degree)

(\*Courses registered but not counted for calculation of aggregate)

A indicates 'ABSENT'



*S. Tara kalyani*

**CONTROLLER OF EXAMINATIONS**