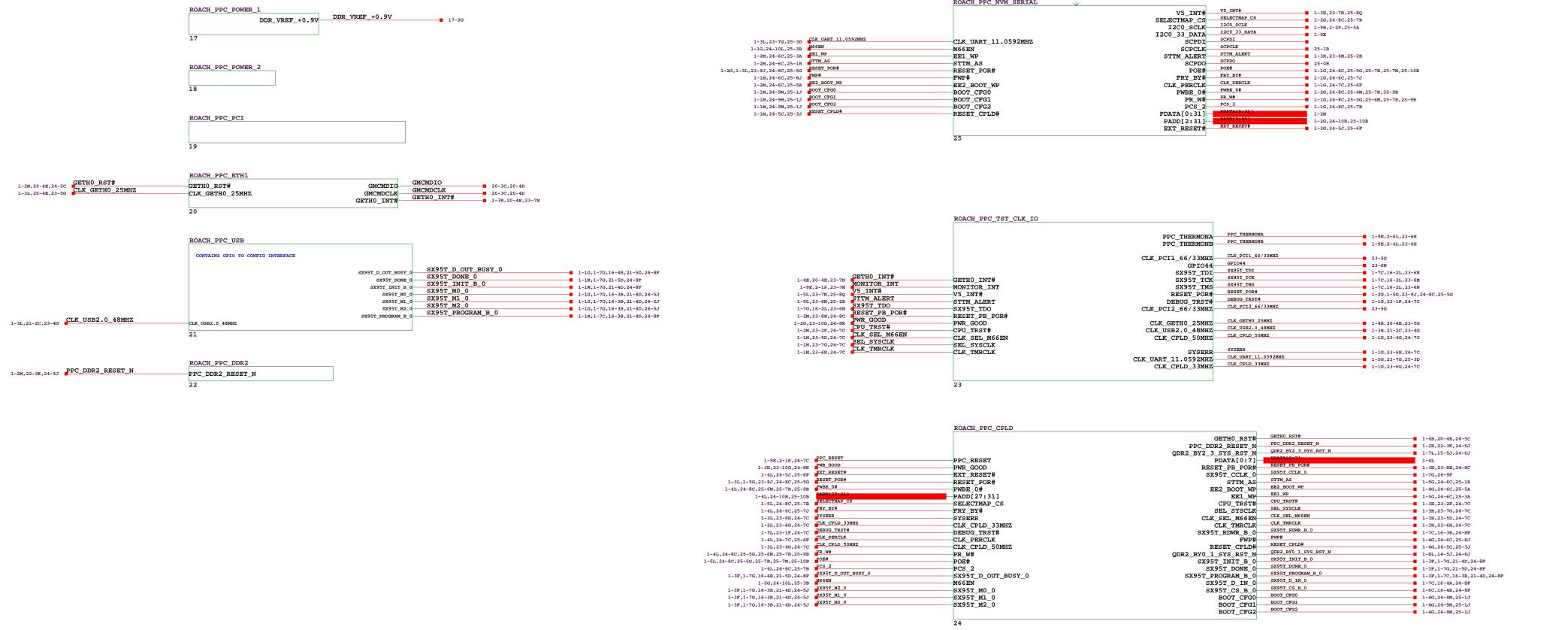
VIRTEX5 ______ ROACH_MONITOR PPC_RESET ROACH_V5_CLK ROACH_TRANSCEIVERS2_3 SX95T_DXN PPC_THERMONB ATRINI PPC_THERMONE MONITOR_INT I2C0_33_DATA PS_ON# PS_ON# SX95T_DXP 1-3L,2-4L,23-6H ROACH_DDR2_DIMM 1-3H,2-1P,23-7M 1-5L,2-2P,25-5A 1-8E,2-2M,3-10B 1-8E,2-1M,3-9B 1-8E,2-1M,3-9B 3V3 ATY I2C0_SCLK FAN2_SENSE FAN1_SENSE 1-8B,2-8B,3-8G FAN3_SENSE 1-8E,2-3A,3-6C 1-8E,2-2B,3-2H 11V8 - 3V3_ATX - 12V5 - 11V8 - 11V5 - 11V2 FAN1_CONTROL LOAD_RES_OFF 1-8B,2-3M,3-10B 1-8B,2-6B,3-5B 2V5_TRACK 2V5_INHIBIT 2N3_CONTRACK 1-8E,2-2B,3-2R 1-8B,2-6B,3-3B VIRTEX5 CLOCKS 1-8E,2-2B,3-5R ZV5_INHIBIT FAN3_CONTROL FAN2_CONTROL MGT_AVTTX_EN MGT_AVCCPLL_EN MGT_AVCC_EN LV8_INHIBIT MGT_AVCC_EN LV8_INHIBIT MGT_AVCC_EN LV8_INHIBIT MGT_AVCC_EN LV8_INHIBIT MGT_AVCC_EN LV8_INHIBIT 1-8E,2-2B,3-8R 1-8B,2-2M,3-9B 1-8E, 2-2B, 3-8R 1-8E, 2-1B, 3-10R 1-8E, 2-4A, 3-7C 1-90, 2-0B, 10-7P 1-90, 2-0B, 10-4G 1-80, 2-0B, 10-7G 1-8E, 2-8B, 3-7C 1-8E, 2-6B, 3-6C 1-8E, 2-6B, 3-6C 1-8B,2-2M,3-10B 1-8L,2-1M,10-5B ROACH_TRANSCEIVERS_PSU 1-9E,2-1M,10-8K 1-9E,2-1M,10-8B 1-9E,2-1M,10-5B MGT_AVCC_PG MGT_AVTTX_PG MGT_AVTTX_PG MGT_AVCCPLL_PG MGT_AVCCPLL_PG MGT_AVCC_PG MGT_AVTTX_PG MGT_AVCCPLL_PG 1-9L,2-1M,10-8B MGT_AVCC_EN 1-9B,2-0B,10-7P ROACH_ADC_0 1-9B,2-0B,10-4G 1V8_INHIBIT 1V8_INHIBIT V5 DDR2 MEMORY MGT_AVTTX_EN MGT_AVCCPLL_PG 1V5_INHIBIT 1V2_INHIBIT 1V2_INHIBIT 1-8B,2-1B,3-1J 1-8B,2-0B,10-7G PWR_OK -12V_ATX 1-8B,2-1B,3-4J 1-8B,2-1B,3-6J 1V0_INHIBIT 1V0_INHIBIT 1-8B,2-0B,3-9J -----______ GIGABIT TRANSCEIVERS ROACH_PSU ROACH_DIFF_GPIO 1-9E,2-6B,3-5B 1-9E,2-3M,3-10B 1-9E,2-2M,3-10B 1-9E,2-2M,3-0B 1-9E,2-2M,3-0B 1-8E,2-1B,3-6J 1-8E,2-1B,3-4J 1-8E,2-1B,3-1J 1-9E,2-6B,3-3B 1-9E,2-1B,3-1B 12V_ATX 3V3_ATX 3V3_ATX 5V_ATX 5V_ATX PWR_OK 11V2 11V2 FAN1_SENSE FAN2_SENSE FAN3_SENSE FAN3_SENSE 11V0 11V5 11V8 11V8 12V5 LOAD_RES_OFF FAN1_CONTROL PS_ON# 1-8B,2-6B,3-60 FAN2_CONTROL FAN3_CONTROL 1-8B,2-8B,3-7C 1-9B,2-2B,3-8R ROACH_ADC_1 ROACH_QDR2P_BY0_3 QDR2_TDI QDR2_TCK QDR2_TMS QDR2_BY0_1_SYS_RST_N 1-9B,2-2M,3-10B QDR2_TDI QDR2_TCK QDR2_BY0_1_TDO 1-9B,2-1M,3-9B 1V8_INHIBIT 2V5_TRACK 1-9B,2-2B,3-5R QDR2_TMS QDR2_BY0_1_SYS_RST_N 2V5_INHIBIT 1-9B,2-2B,3-2R TBD: QDRII+ JTAG?? V5 DIFFERENTIAL GPIO ROACH_QDR2P_BY2_ QDR2_BY0_1_TDO QDR2_TMS QDR2_TCK QDR2_BY2_3_SYS_RST_N QDR2_BY2_3_TDO POWER SUPPLY AND MONITORING ZDOK/ADC INTERFACE ------ROACH_GPIO_MISC ROACH_5V_POWER 1-1G,1-3F,16-3B,21-4D,24-5J 1-1G,1-3F,16-3B,21-4D,24-5J QDRII+ MEMORY SX95T_M0_0 1-1M,16-4A,24-8F 1-3L,16-2L,23-6N 1-3L,16-2L,23-6N 1-3L,16-2L,23-6N 1-1M,16-3B,24-48F 1-1M,1-3F,16-3B,21-4D,24-8F 1-1M,16-4B,24-9F 1-1M,16-4B,24-9F | SX95T_DONE_0 | SX95T_DONE_0 | SX95T_CCLK_0 | SX95T_CCLK_0 | SX95T_D_OUT_BUSY_0 | SX95T_INIT_B_0 | SX95T_INIT_B_0 | SX95T_DONE_0 SX95T_TCK 1-1M,1-3F,21-5D,24-8F SX95T_TMS SX95T_TDI 1-2M,24-9F 1-1G,1-3F,16-4B,21-5D,24-8F SX95T_RDWR_B_0 SX95T_PROGRAM_B_0 1-1M,1-3F,21-4D,24-8F SX95T_CS_B_0 VIRTEX5 CONFIGURATION V5 SE GPIO & MISCELLANEOUS V5 POWER PPC ROACH_PPC_NVM_SERIAL ROACH_PPC_POWER_1 1-3H,23-7N,25-8Q 1-2G,24-8C,25-7B DDR_VREF_+0.9V 1-9B,2-2P,25-5A 1-3L,23-7G,25-3D CLK_UART_11.0592MHZ CLK_UART_11.0592MHZ 1-1G,24-10L,25-3B M66EN 1-2M,24-6C,25-3A EE1_WP 1-2M,24-6C,25-1B STTM_AS M66EN EE1_WP 25-1B | SCPCLK | SCPCLK | STTM_ALERT | SCPDO | SCPDO | POE# | FRY_BY# | CLK_PERCLK | PWBE_0# | PR_W# | PCS_2 | PDATA[0.31] | PDATA[0.3 1-3H,23-6M,25-2B STTM AS ■ 25-0B ROACH_PPC_POWER_2 1-2M,24-6C,25-1B 1-2G,1-3L,23-9J,24-8C,25-5G 1-1M,24-6C,25-5J 1-2M,24-6C,25-5J 1-1M,24-9M,25-1J 1-1M,24-9M,25-1J 1-1M,24-9M,25-1J 1-1M,24-9M,25-1J RESET_POR# 1-1G,24-8C,25-5G,25-7B,25-7M,25-10B FWP# EE2_BOOT_WP BOOT_CFG0 BOOT_CFG1 BOOT_CFG2 1-1G,24-7C,25-6F 1-2G,24-8C,25-6M,25-7B,25-9B 1-1G,24-8C,25-5G,25-6M,25-7B,25-9B 1-1M,24-9M,25-1J 1-1M,24-5C,25-3J RESET_CPLD# 1-1G,24-8C,25-7B PDATA[0:31] RESET_CPLD# 1-2M 1-2G,24-10B,25-10B ROACH_PPC_PCI 1-2G,24-5J,25-6F ROACH_PPC_ETH1 GMCMDIO GMCMDCLK GETH0_INT# GETHO_RST# GMCMDIO 20-3C,20-4D GMCMDCLK GETH0_INT# 20-3C,20-4D 1-3H,20-4E,23-7N CLK_GETH0_25MHZ

SX95T_M0_0 SX95T_M1_0

SX95T_M2_0

1-6C,16-4B,24-9F 1-4G,24-9M,25-1J

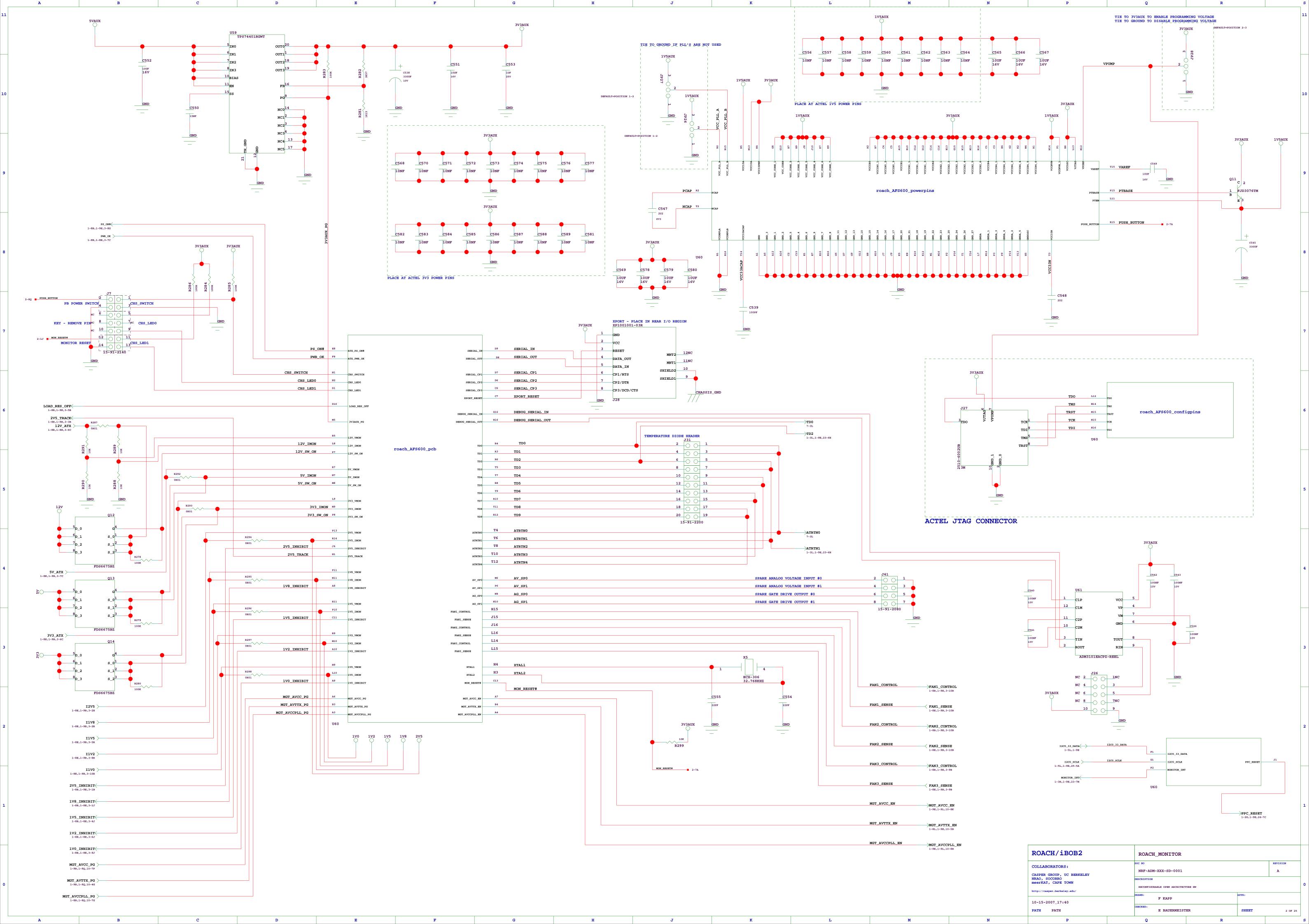
1-4G,24-9M,25-1J

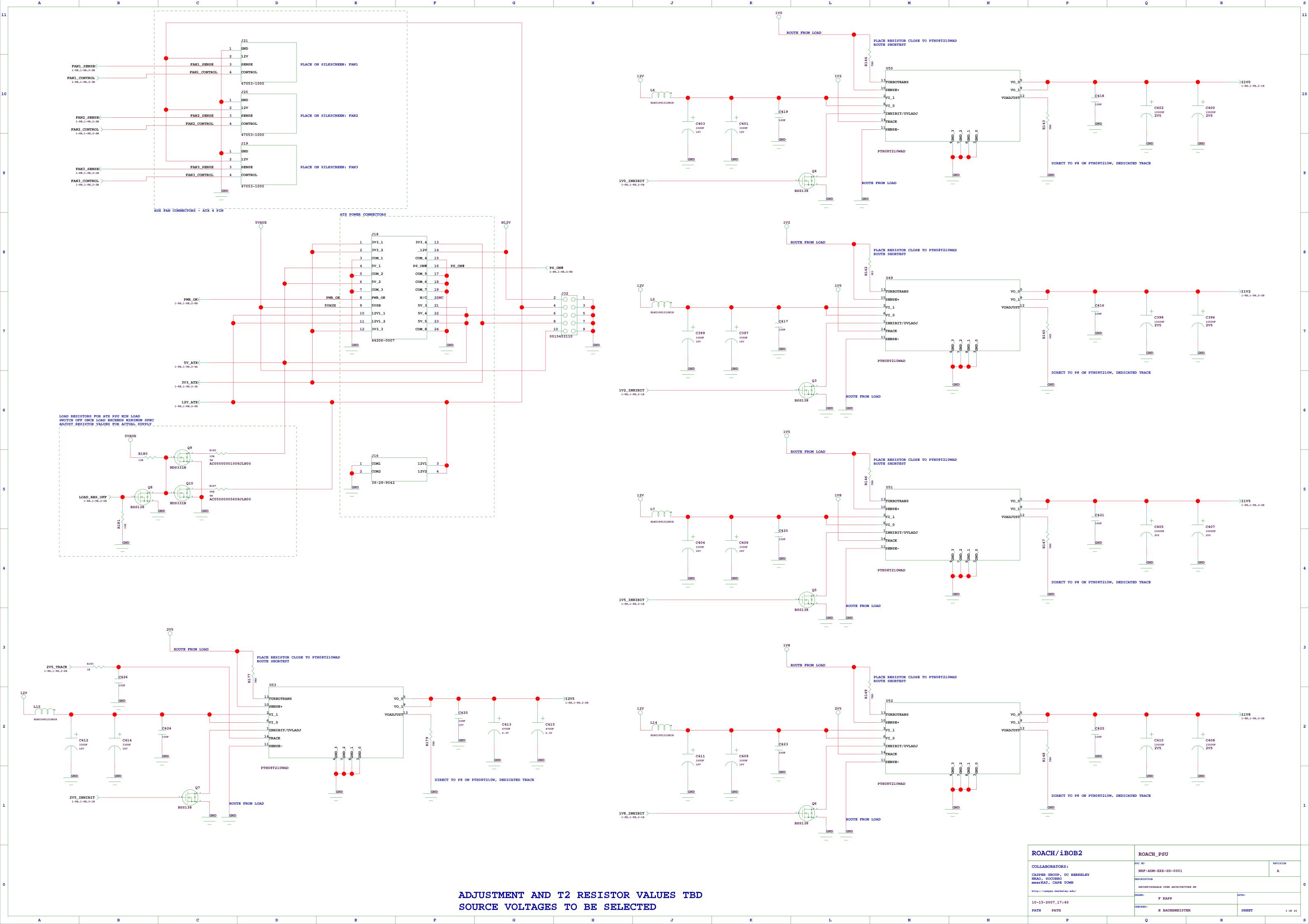


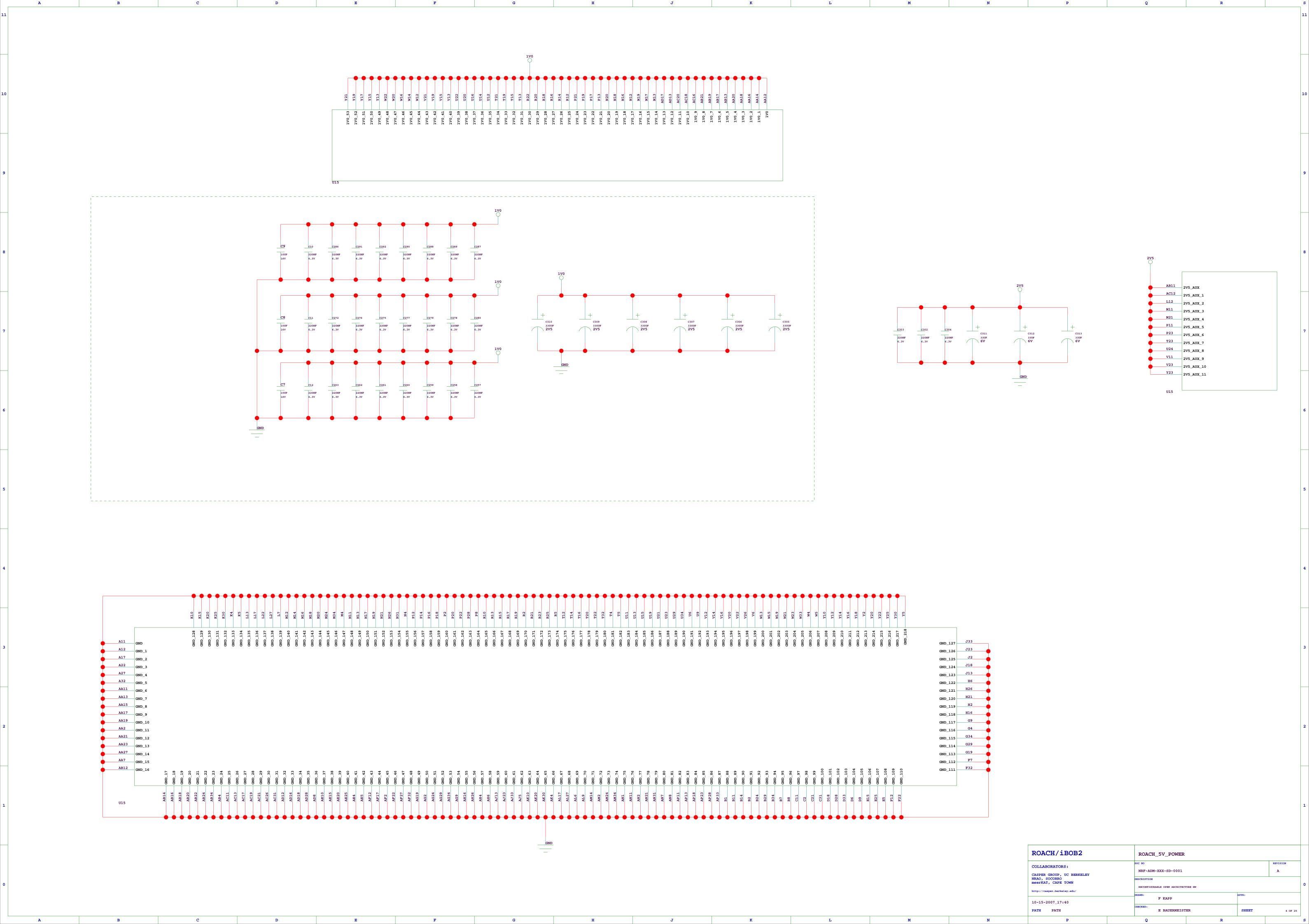
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CONTRIBUTORS ETIENNE BAUERMEISTER HENRY CHEN STEVE DURAND FRANCOIS KAPP ALAN LANGMAN GEORGE PECK MIKE REVNELL HAYDEN SO DAN WERTHIMER

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COLLABORATORS:	DOC NO		REVISION	
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NRAO, SOCORRO meerKAT, CAPE TOWN	DESCRIPTION			0
http://casper.berkeley.edu/	RECONFIGURABLE OPEN ARCHITECTURE HW			
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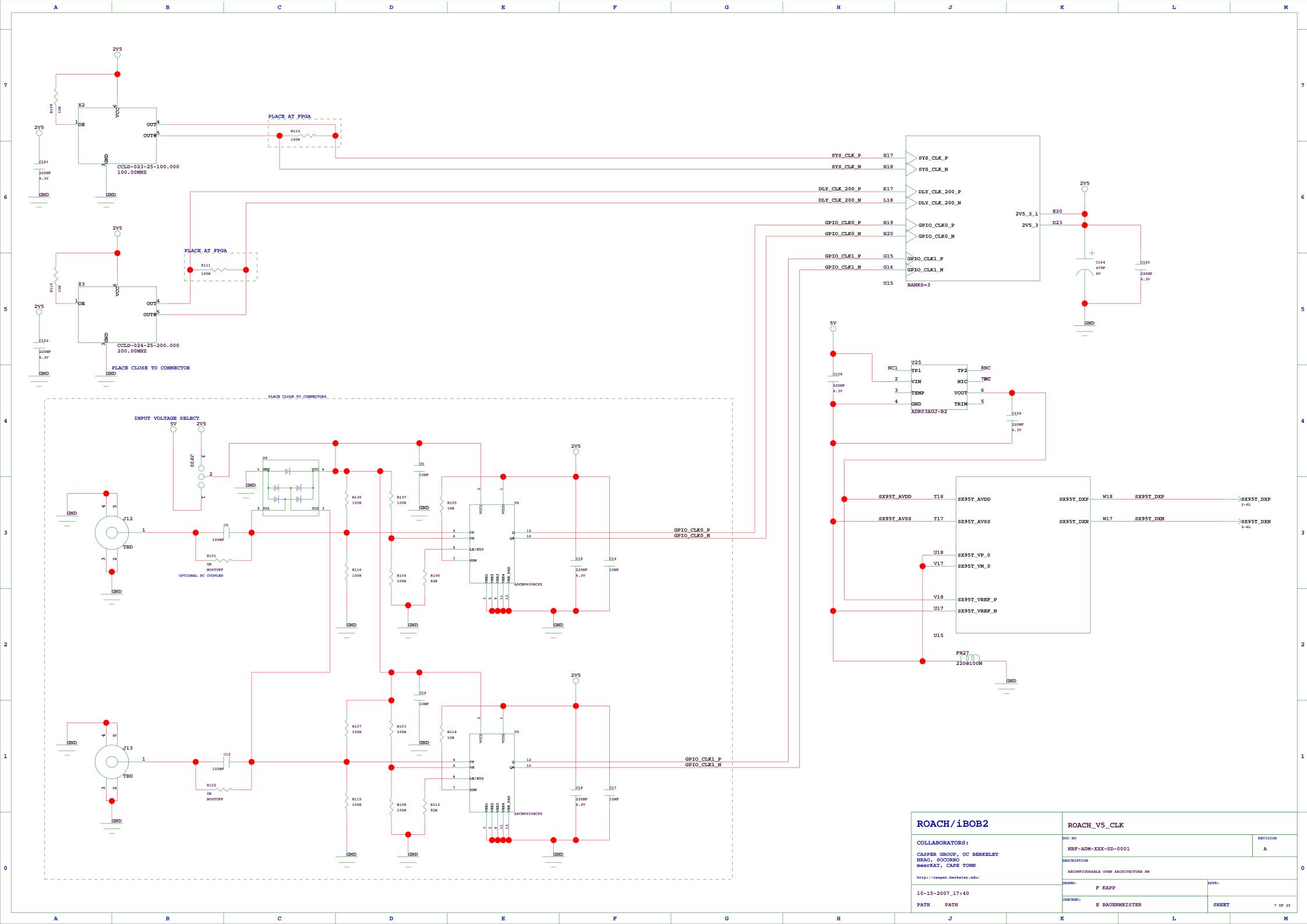


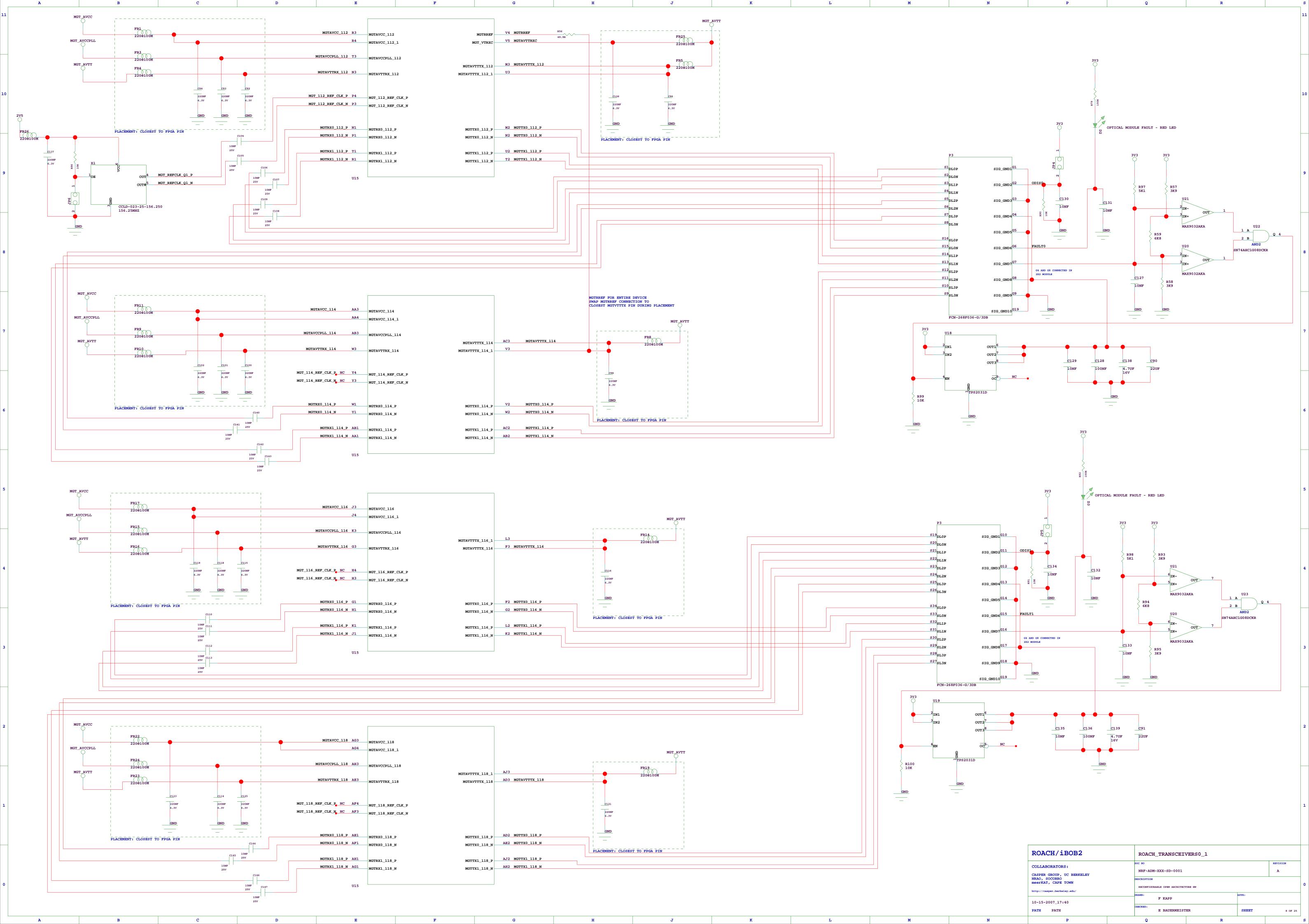


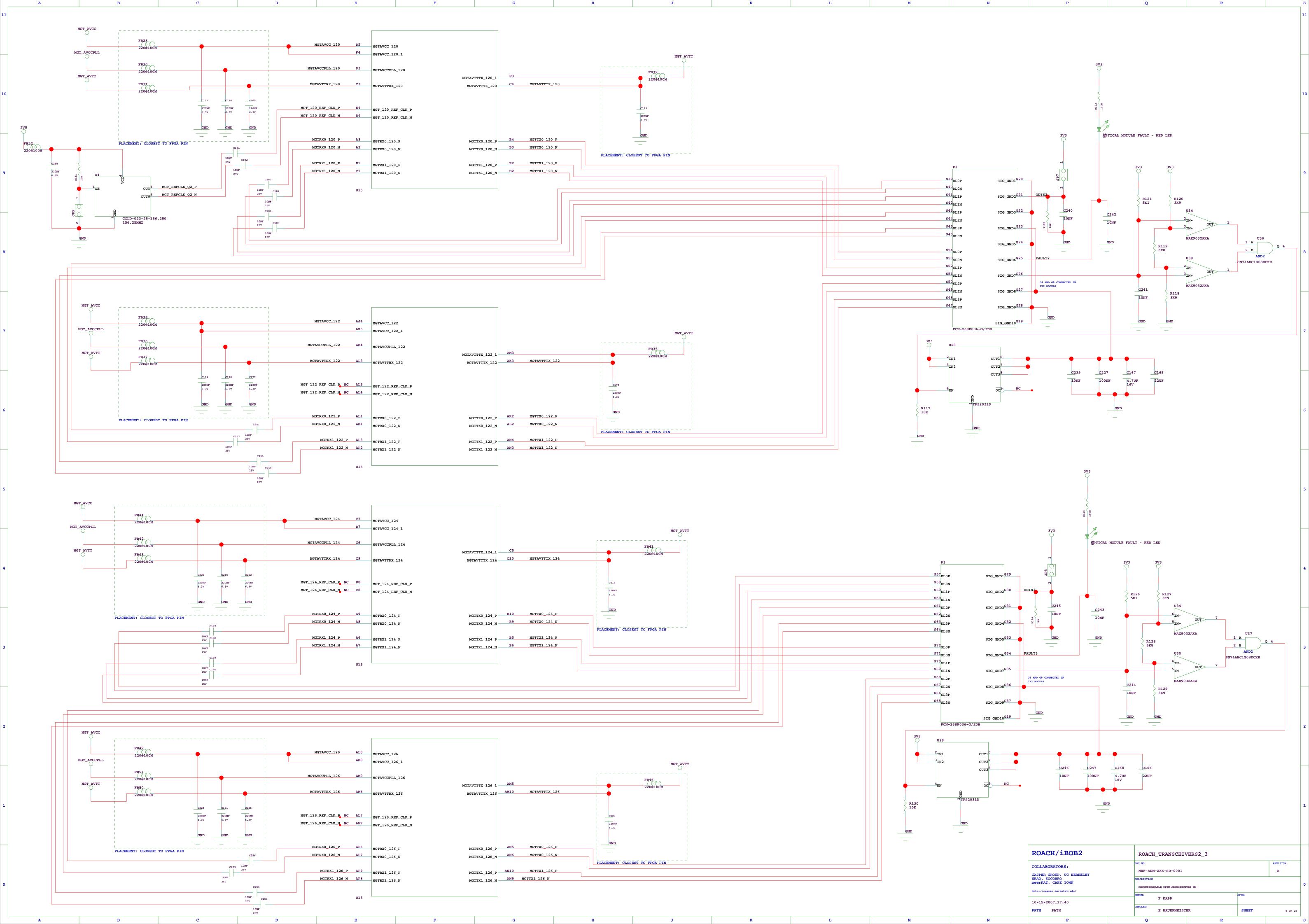


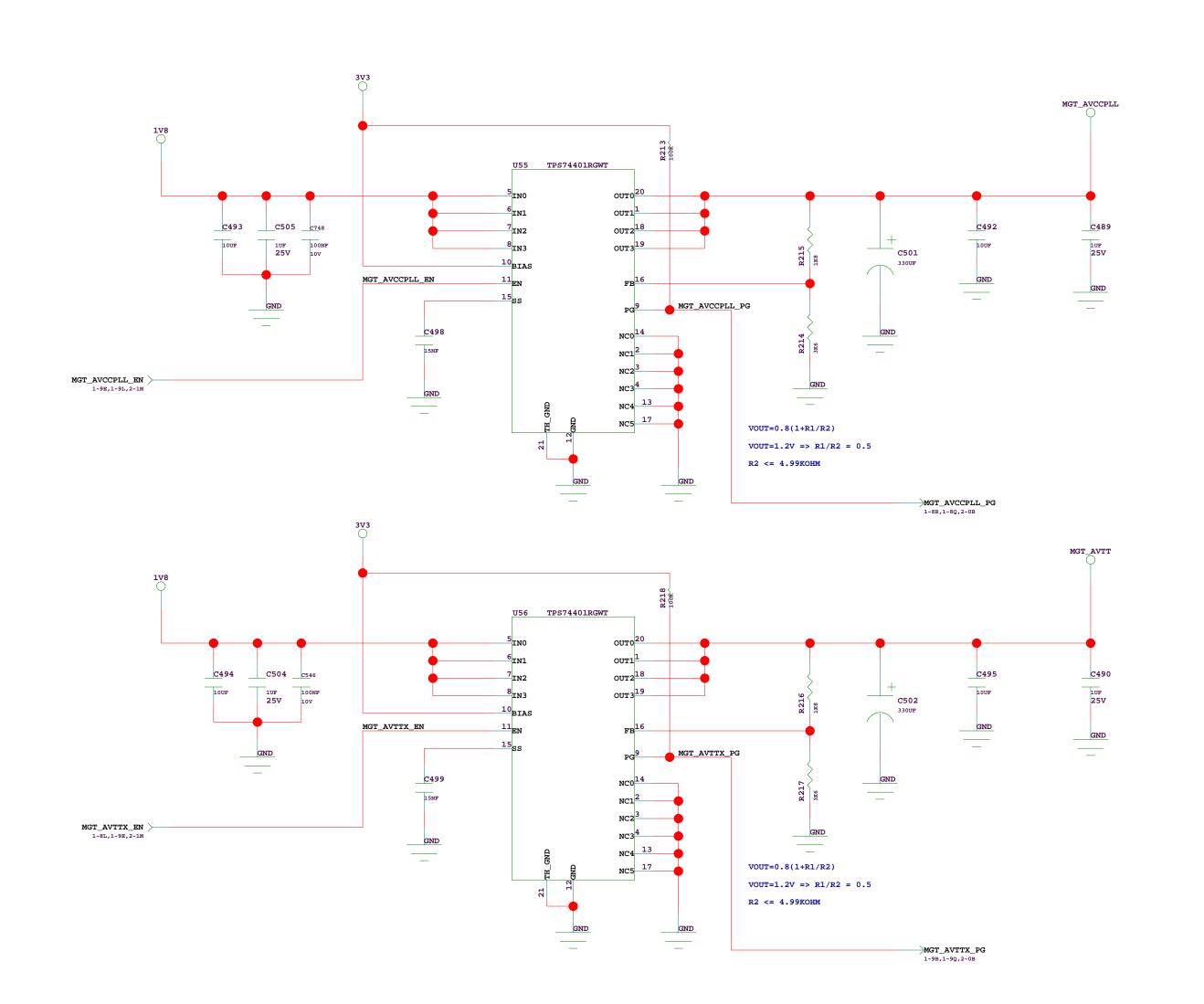


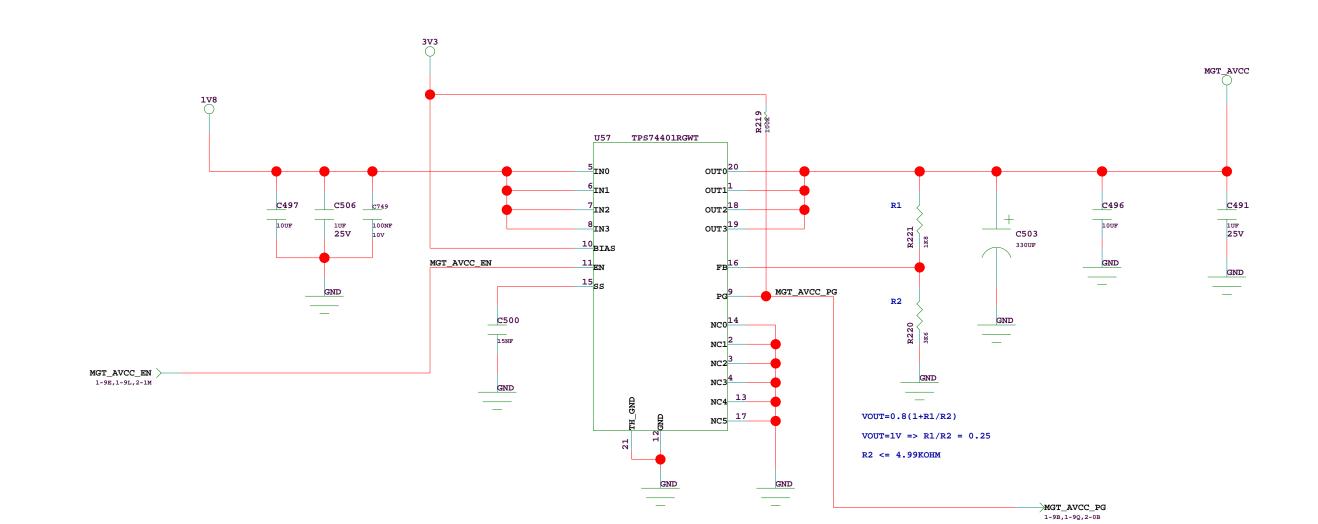












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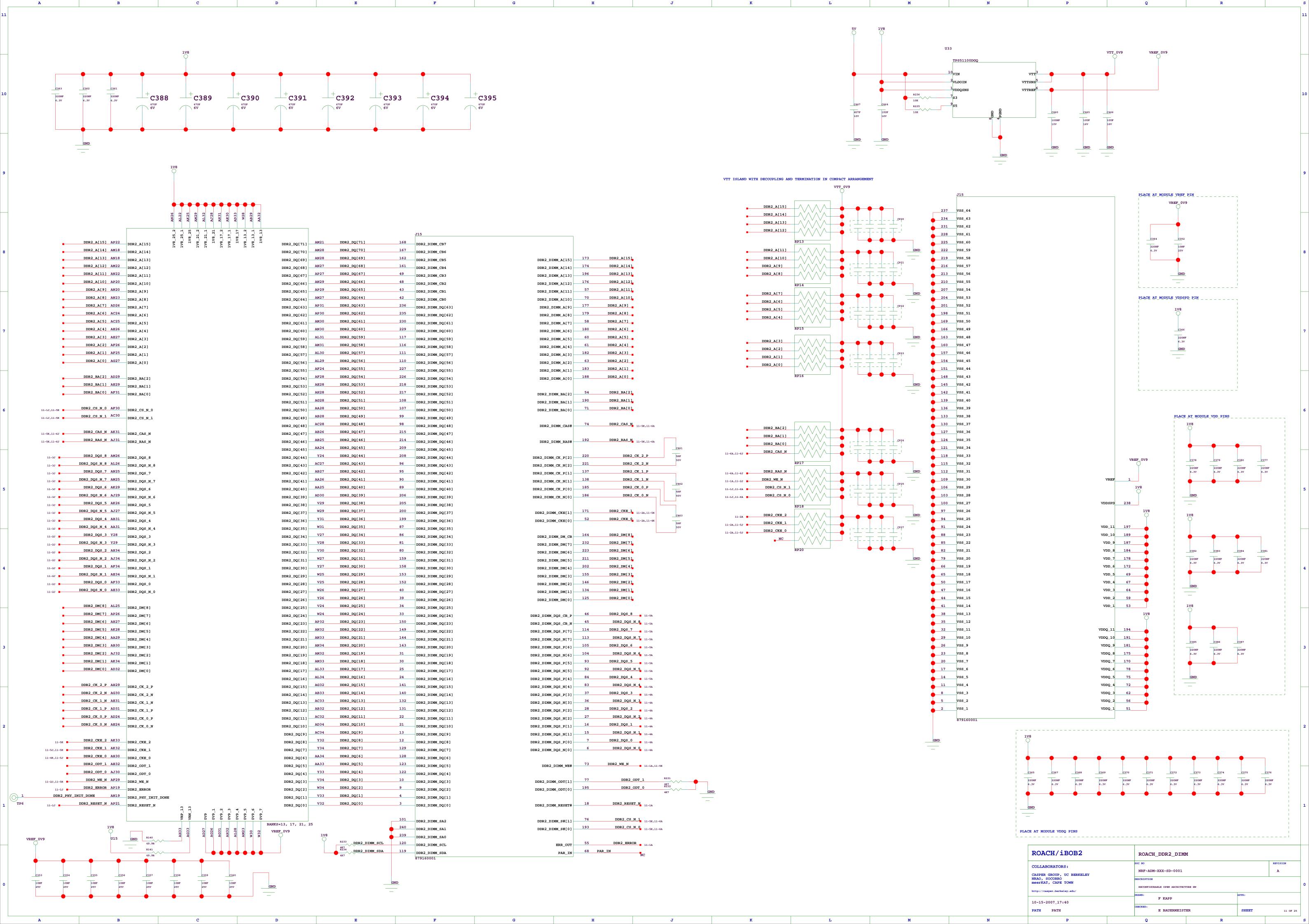
RECONDIQURABLE OPEN ARCHITECTURE HW

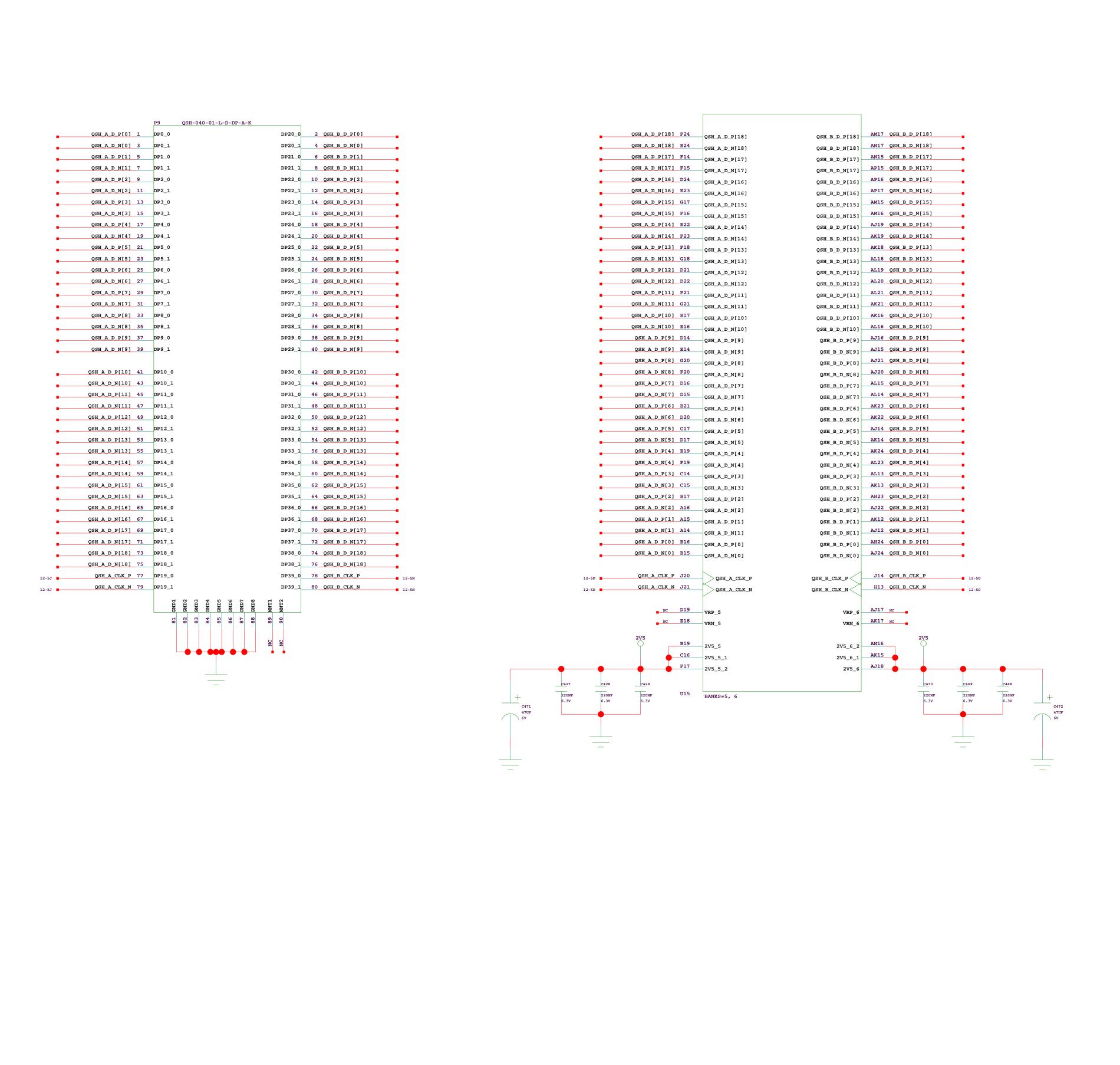
CHECKED:

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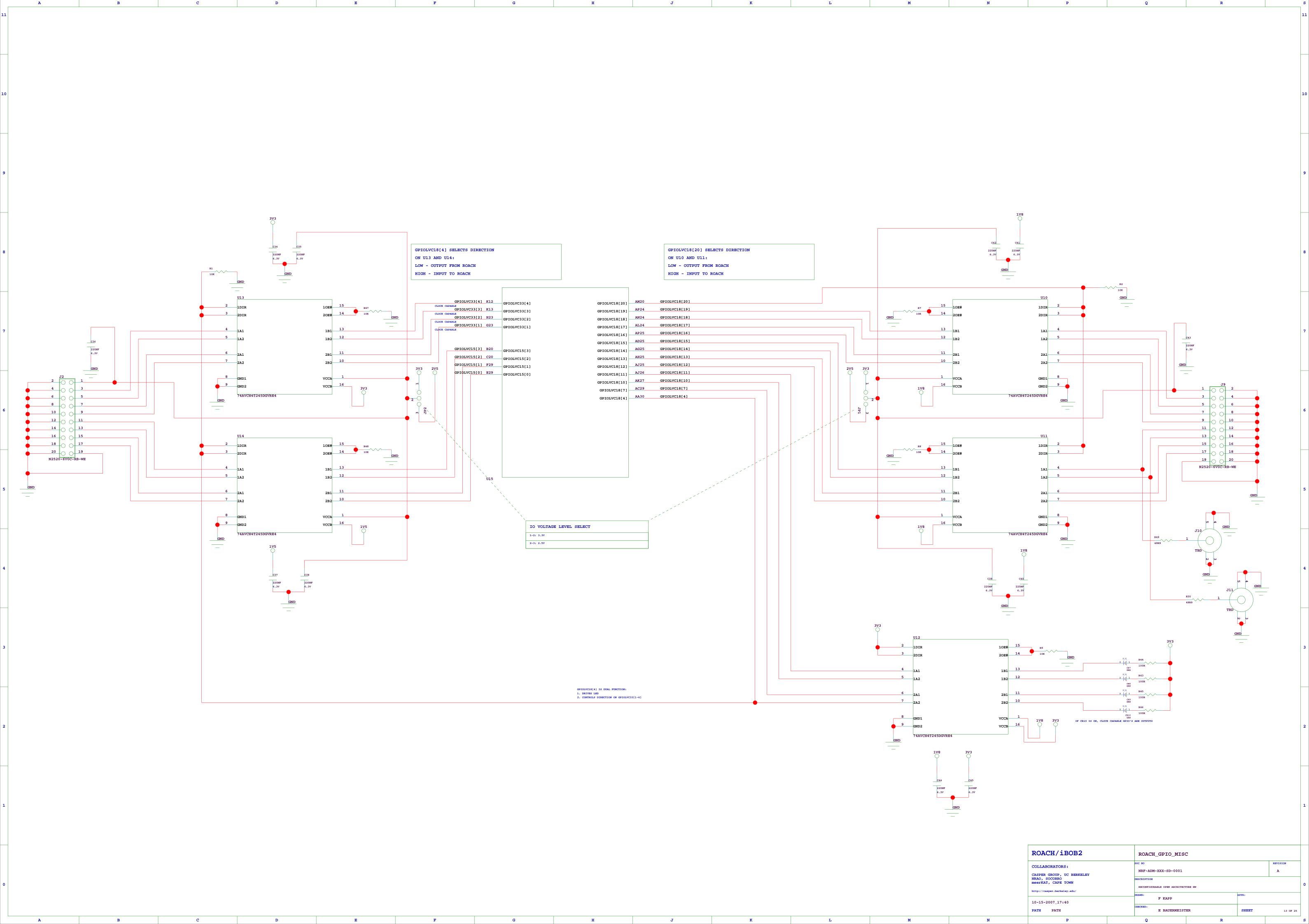
SHEET

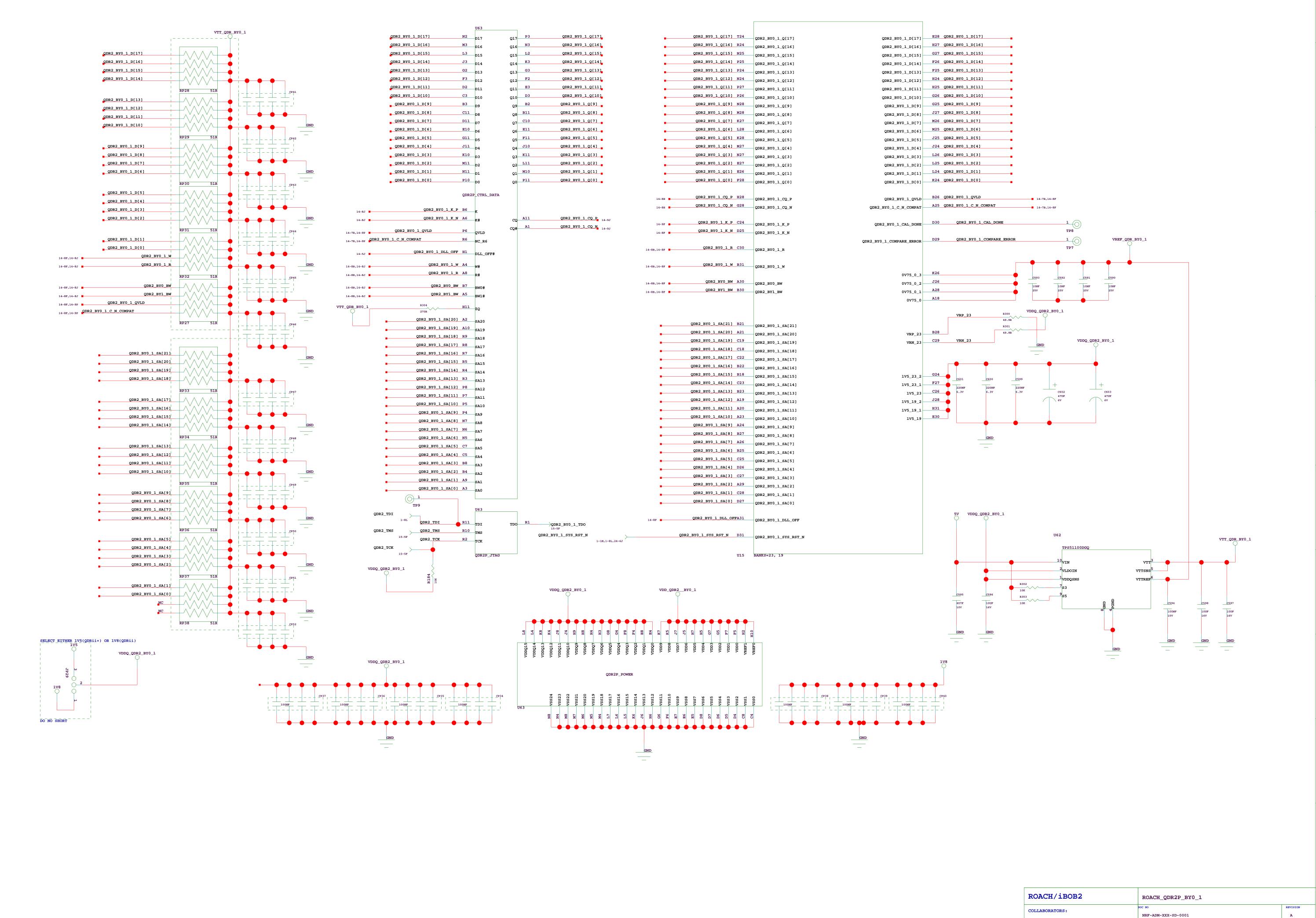
10 OF 25





ROACH/iBOB2 ROACH_DIFF_GPIO COLLABORATORS: NRF-ADM-XXX-SD-0001 CASPER GROUP, UC BERKELEY NRAO, SOCORRO meerKAT, CAPE TOWN RECONFIGURABLE OPEN ARCHITECTURE HW F KAPP 10-15-2007_17:40 E BAUERMEISTER PATH PATH





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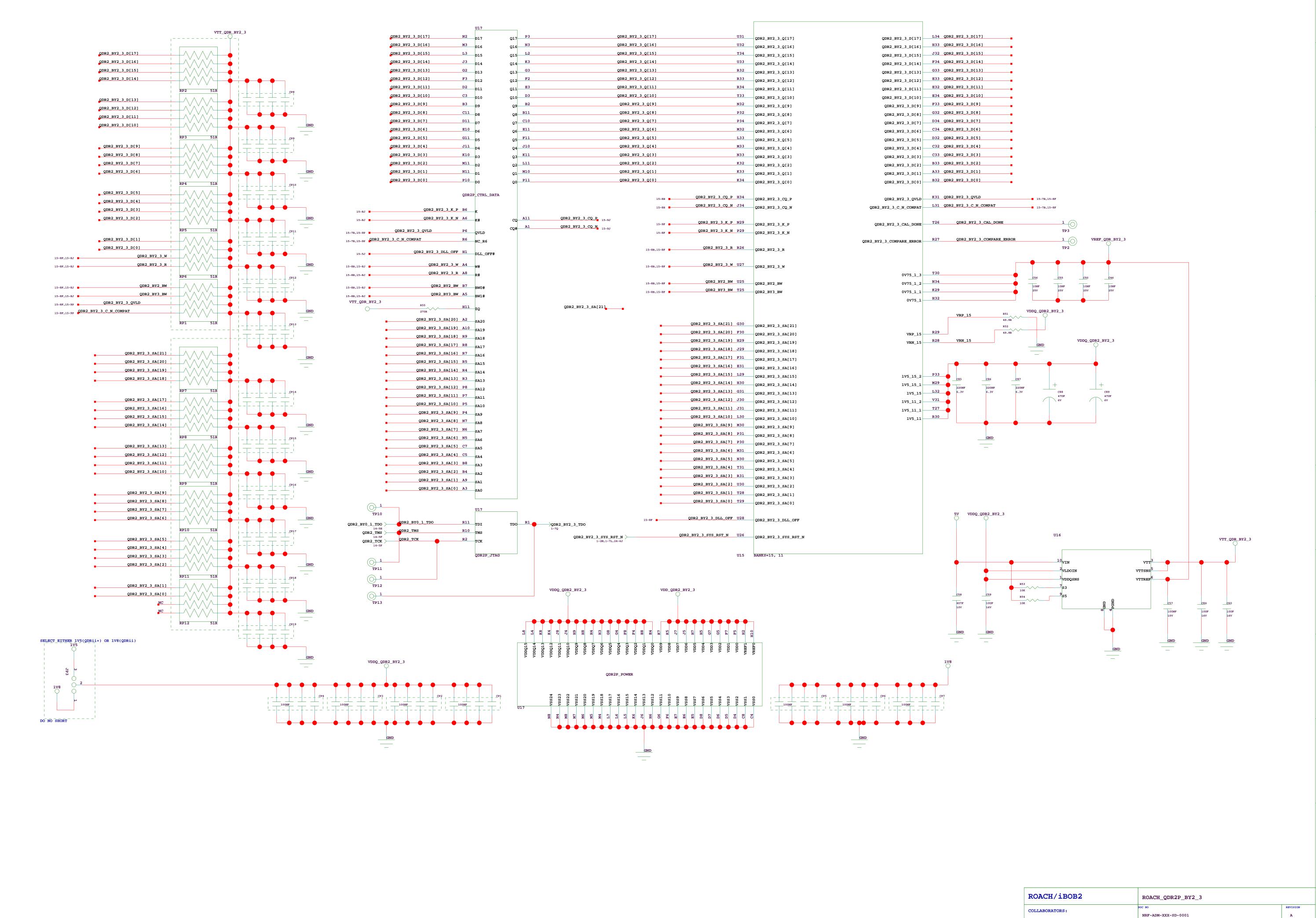
RECONFIGURABLE OPEN ARCHITECTURE HW

CHECKED:

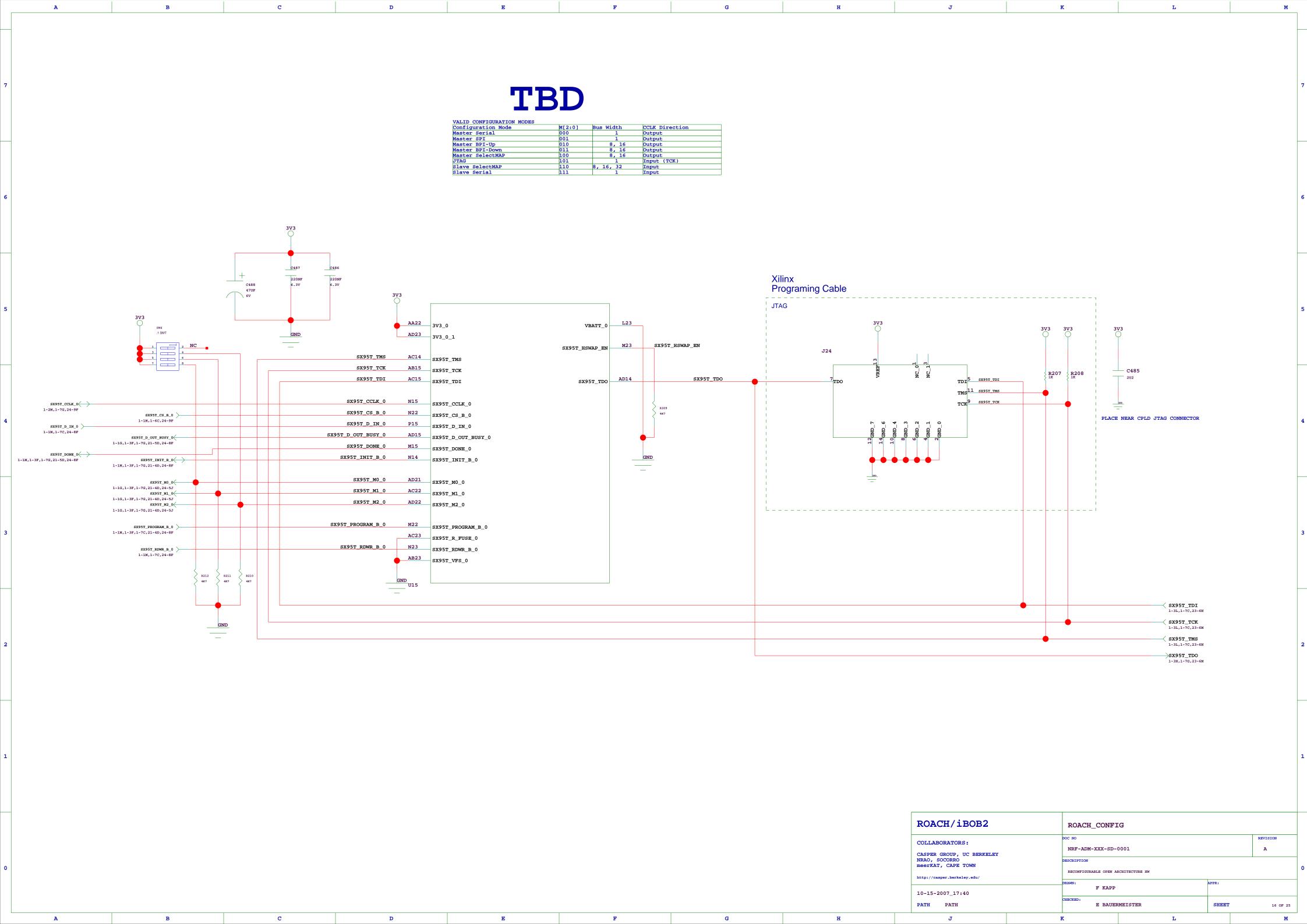
E BAUERMEISTER

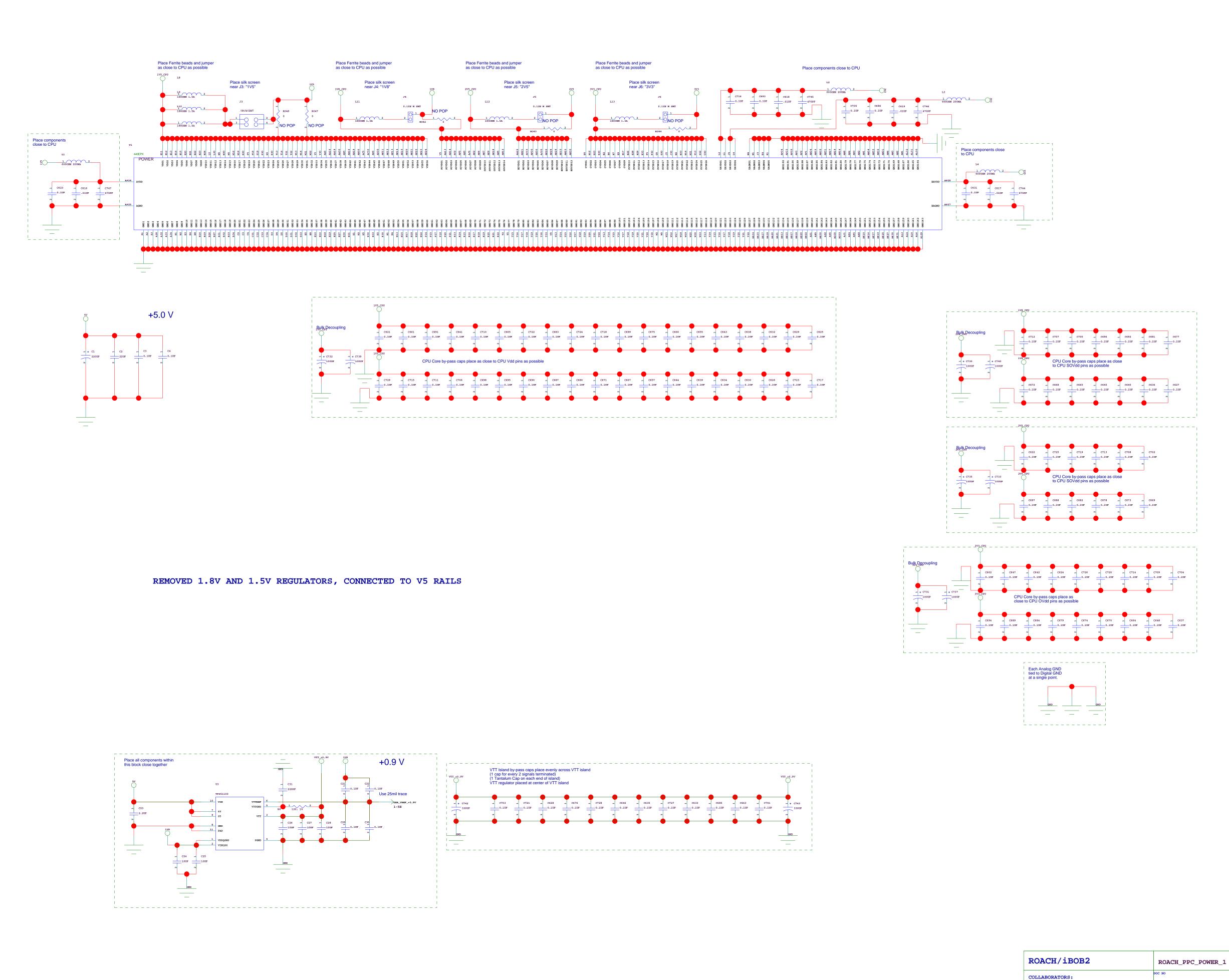
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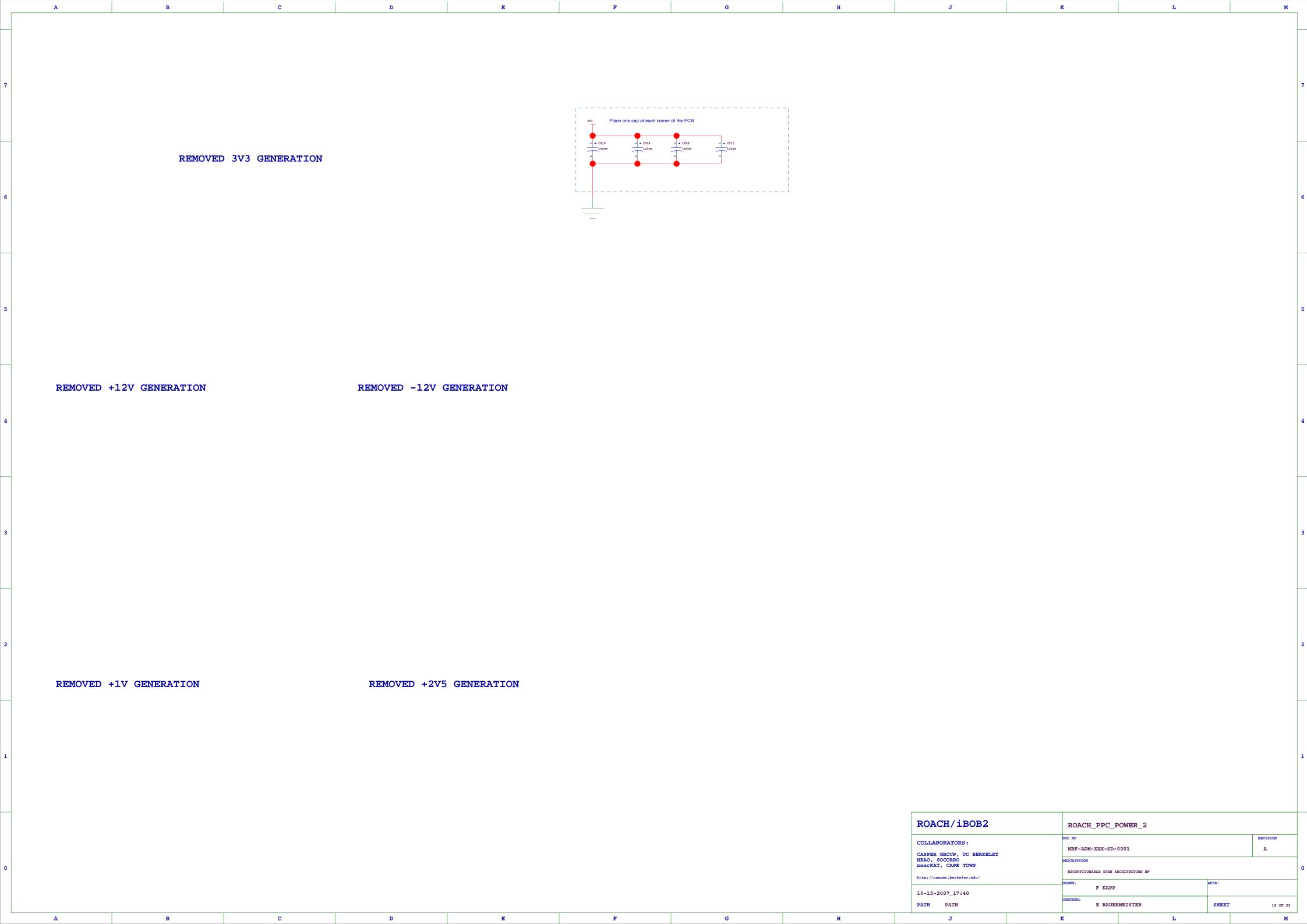
A

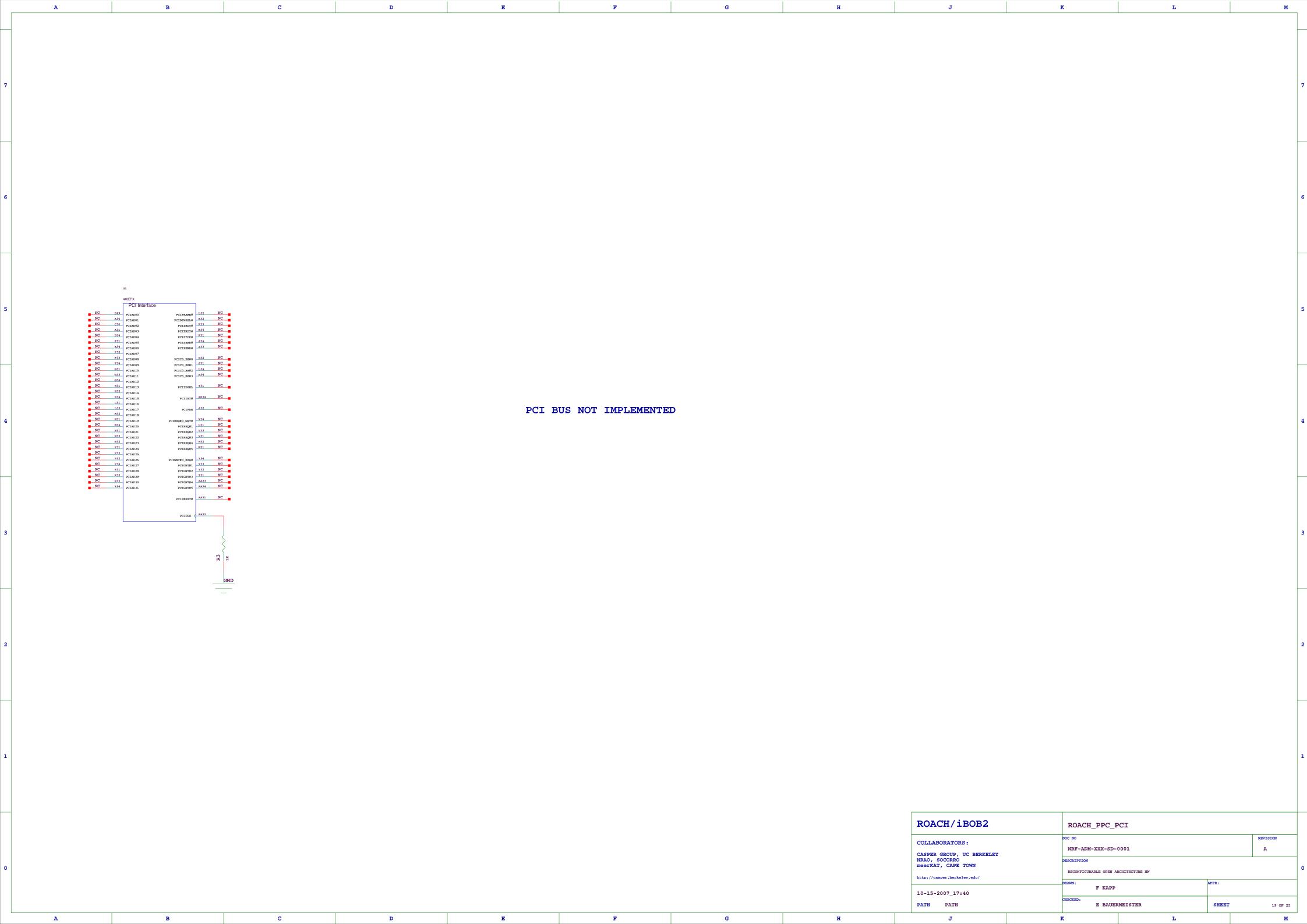
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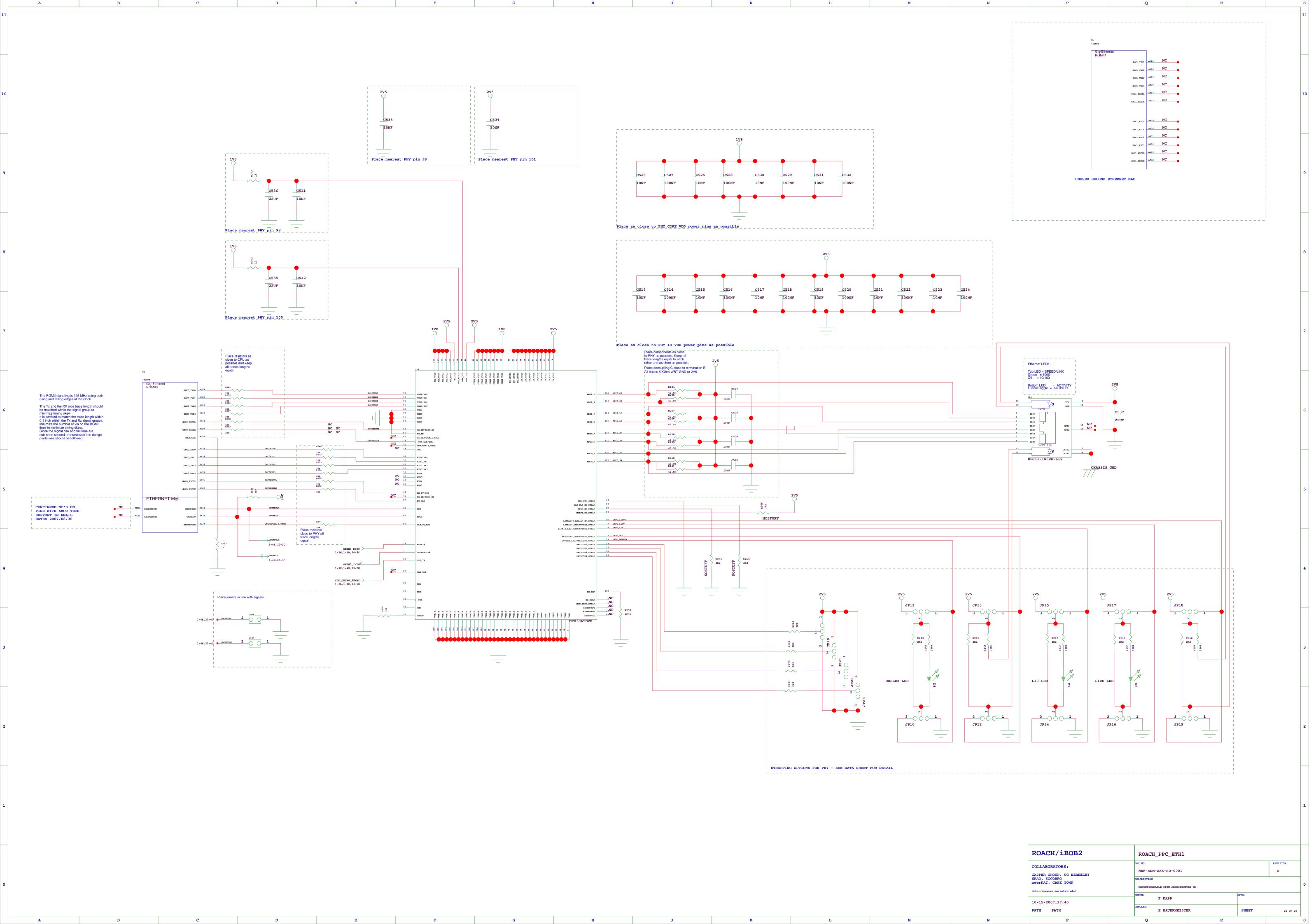
APPR:

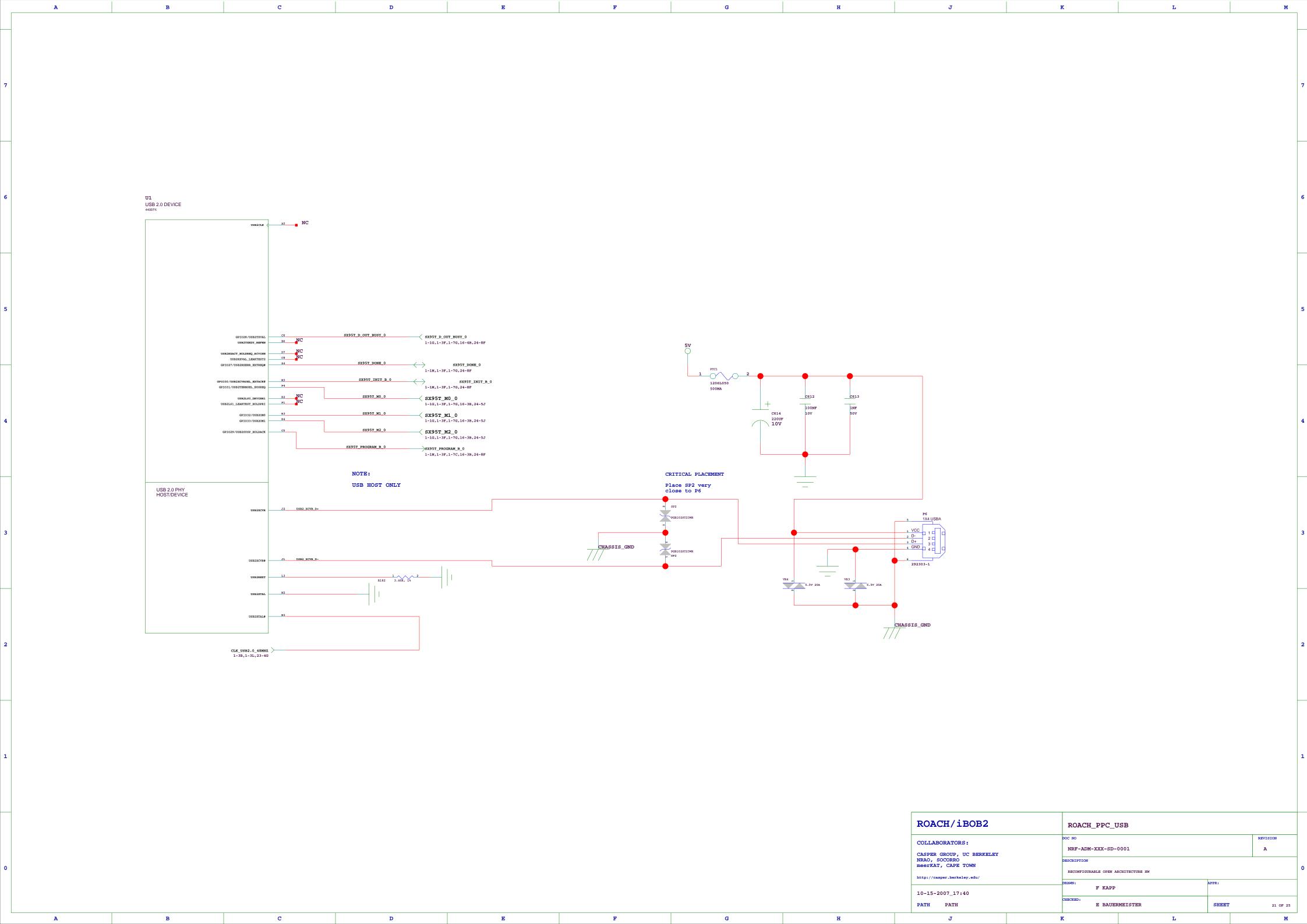
SHEET

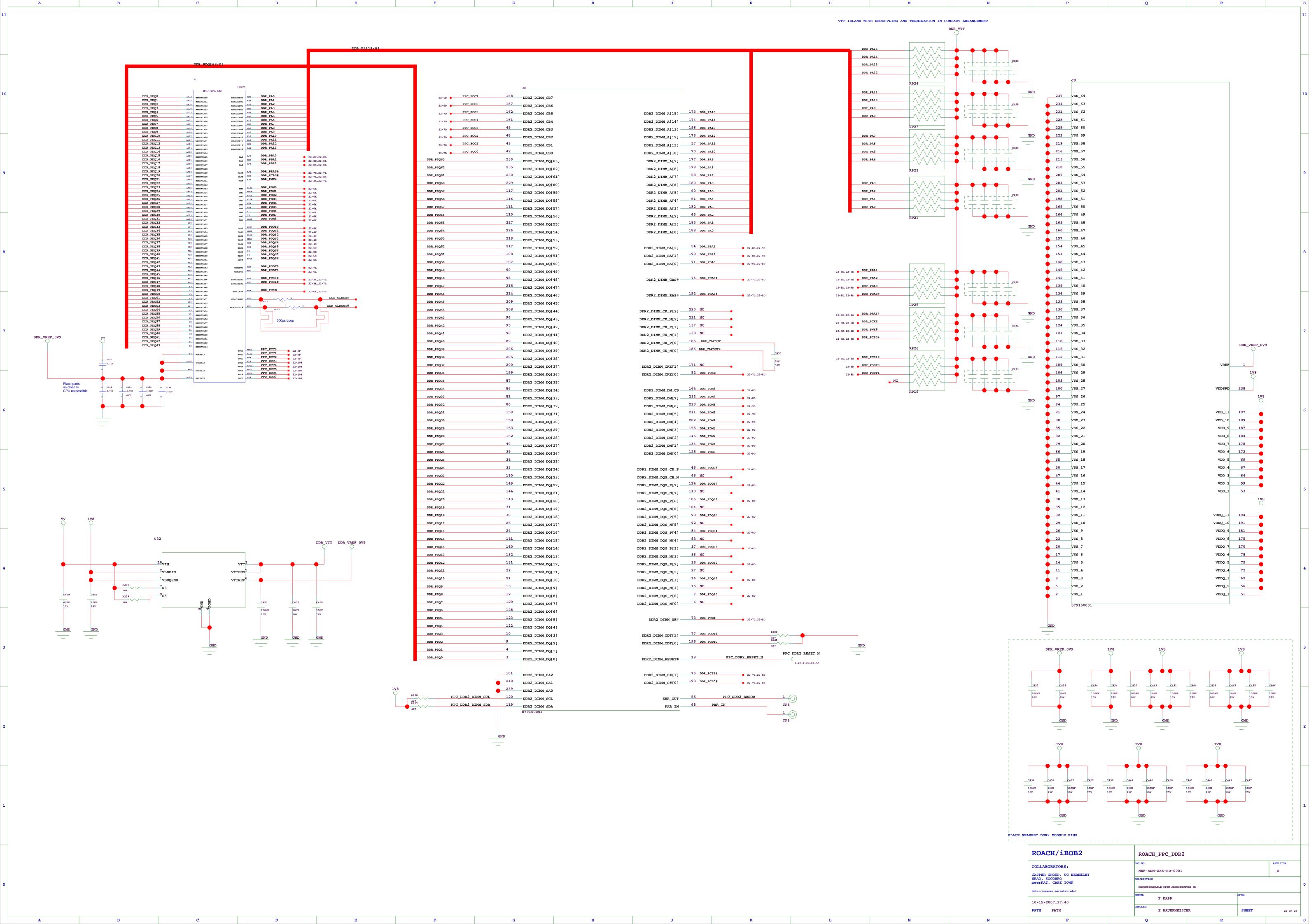
17 OF

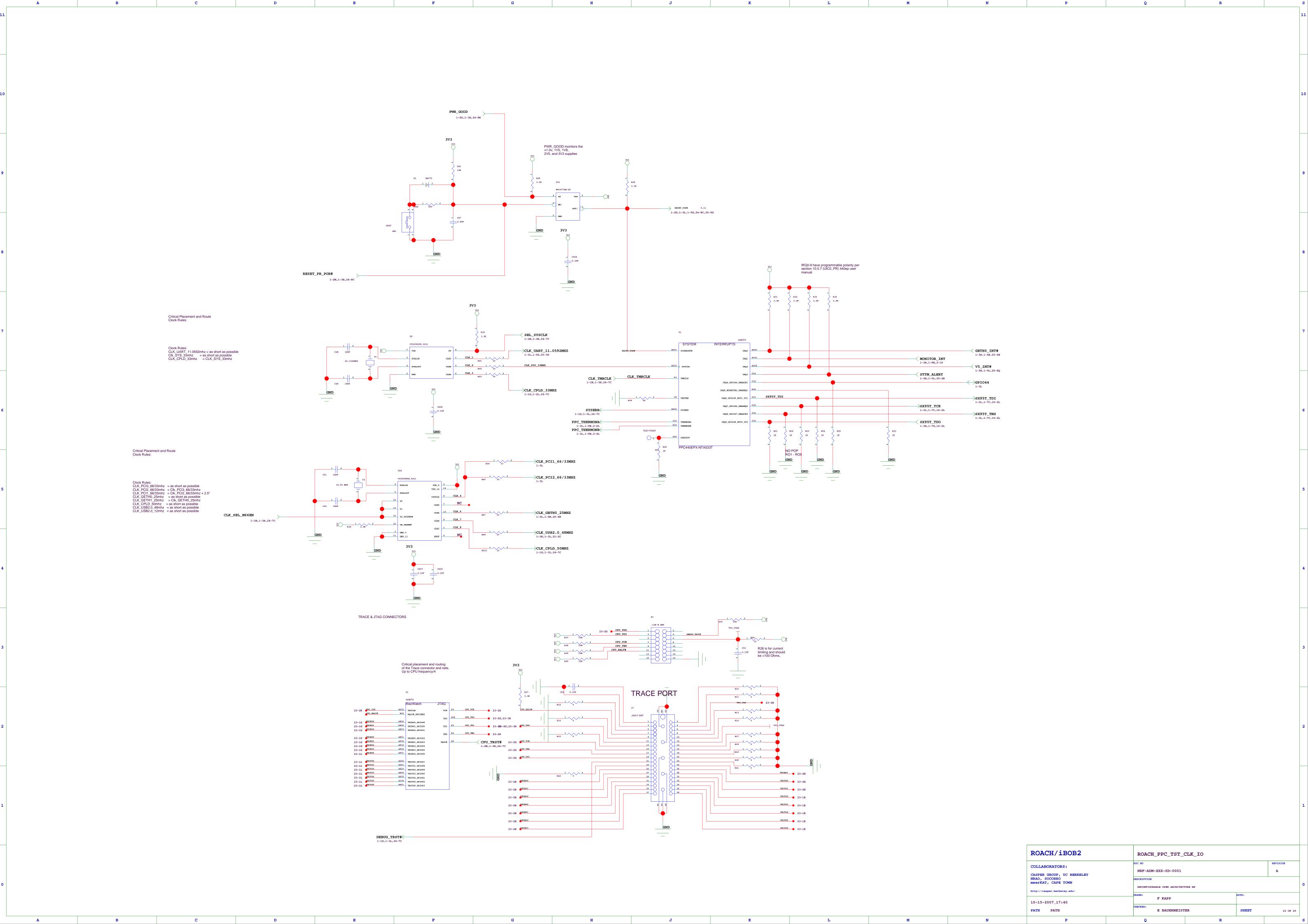


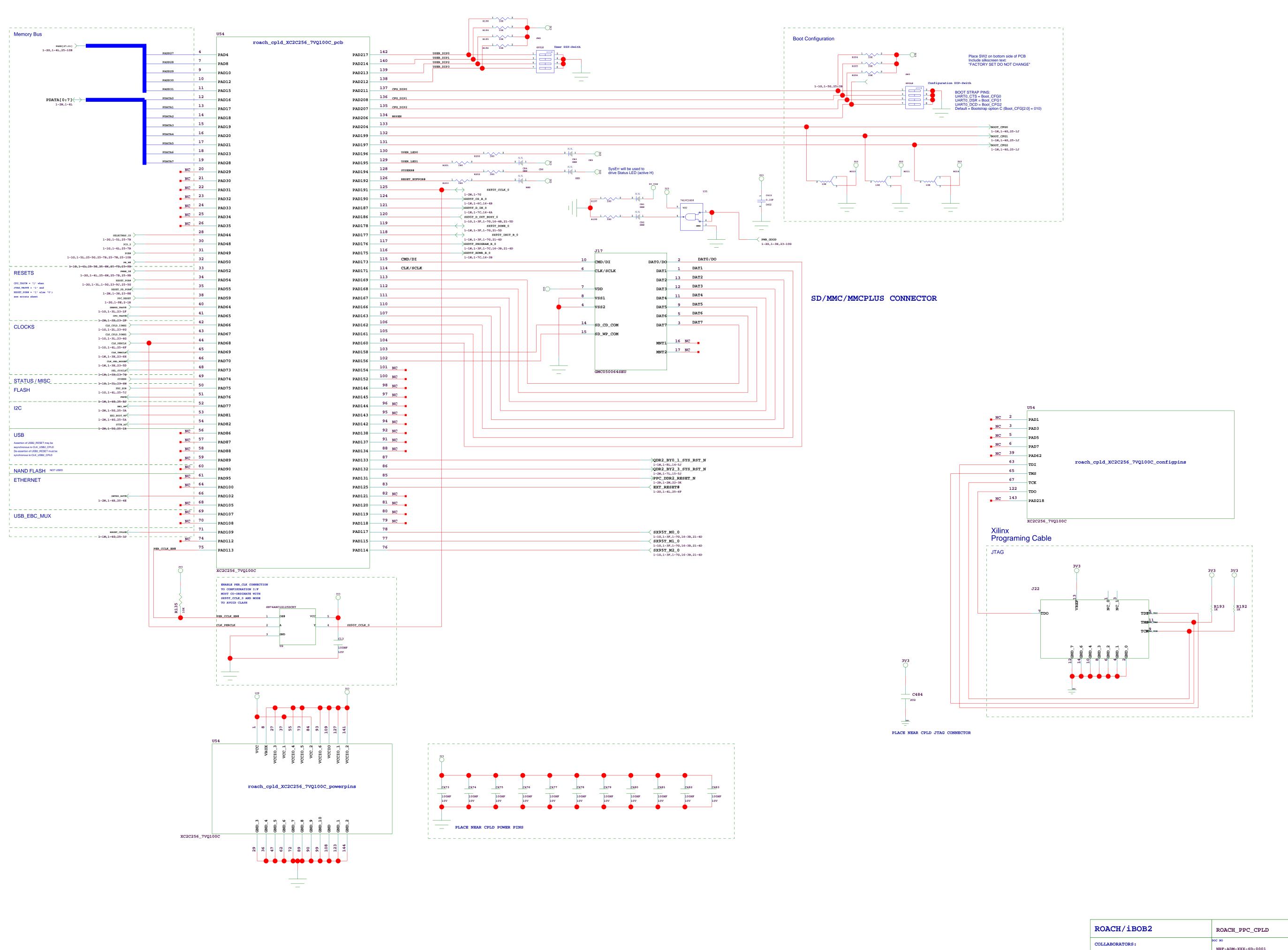












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