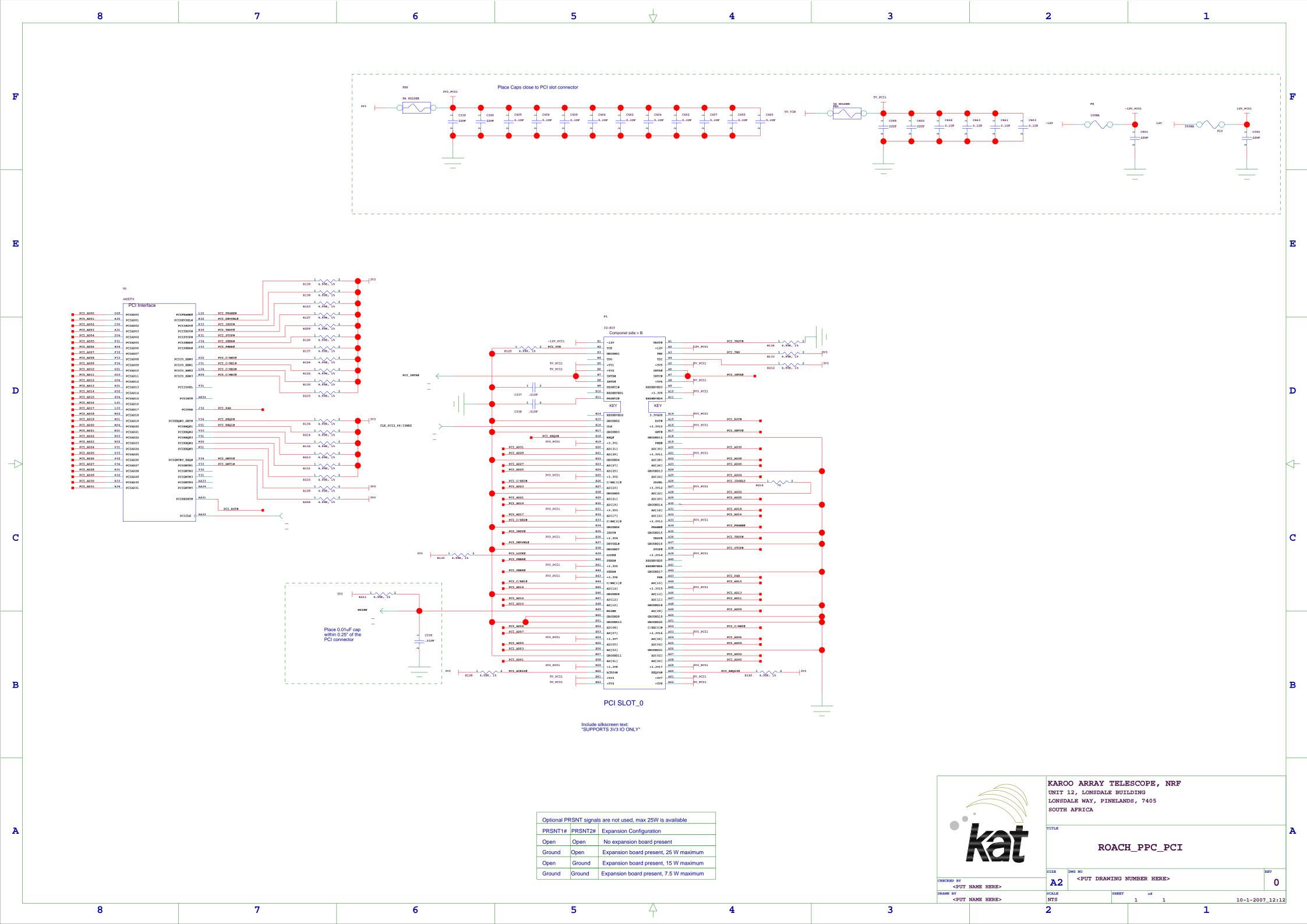
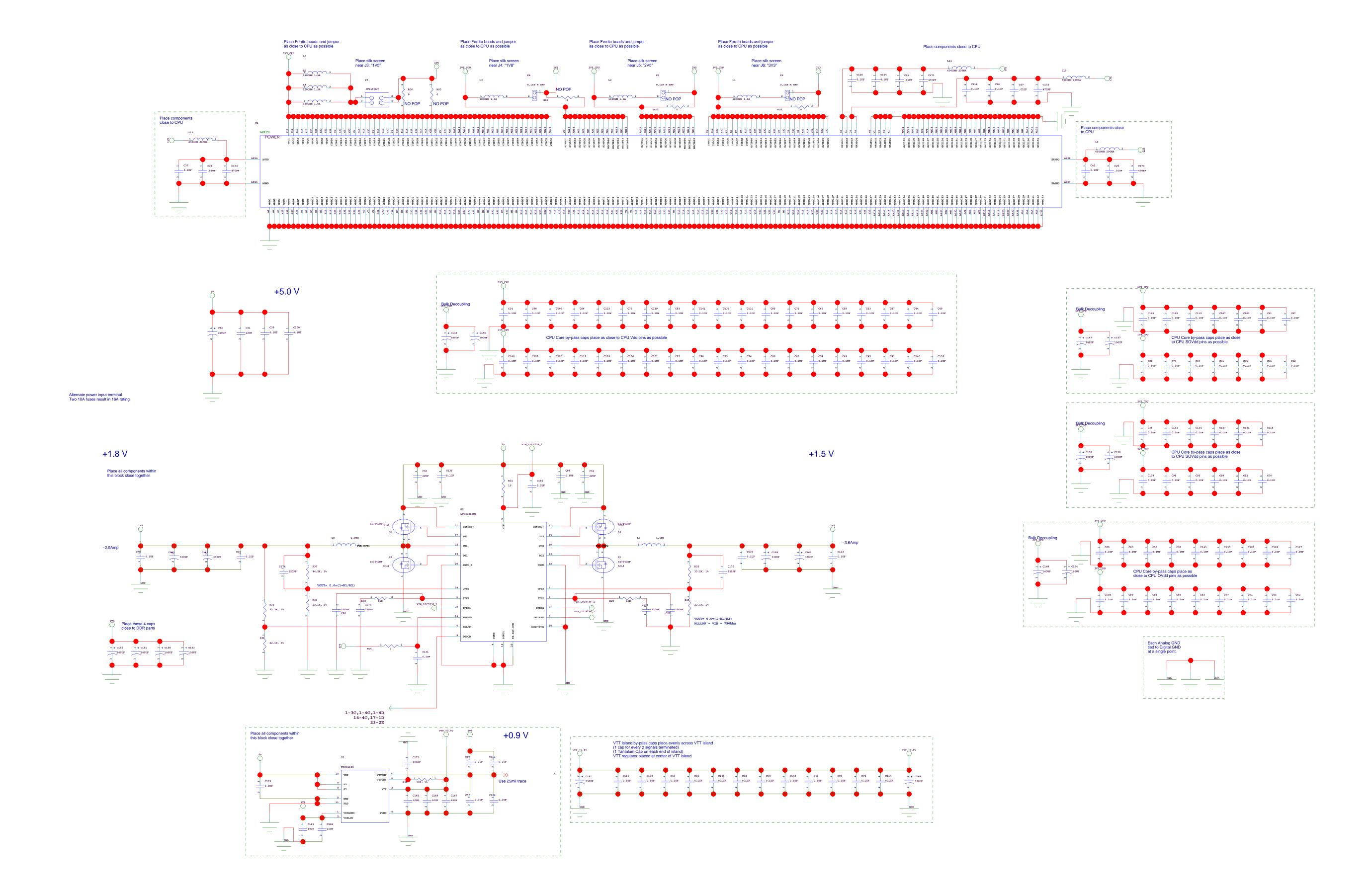
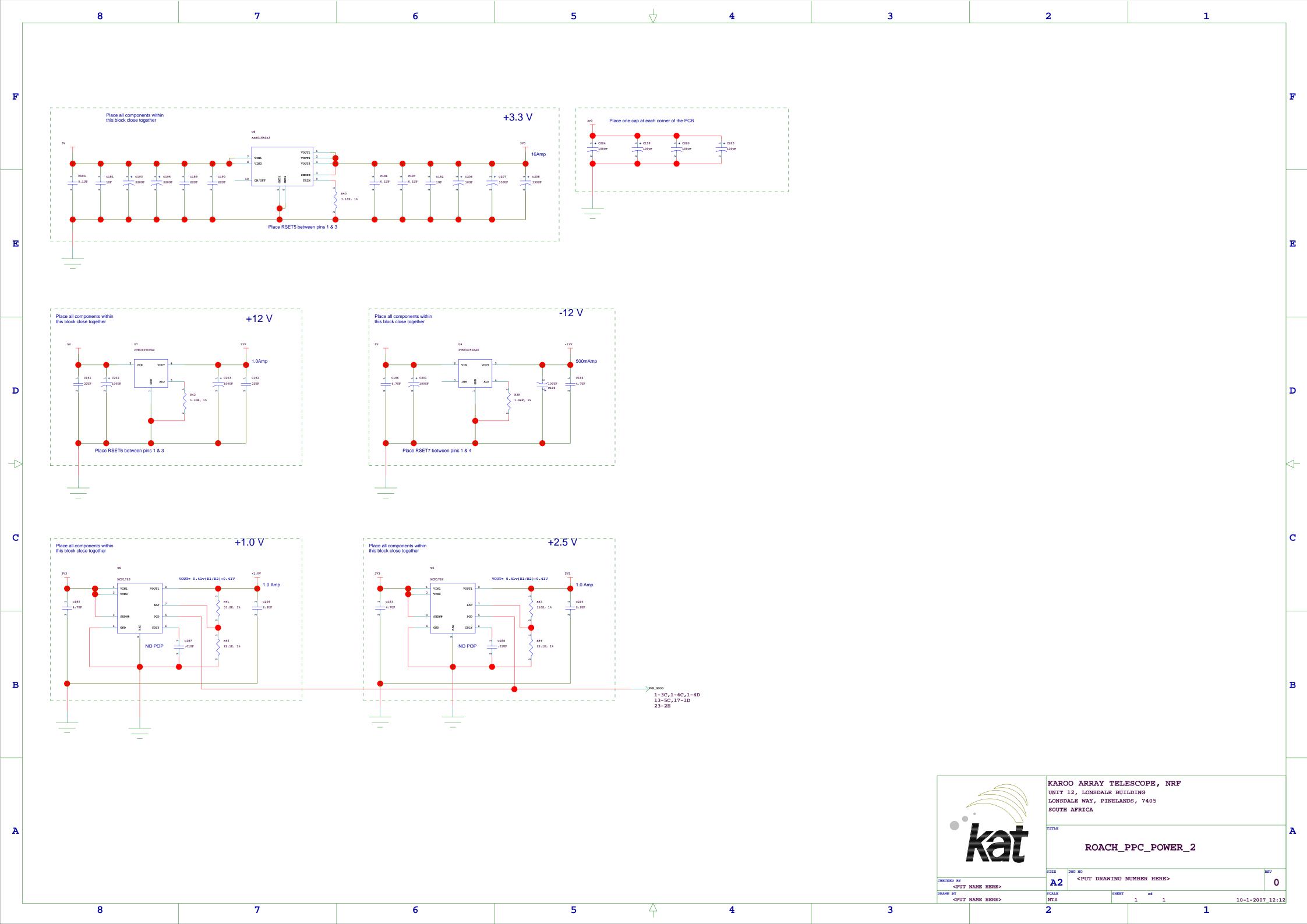
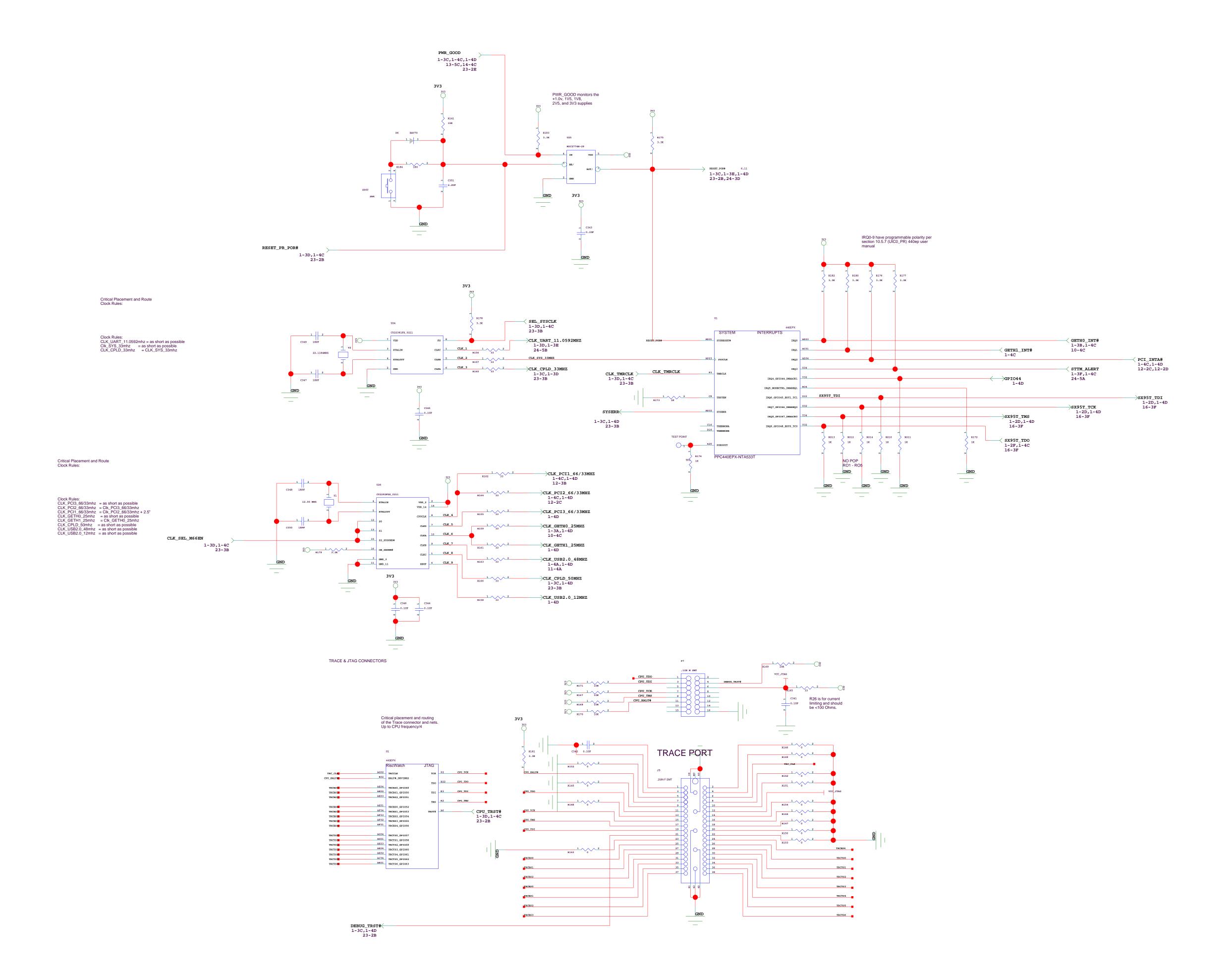


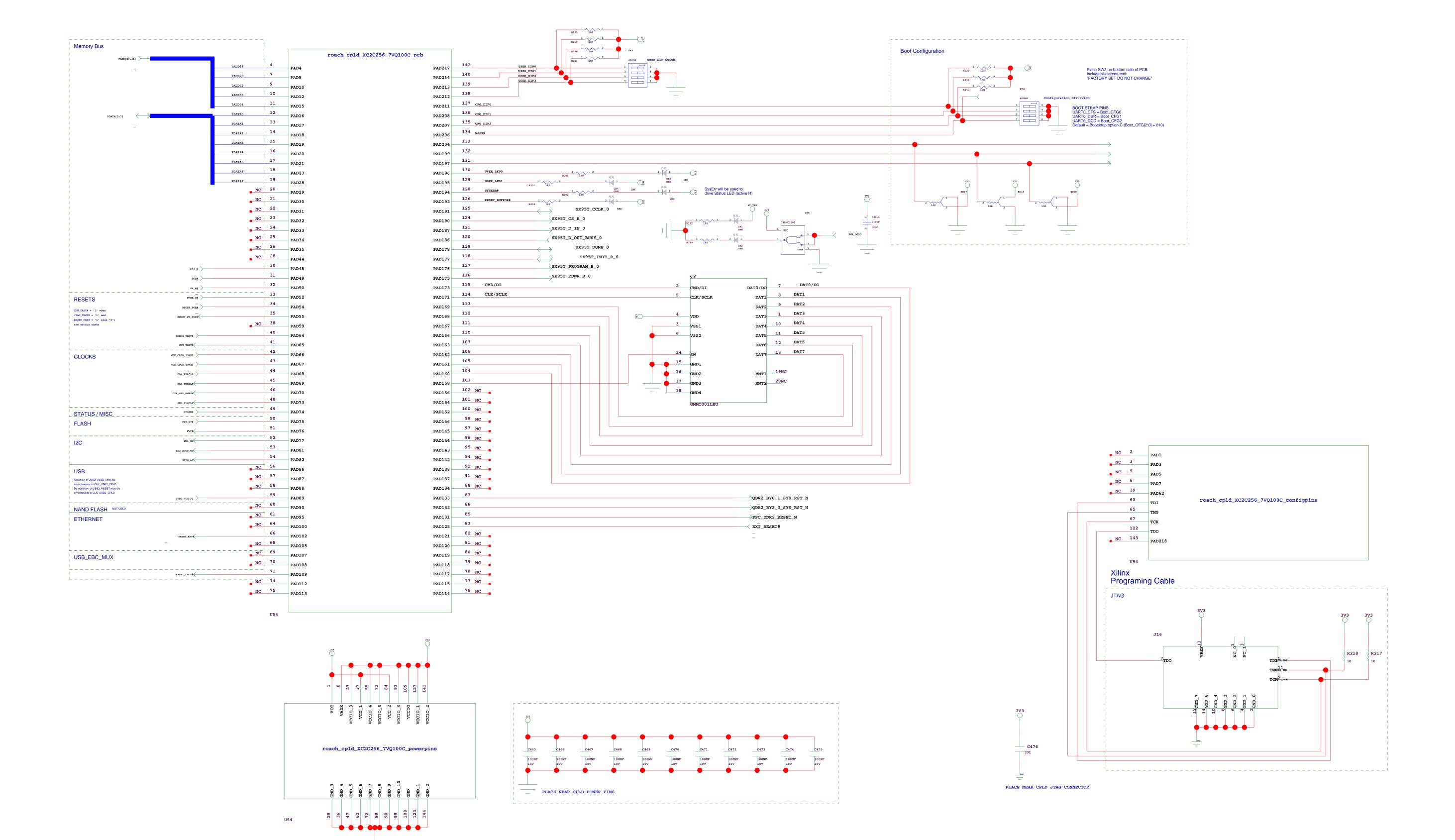
VTT ISLAND WITH DECOUPLING AND TERMINATION IN COMPACT ARRANGEMENT





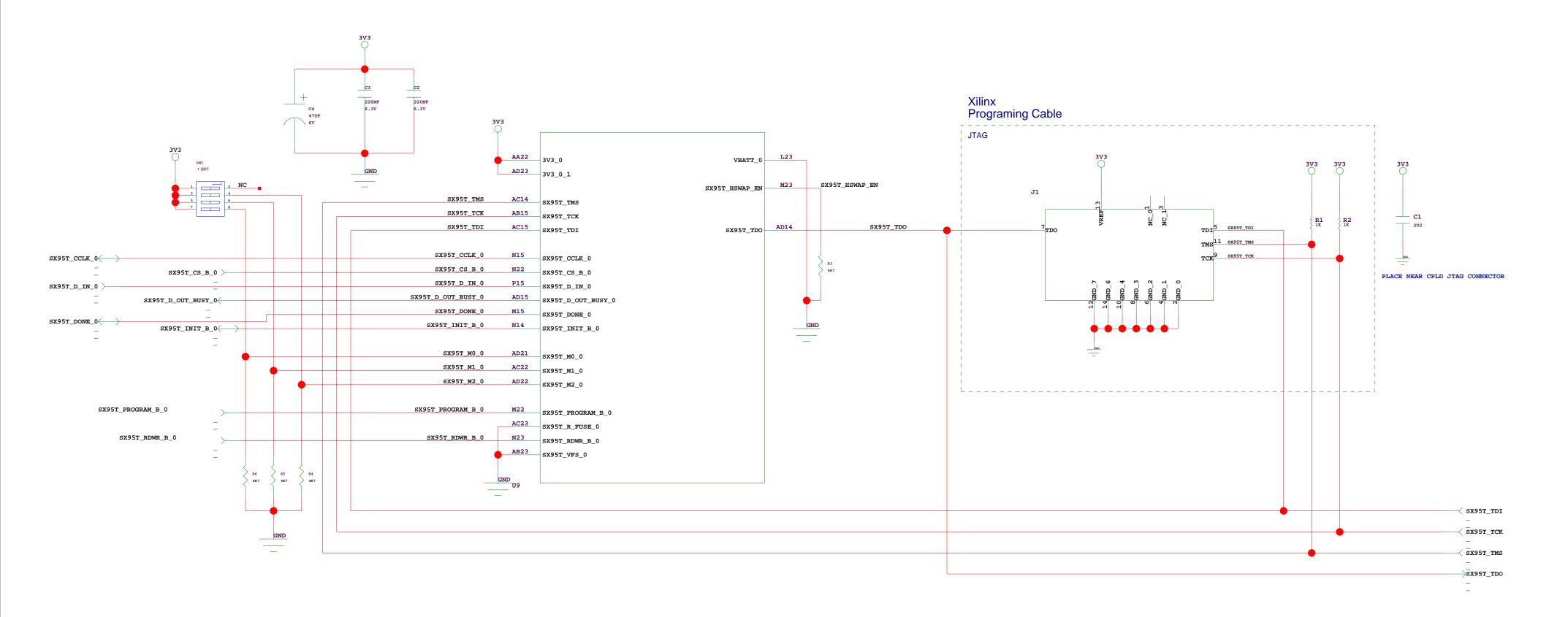


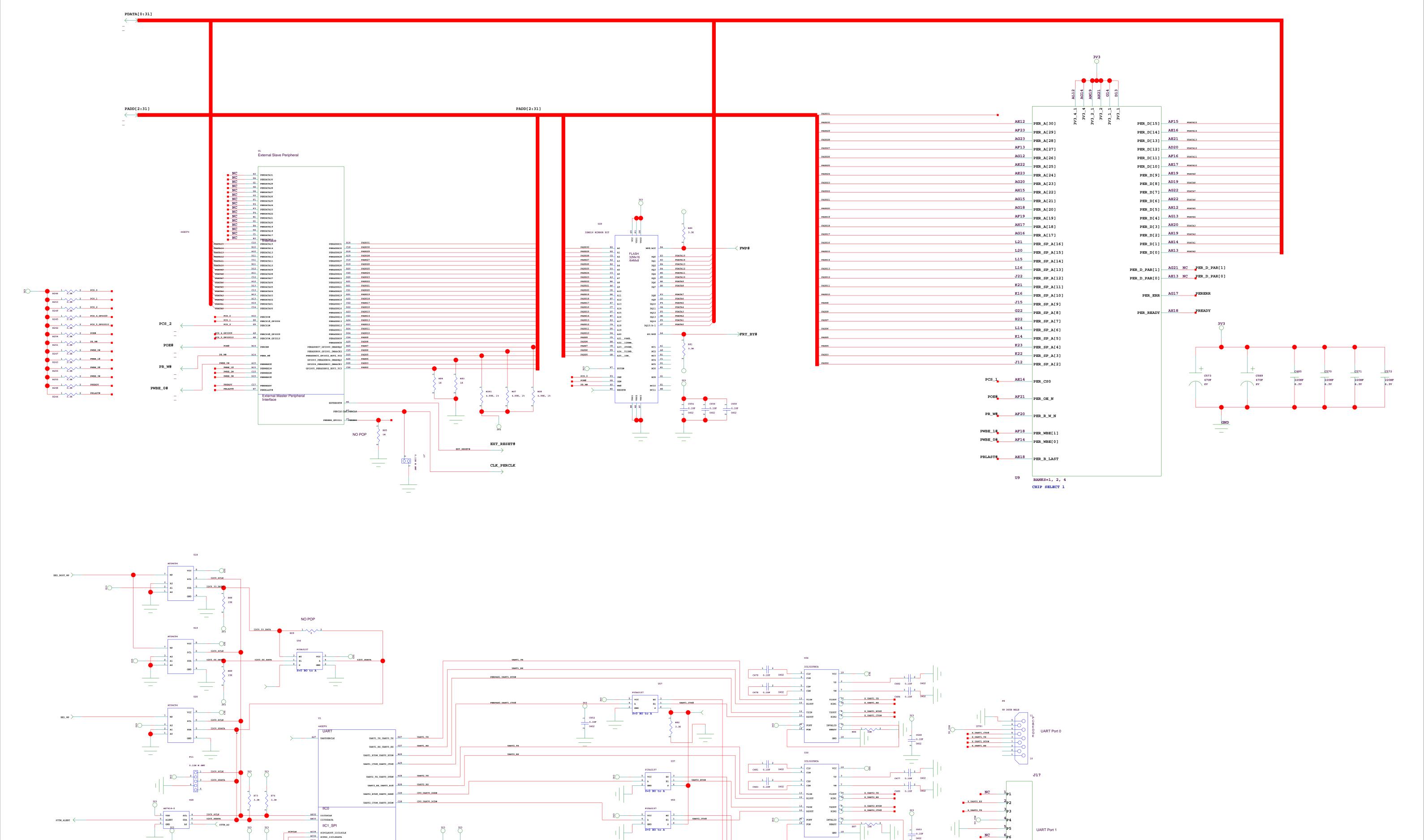




TBD

VALID CONFIGURATION MODES			
Configuration Mode	M[2:0]	Bus Width	CCLK Direction
Master Serial	000	1	Output
Master SPI	001	1	Output
Master BPI-Up	010	8, 16	Output
Master BPI-Down	011	8, 16	Output
Master SelectMAP	100	8, 16	Output
JTAG	101	1	Input (TCK)
Slave SelectMAP	110	8, 16, 32	Input
Slave Serial	111	1	Input





BOOT_CFG2

BOOT_CFG1

0.1UF 0.1UF 0.1UF

0.1IN M SMT

NFCE3#_PERCS3# D8

X_UART2_RTS#

NC 9

NC 10

HLE-105-02-G-DV

