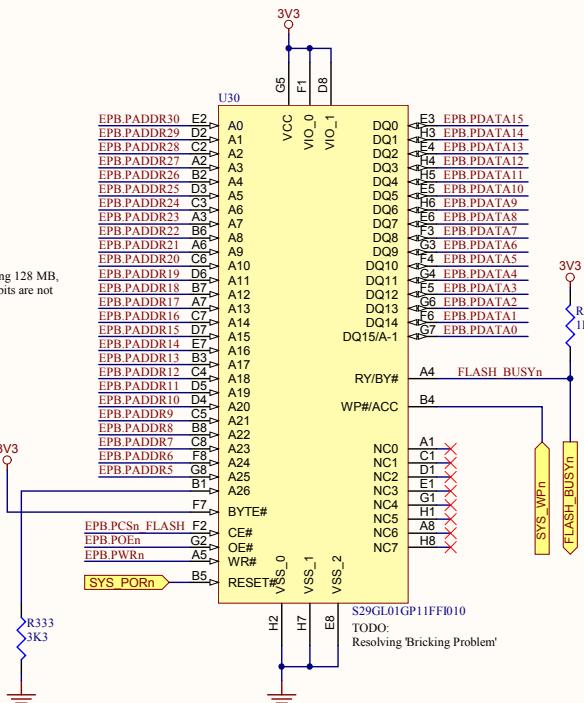
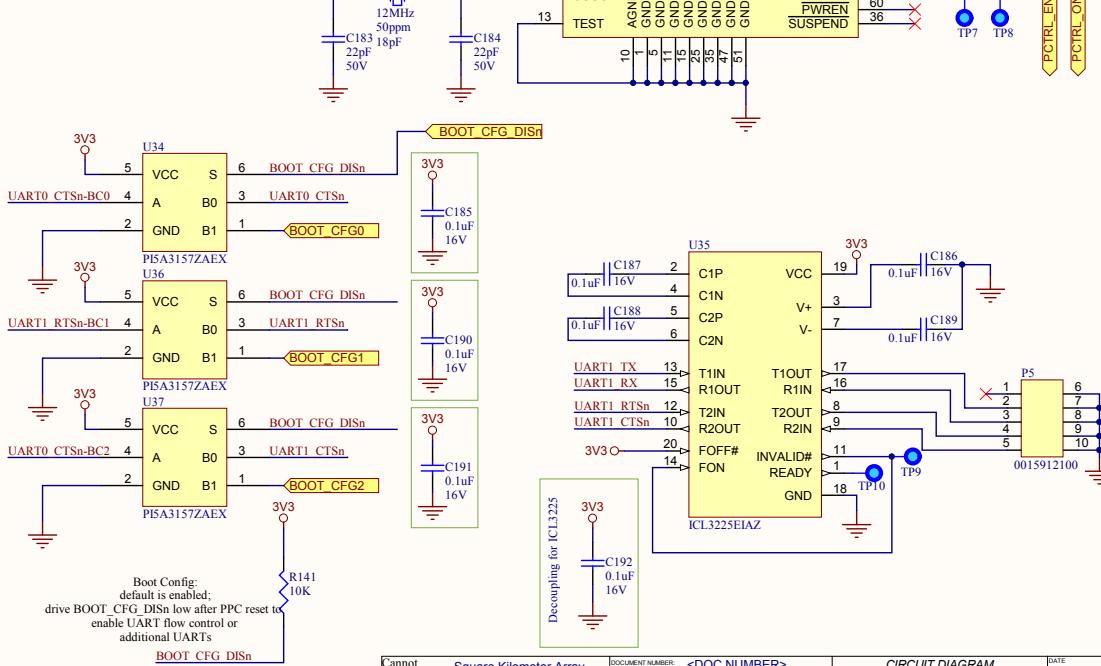
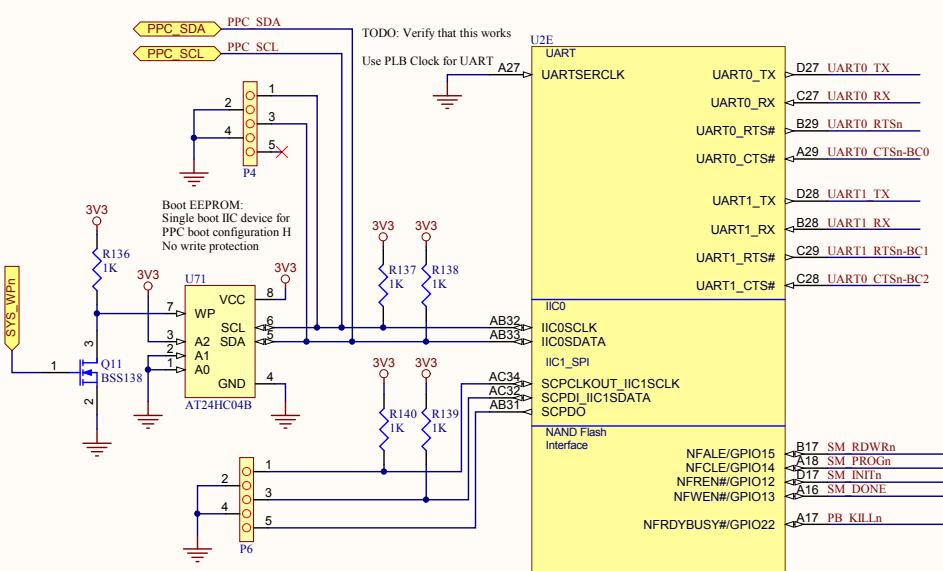
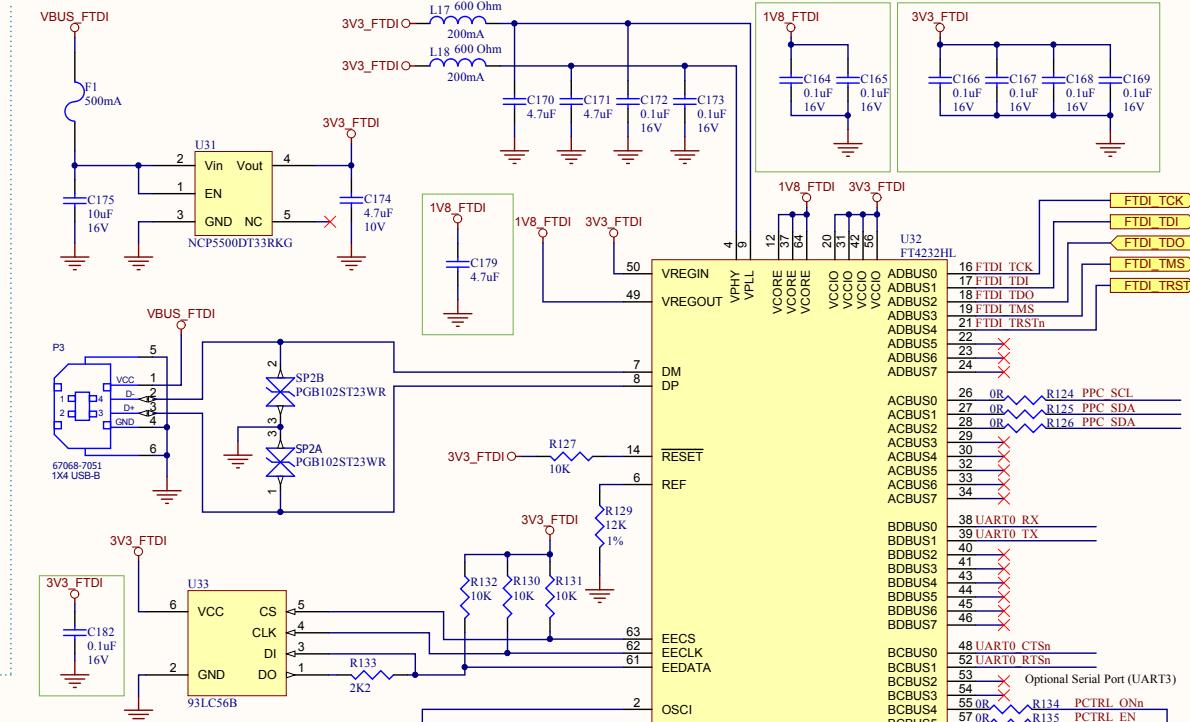
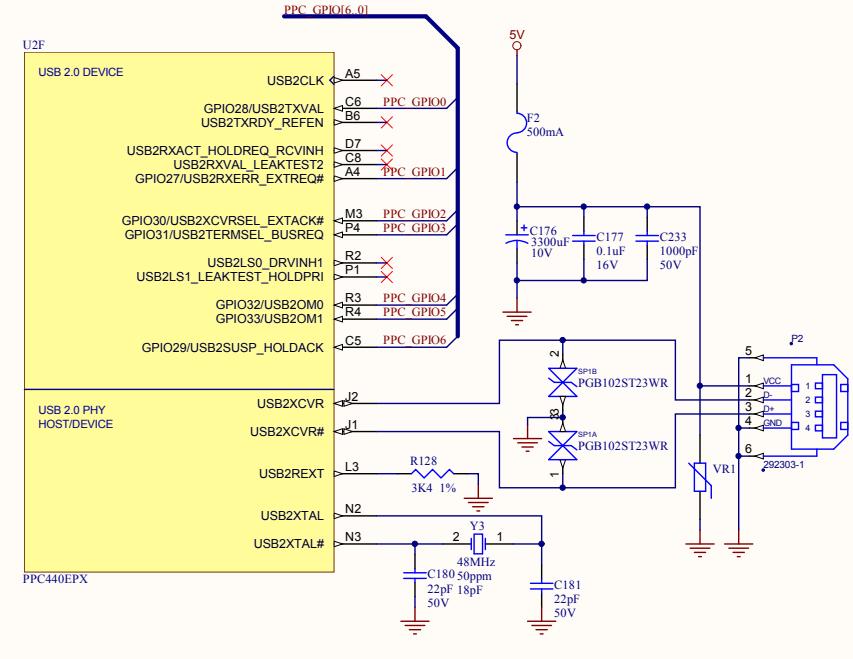


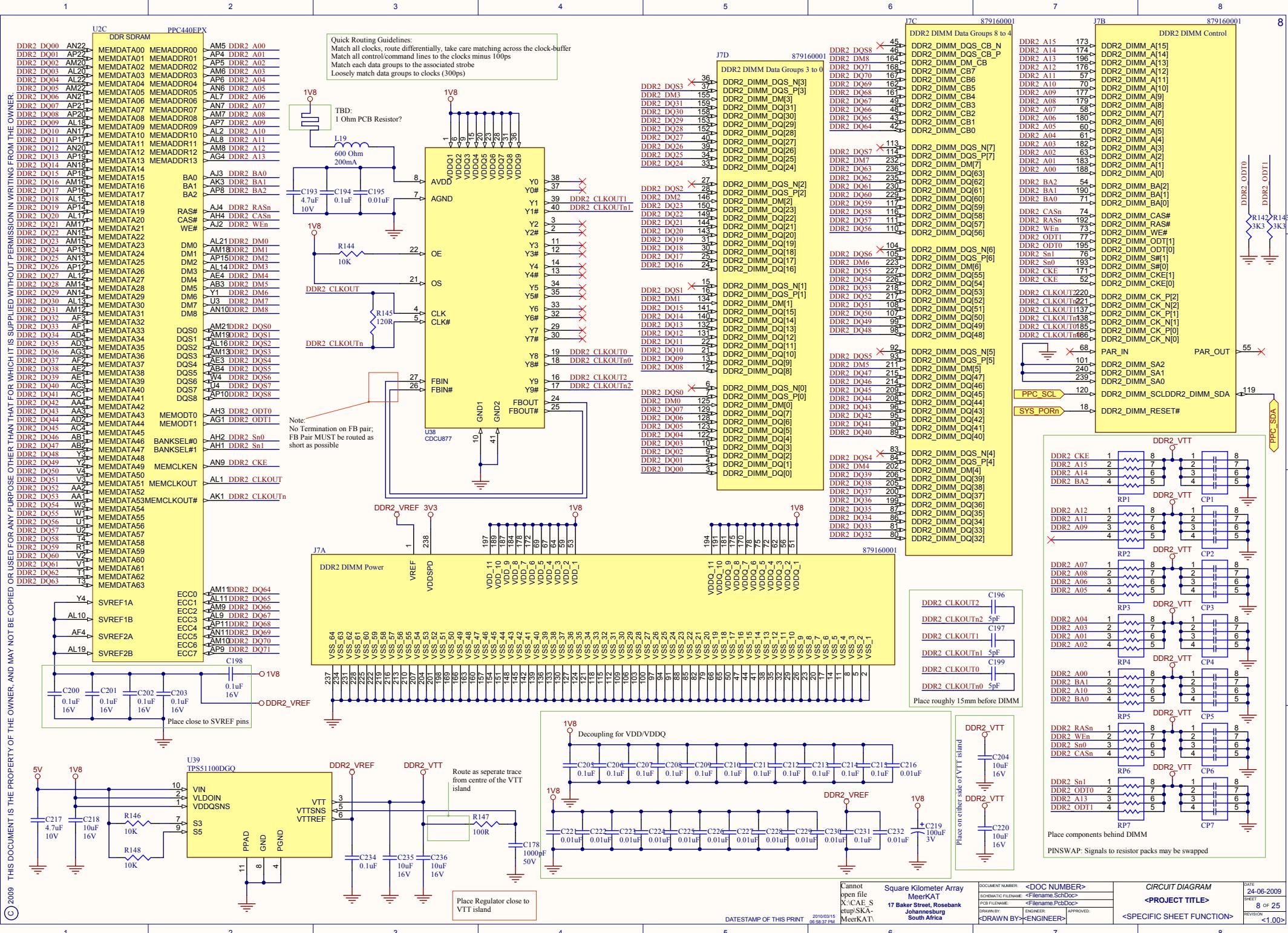
EPB.PERCLK	EPB	PERCLK
EPB.PADDR[5..31]	PADDR[5..31]	
EPB.PDATA[0..31]	PDATA[0..31]	
EPB.PBen[0..31]	PBen[0..31]	
EPB.POEn	POEn	
EPB.PWRn	PWRn	
EPB.PBLASTn	PBLASTn	
EPB.PRDY	PRDY	
EPB.PCSn_FPGA0	PCSn_FPGA0	
EPB.PCSn_CPLD	PCSn CPLD	
EPB.PCSn_SMAP	PCSn_SMAP	
EPB.PCSn_FPGA1	PCSn FPGA1	

Routing constraint:
EPB signals should be loosely length matched (500ps)

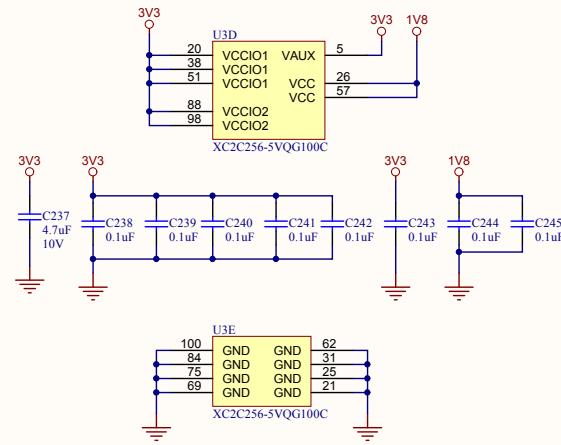
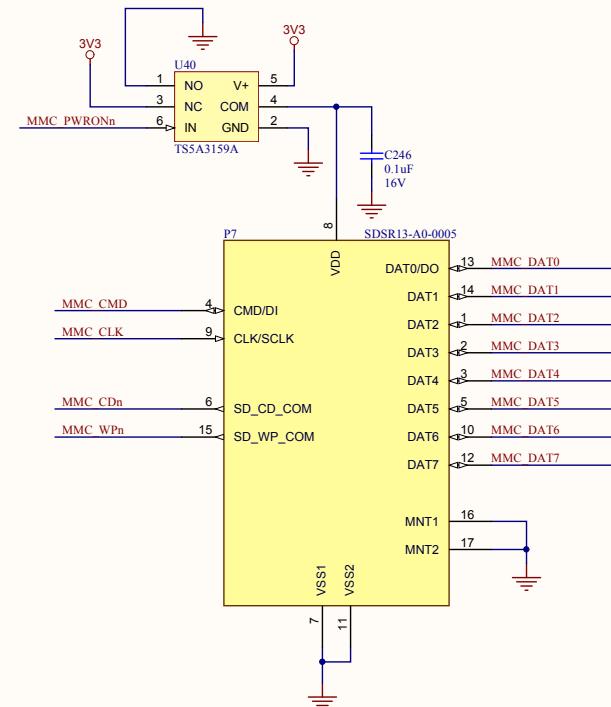
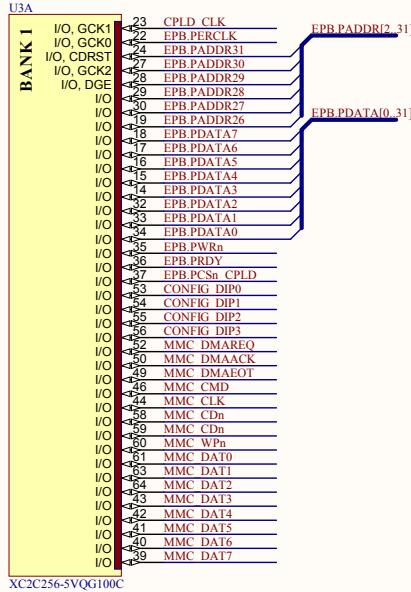


A B C D

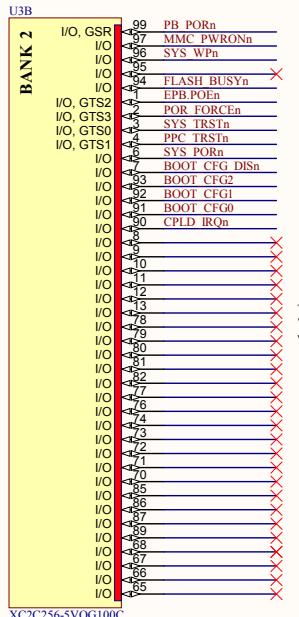
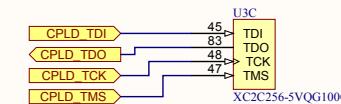




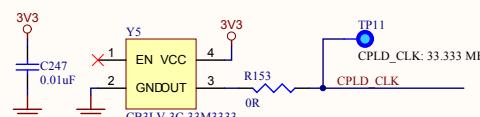
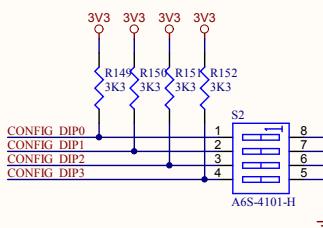
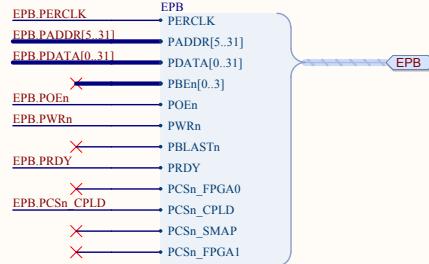
PINSWAP:
CPLD_CLK and EPB.PERCLK MUST go on GCK pins
SYS_PORn MUST go on GSR pin
EPB.POEn MUST go on GTS pin
Otherwise, swap away

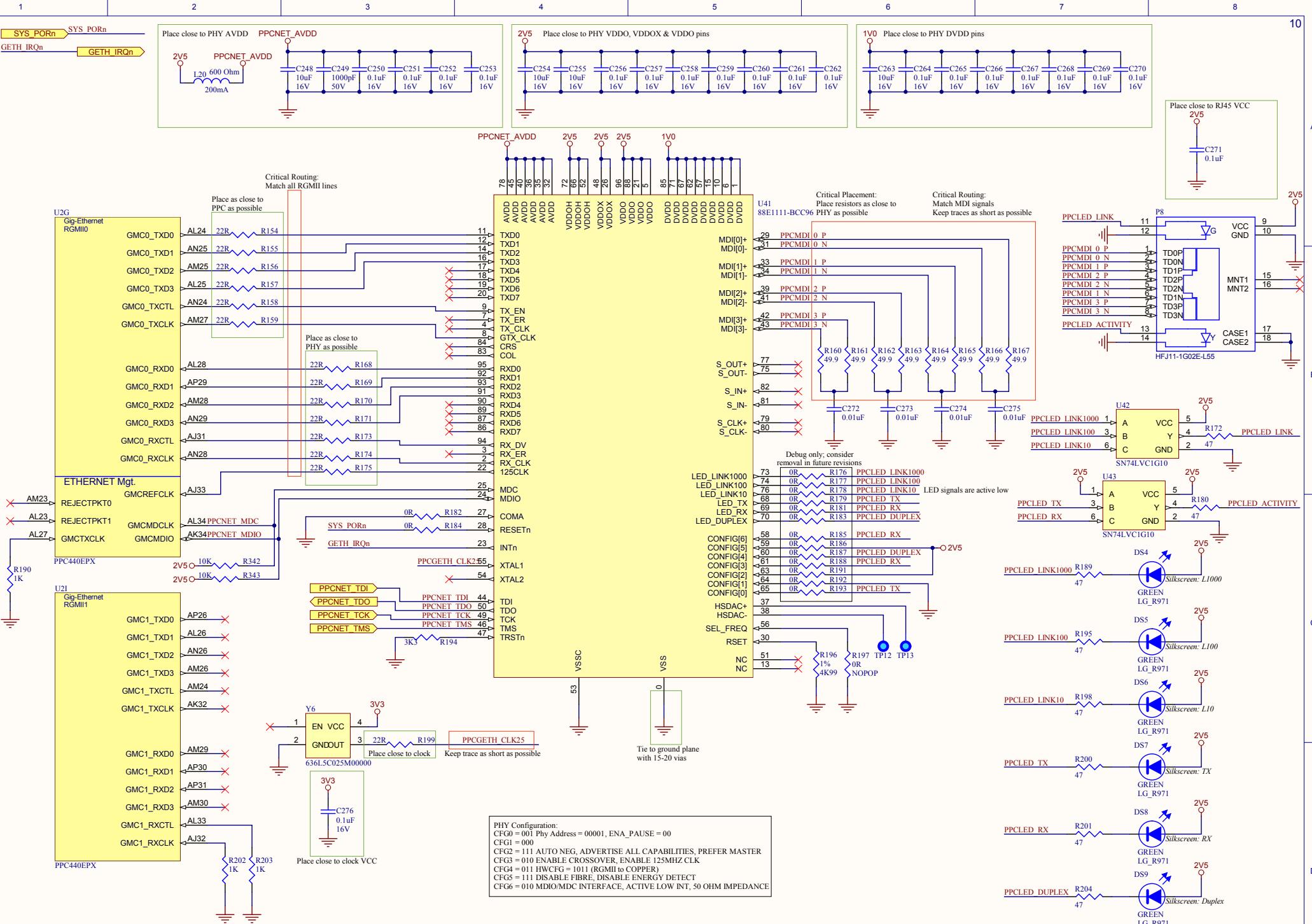


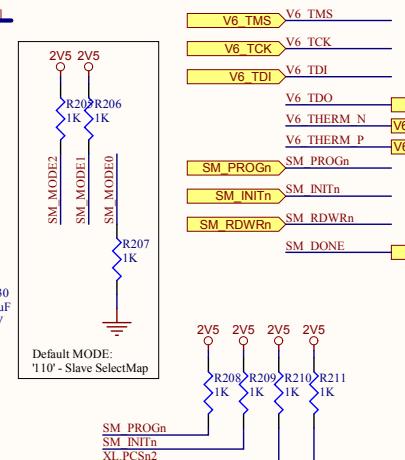
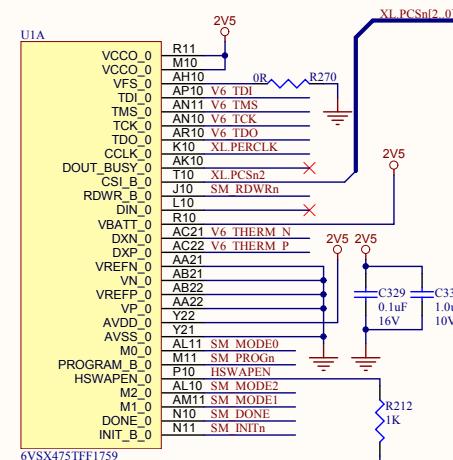
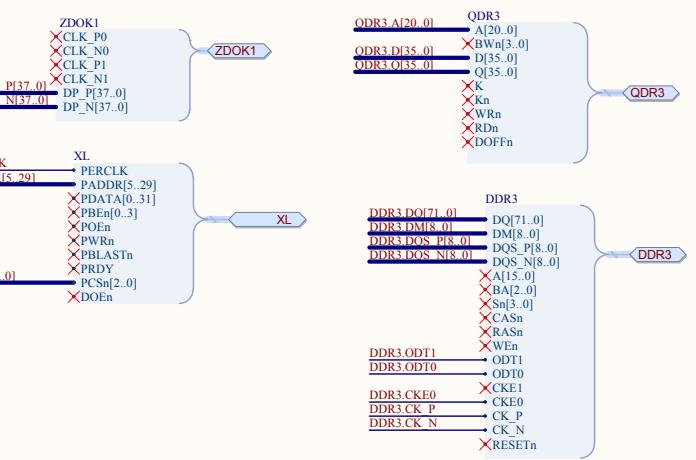
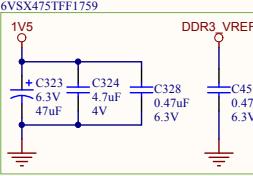
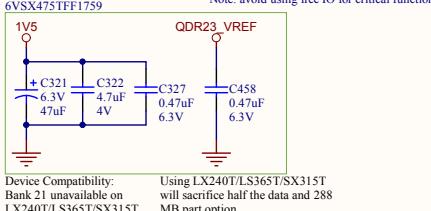
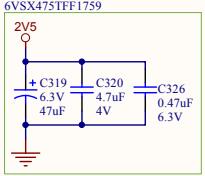
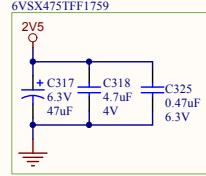
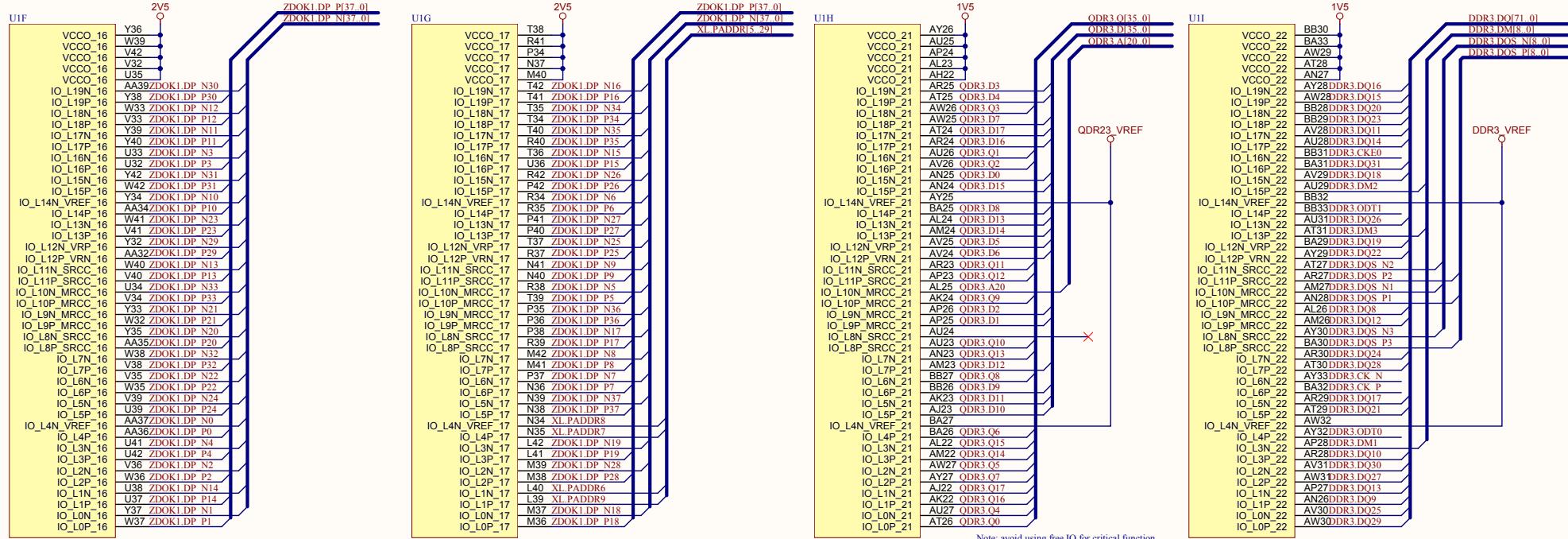
SEQUENCING NOTE:
Xilinx recommends that the IO voltage (3v3) be applied after VCC(1v8).
Too bad.



TODO:
We have LOTS of IO free
what else can we do with them







Note:
External sensors will be used for monitoring,
hence the lack of more rigorous filtering

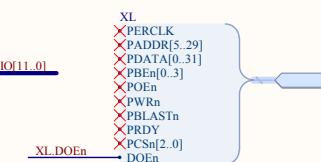
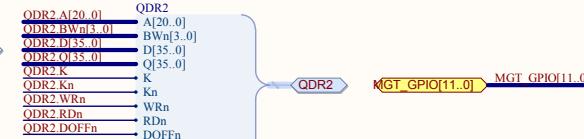
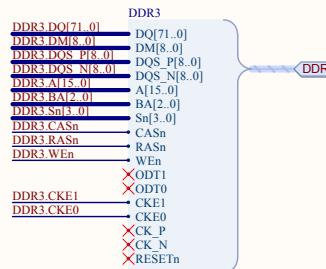
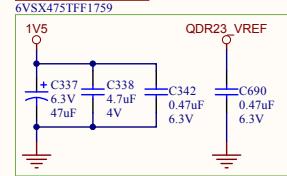
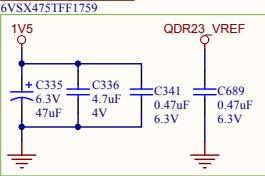
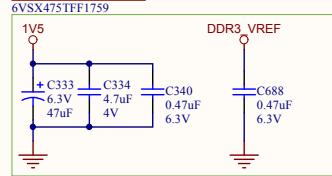
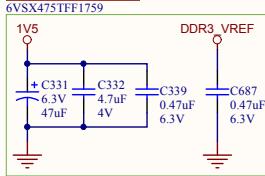
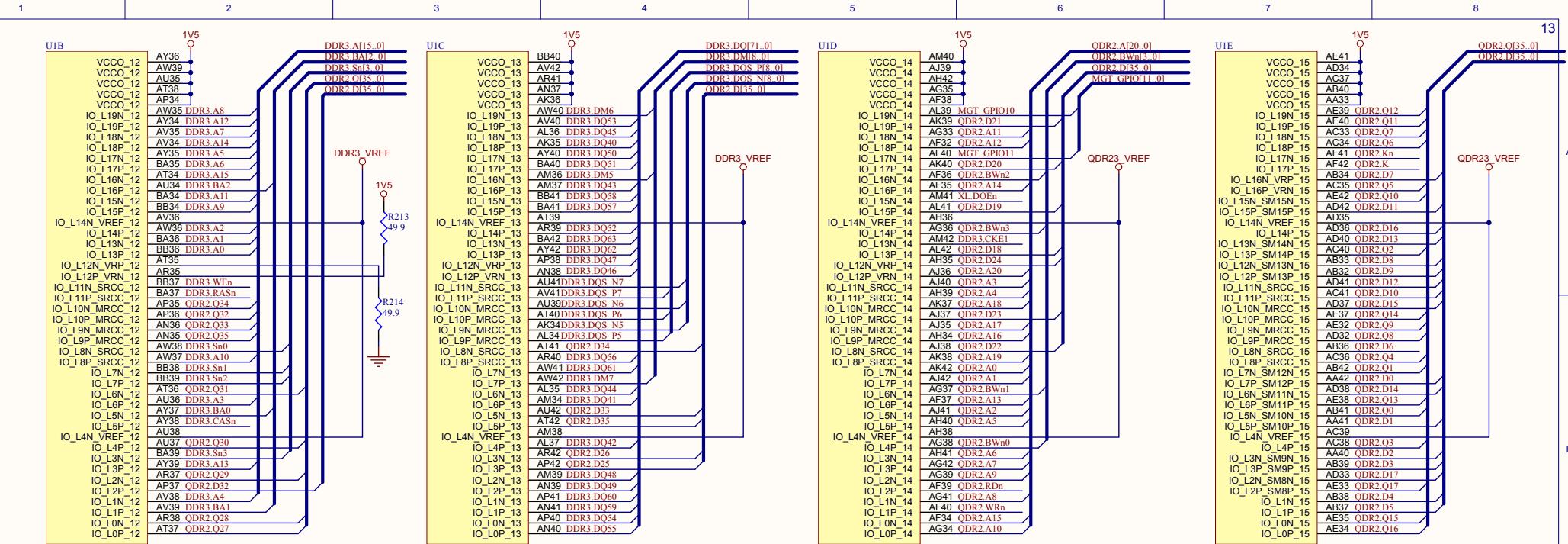
DATESTAMP OF THIS PRINT 2010/03/15 06:58:39 PM

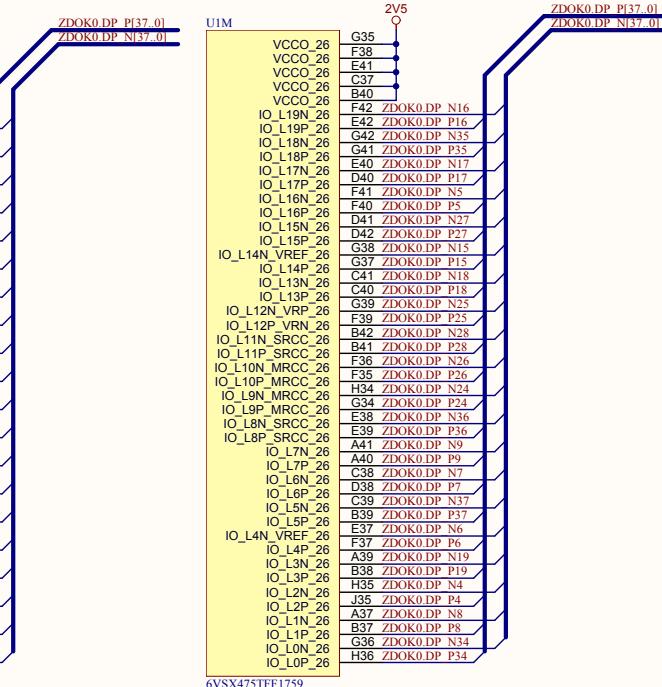
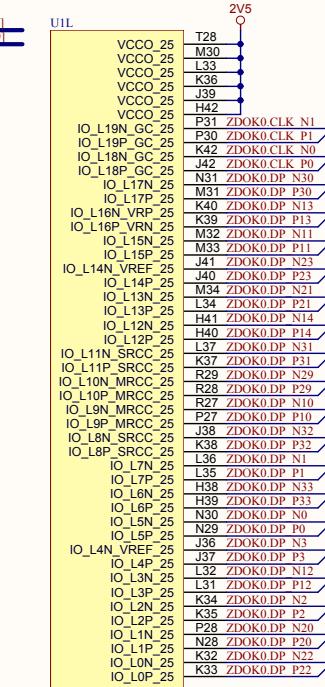
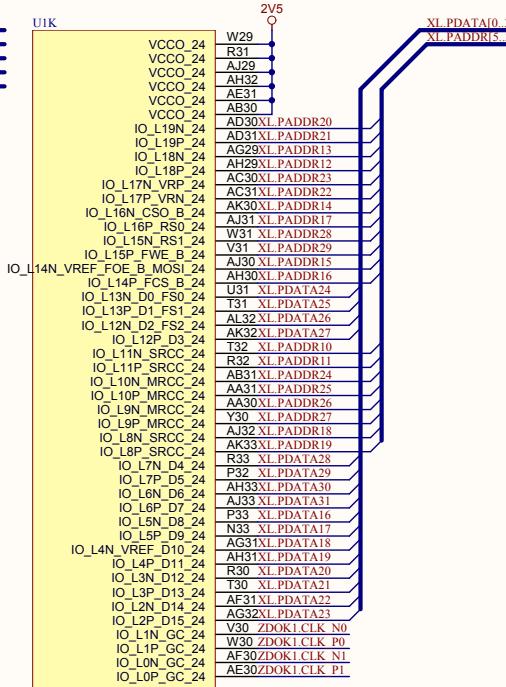
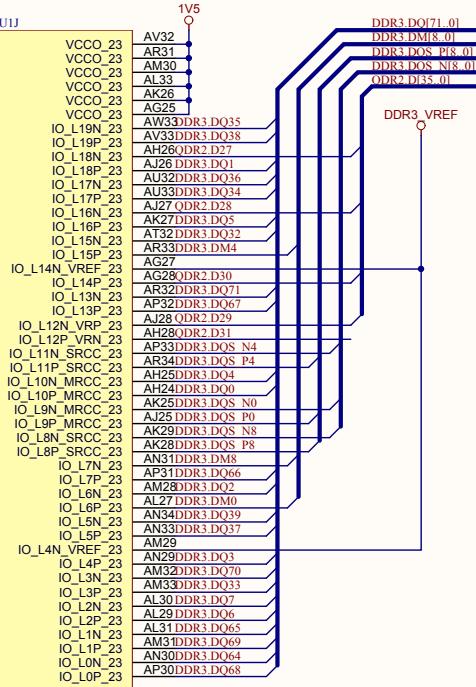
Square Kilometer Array
MeerKAT
17 Baker Street, Rosebank
Johannesburg
South Africa

DOCUMENT NUMBER: <DOC NUMBER>
SCHEMATIC FILENAME: <Filename.SchDoc>
PCB FILENAME: <Filename.PcbDoc>
DRAWN BY: <DRAWN BY>
ENGINEER: <ENGINEER>
APPROVED: <APPROVED>

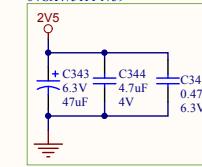
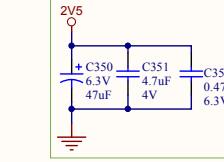
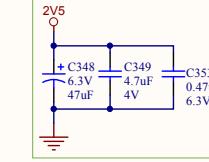
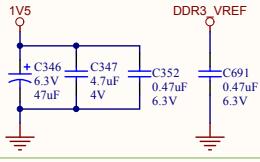
CIRCUIT DIAGRAM
<PROJECT TITLE>
<SPECIFIC SHEET FUNCTION>

DATE: 24-06-2009
SHEET: 12 of 25
REVISION: <1.00>

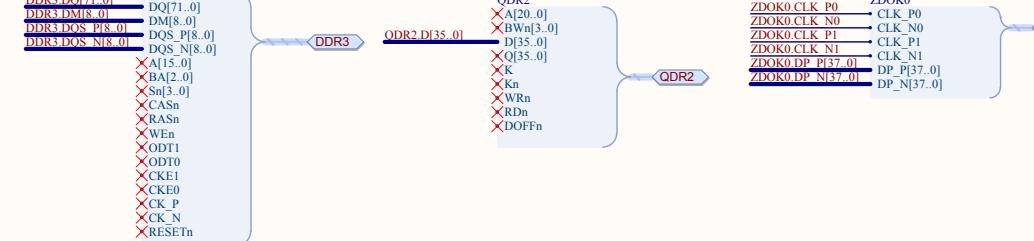




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DATESTAMP OF THE

PRINT 2010/03/15
06:58:39 PM

Cannot open file
X:\CAE_S
etup\SKA-
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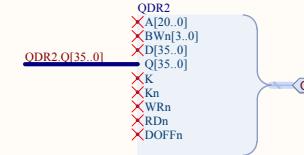
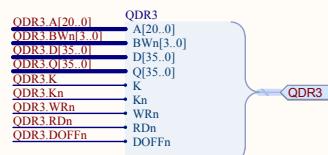
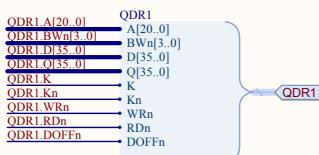
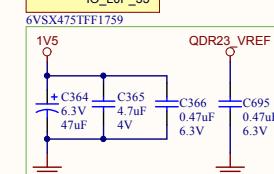
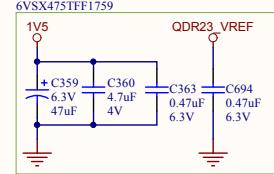
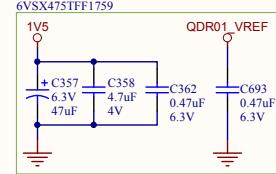
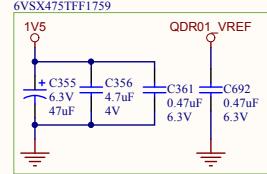
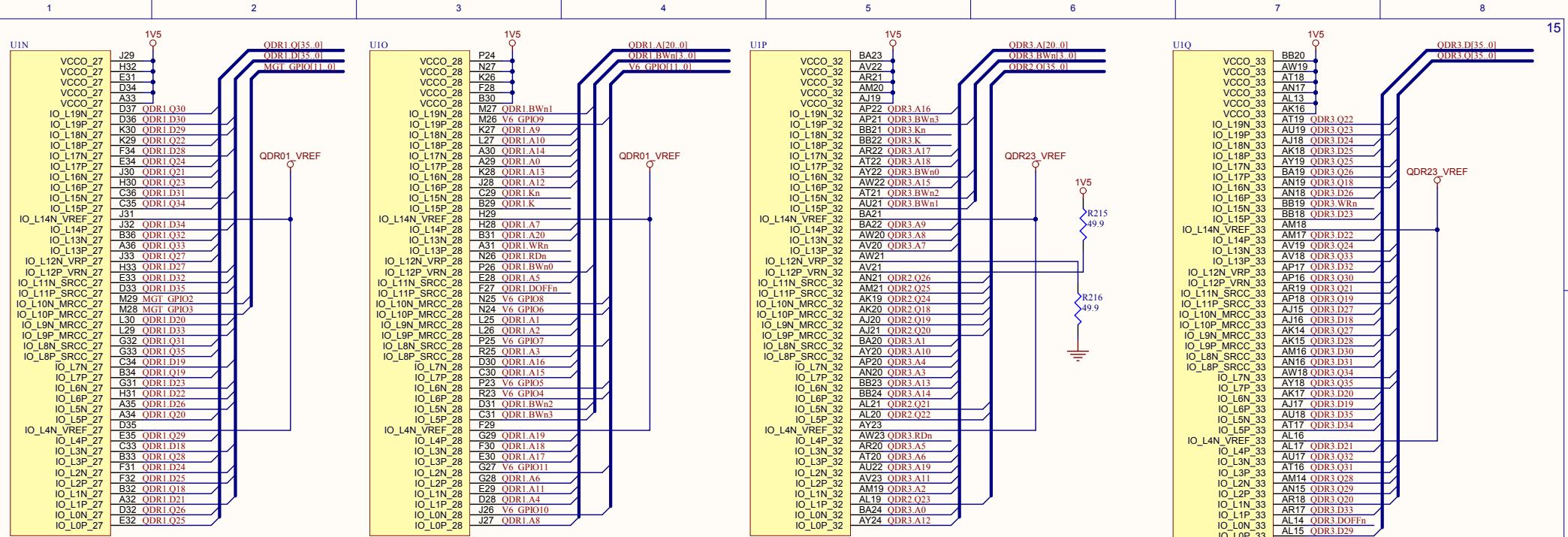
rometer Array
rKAT
DDR_XLESS_1
DDR_XLESS_2
DDR_XLESS_3

MENT NUMBER: <DOC M
AUTOMATIC FILENAME: <Filename
FILENAME: ADDR_XREF<filename
DRAWN BY: ><ENGINEER
IN BY: ENGINEER:

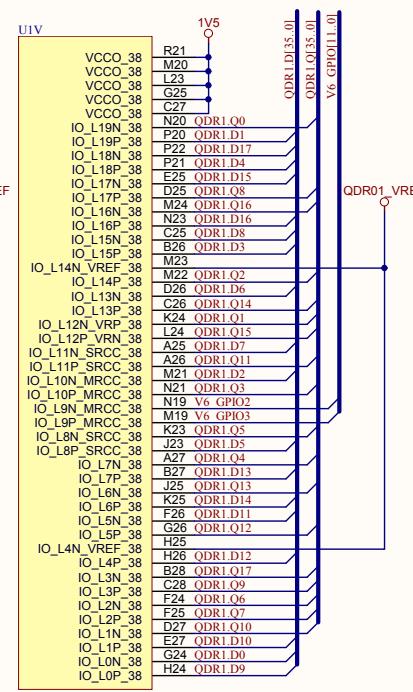
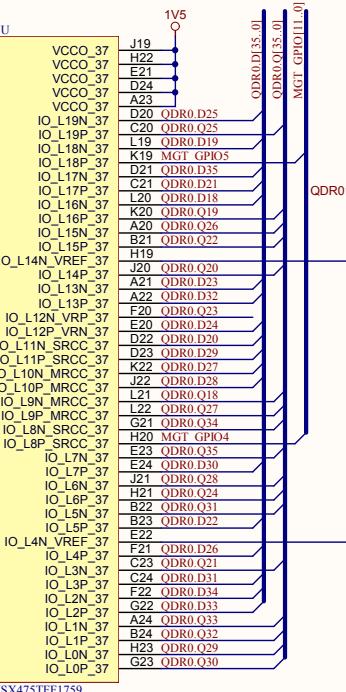
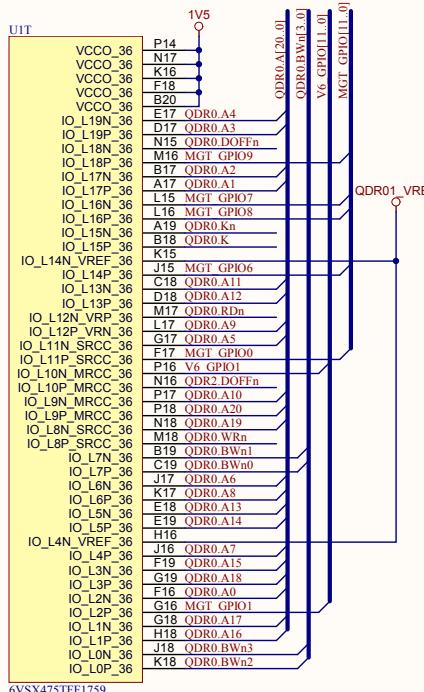
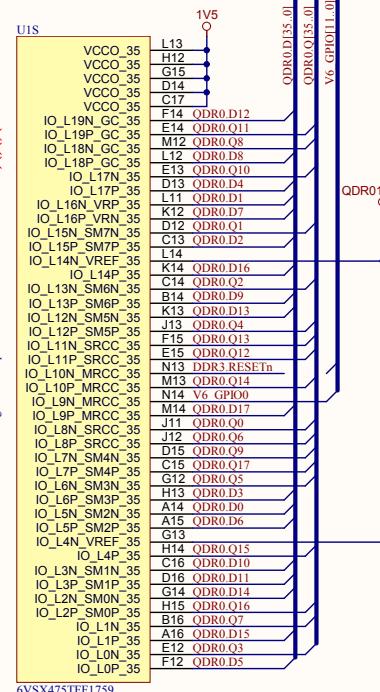
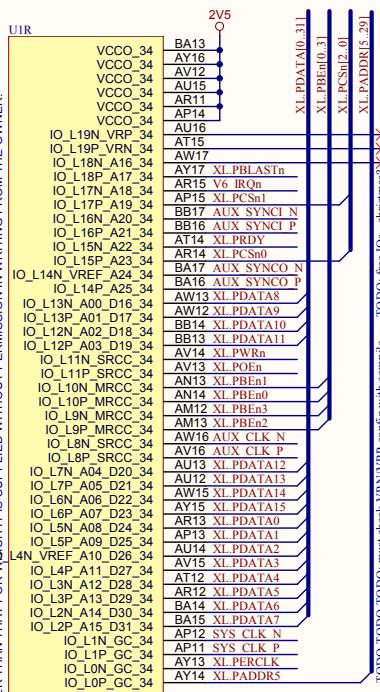
NUMBER>
EhDoc>
2005-Xless3
cbDoc>

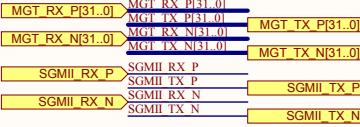
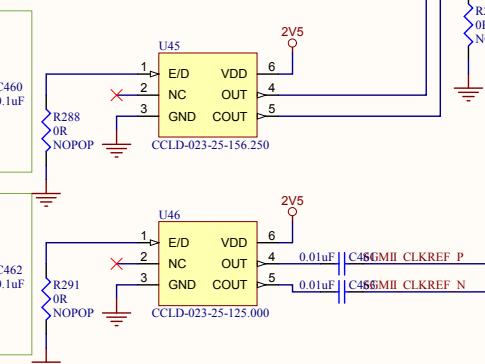
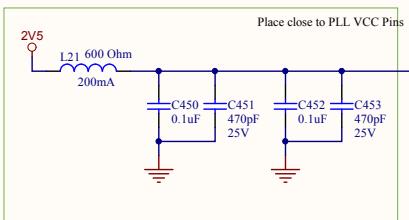
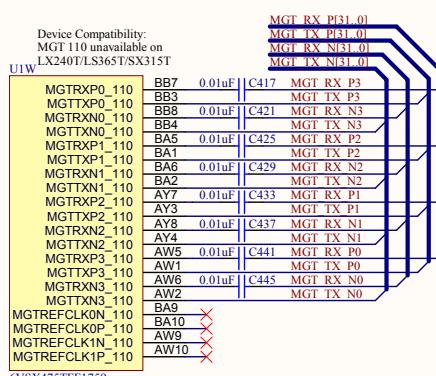
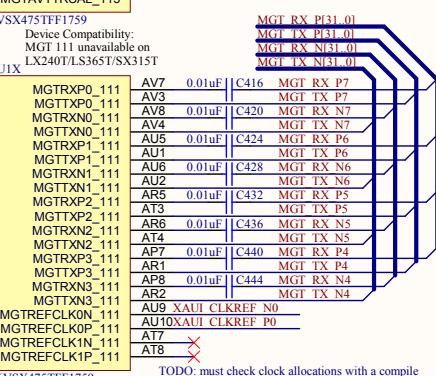
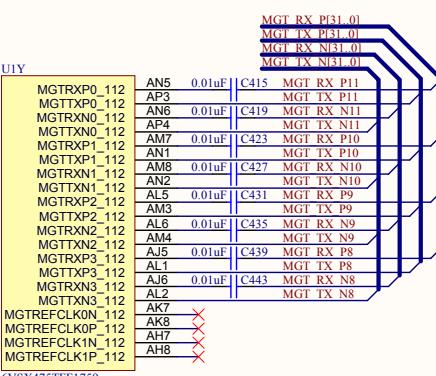
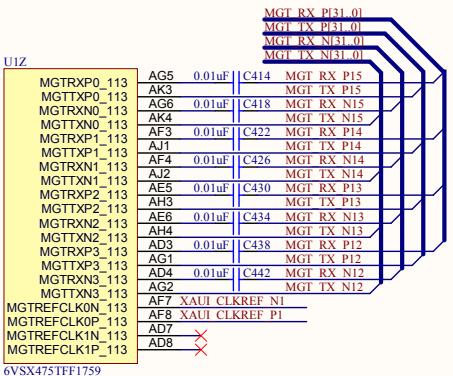
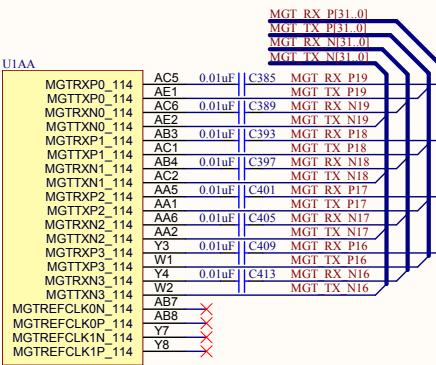
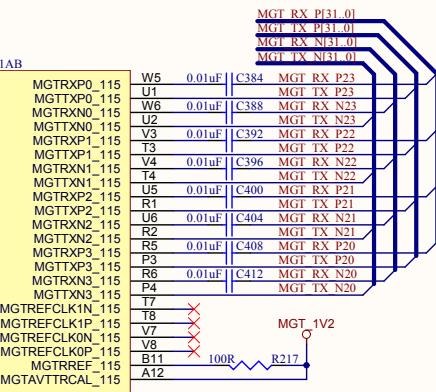
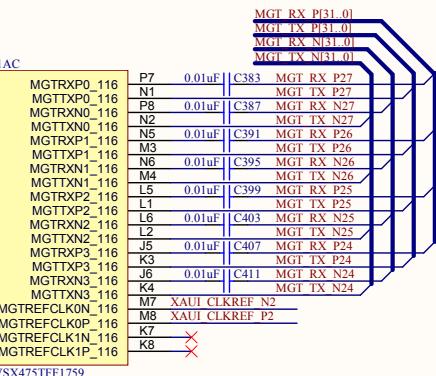
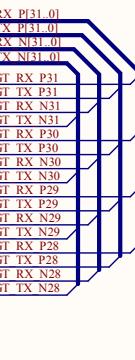
CIRCUIT DIAGRAM
<PROJECT 1>
SPECIFIC SHEET

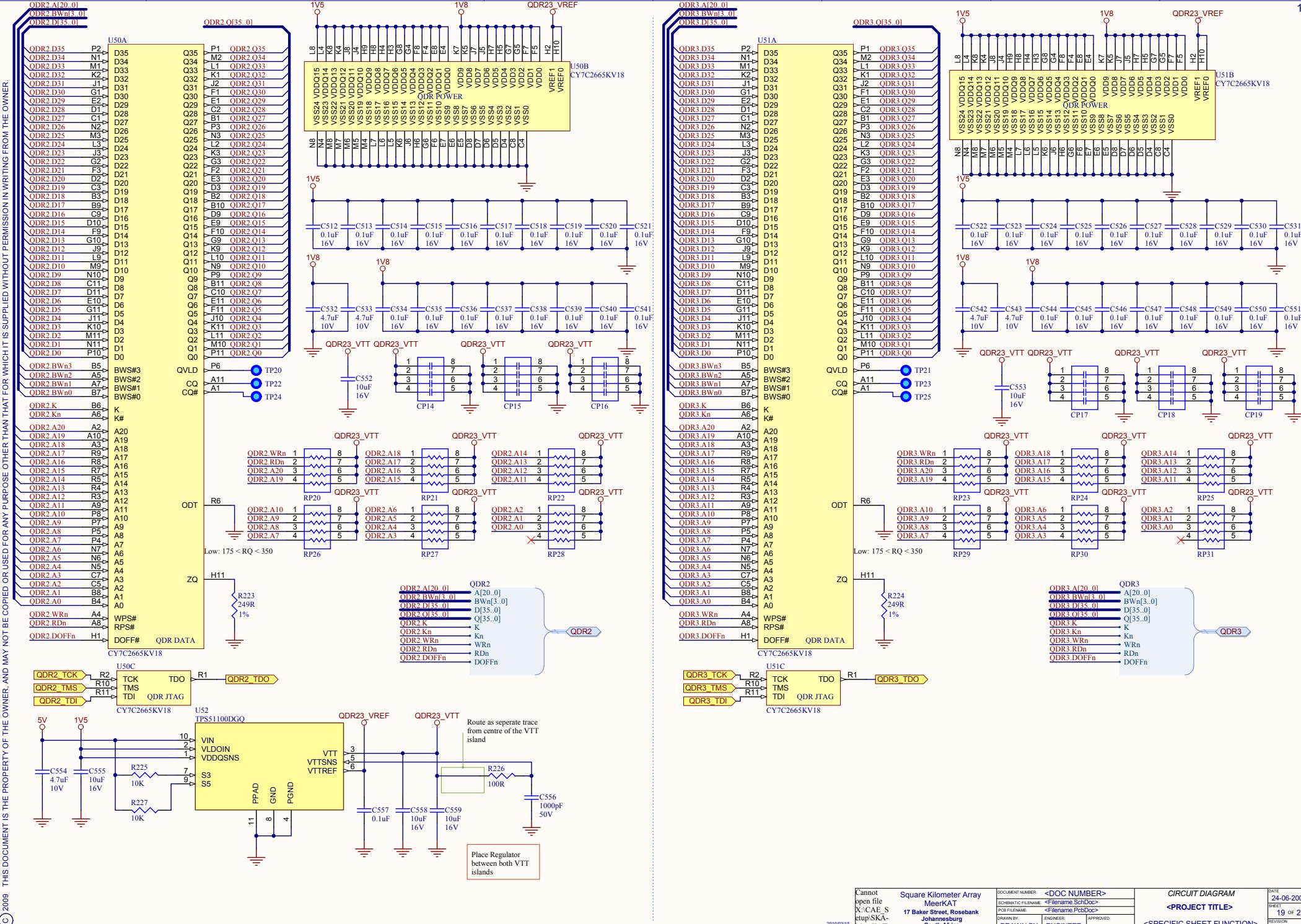
RAM
TITLE>
FUNCTION>



A



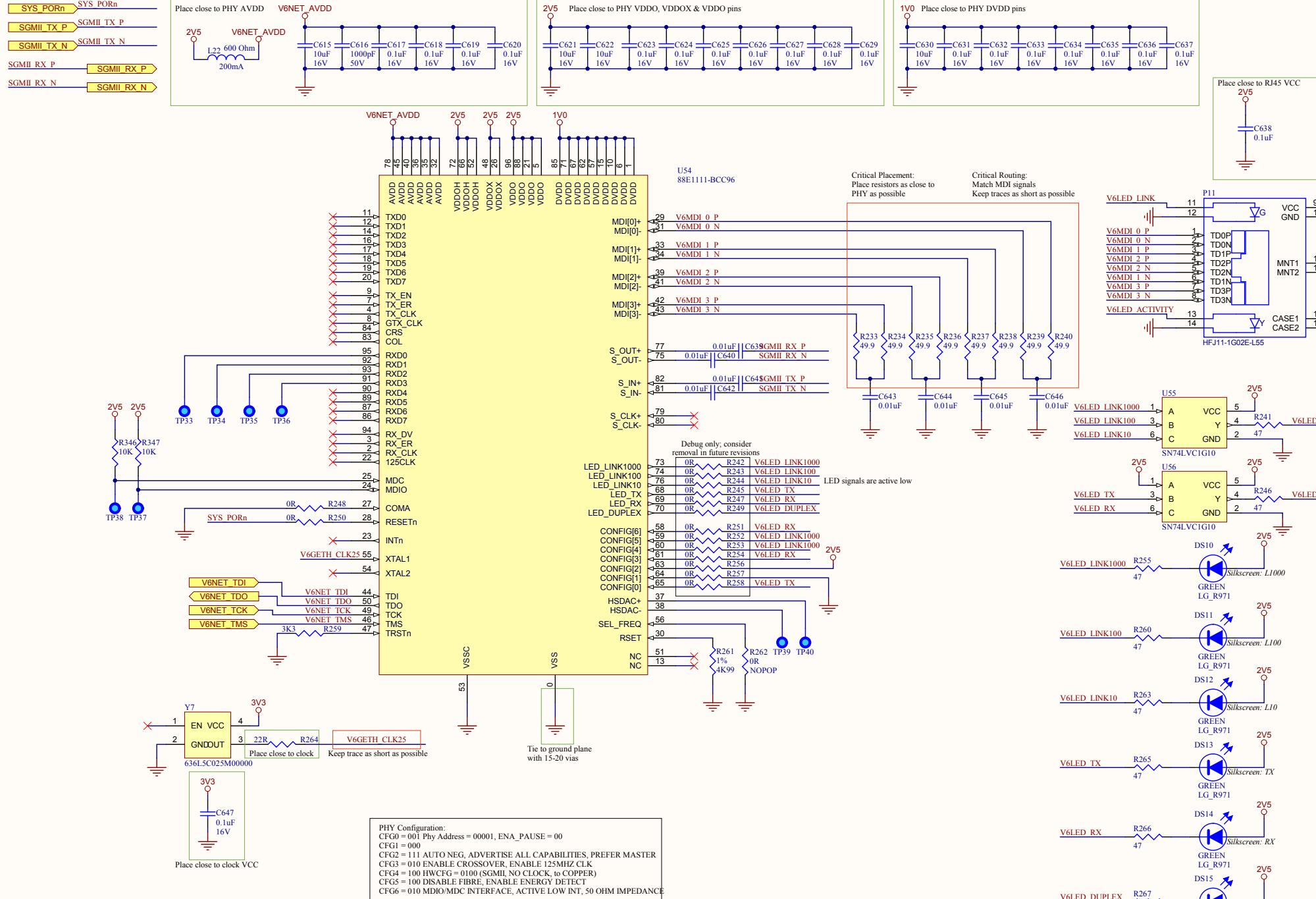


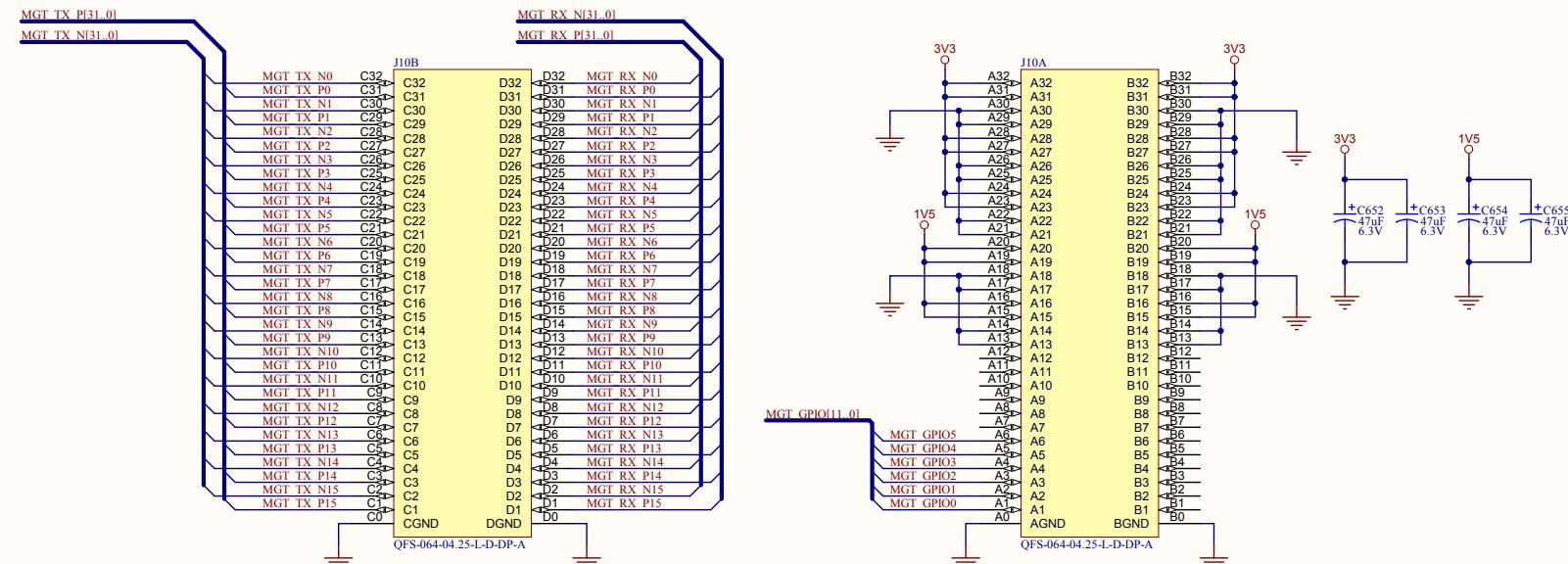
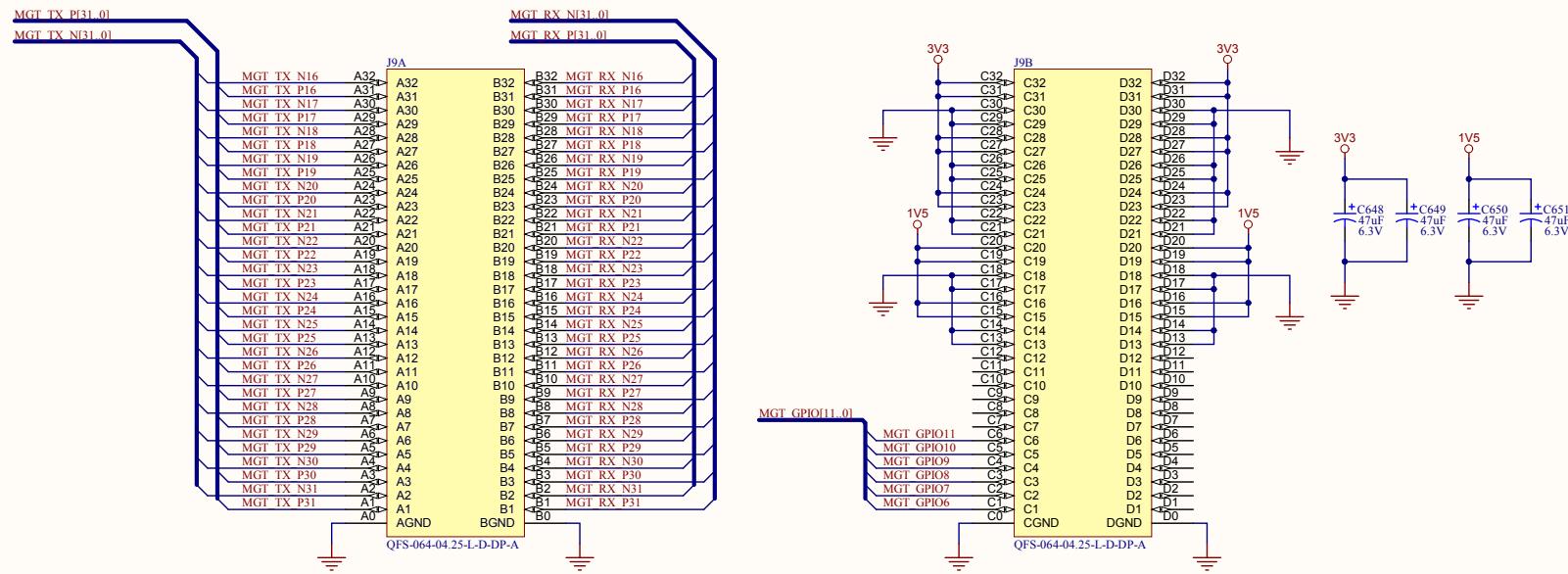


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MGT_RX_P[31..0] **MGT_RX_P[31..0]** **MGT_RX_P[31..0]**
MGT_RX_N[31..0] **MGT_RX_N[31..0]** **MGT_RX_N[31..0]**
MGT_GPIO[11..0] **MGT_GPIO[11..0]**

DATESTAMP OF THIS PRINT

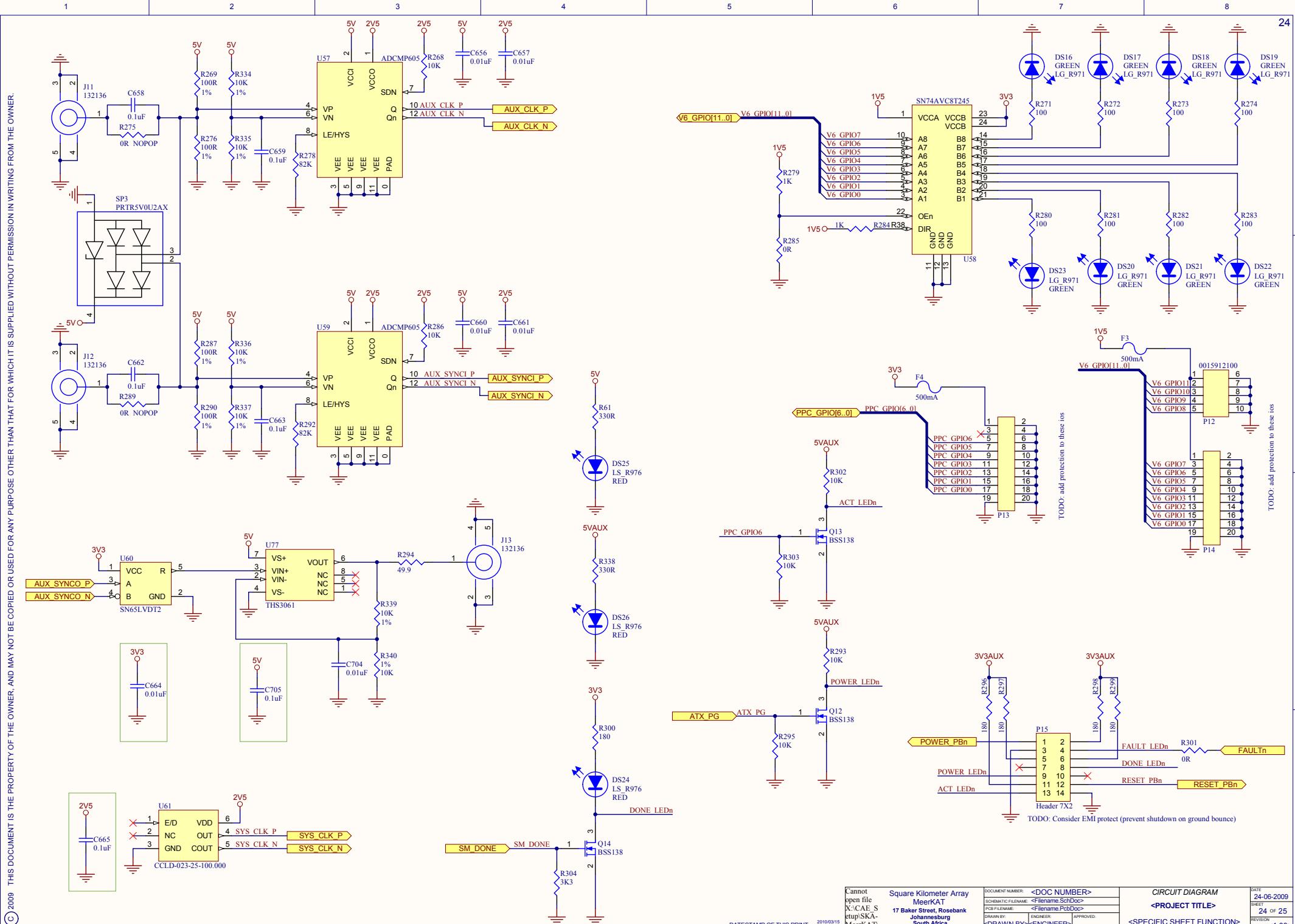
2010/03/15
06:58:43 PM

Cannot open file
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etup\SKA-
MeerKAT

Square Kilometer Array
MeerKAT
17 Baker Street, Rosebank
Johannesburg
South Africa

DOCUMENT NUMBER: <DOC NUMBER>
SCHEMATIC FILENAME: <Filename.SchDoc>
PCB FILENAME: <Filename.PcbDoc>

CIRCUIT DIAGRAM
<PROJECT TITLE>
DATE: 24-06-2009
SHEET: 23 of 25
DRAWN BY: <DRAWN BY>
ENGINEER: <ENGINEER> APPROVED:
<SPECIFIC SHEET FUNCTION>
REVISION: <1.00>



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