

# SriKrishna Chaitanya Kompella

Grad student at UMass Amherst interested in Machine Learning, Natural Language Processing and AI

## CONTACT

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## LINKS

skckompella.com  
linkedin.com/in/skckompella  
github.com/skckompella

## SKILLS

### PROGRAMMING

Over 5000 lines:

- C • C++ • Bash

Over 1000 lines:

- Python • MATLAB

Familiar:

- Java • PHP • Perl

- HTML/CSS • JavaScript

### TOOLS AND FRAMEWORKS

- OpenCV

- NLTK

- NumPy, SciPy

### DOMAIN KNOWLEDGE

Systems

- CPU, GPU and SoC Arch

- SPI, I2C, UART, CAN

- Virtualization on ARM SoCs

- Automotive safety

## ACHIEVEMENTS

**Fastest promotion** (in 1 year)  
(from a new grad to IC Level 2)

**Youngest Tech Lead**  
(usually requires 4 years exp)

**Youngest Project Lead**  
(usually requires 4 years exp)

**Among Top 5 projects**  
Undergrad Capstone project

## EXTRA-

## CURRICULAR

- Conducted workshop for about 120 people to foster application oriented learning
- Organized inter-collegiate cultural festival for three years; serving in the core committee for the final year

## EDUCATION

**UNIVERSITY OF MASSACHUSETTS AMHERST** | MS IN COMPUTER SCIENCE

Expected Graduation: May 2018

Courses: Machine Learning - Pattern Recognition, Natural Language Processing

**STANFORD CENTER FOR PROFESSIONAL DEVELOPMENT** | NON DEGREE

Courses: CS110 - Principles of Computing

**PES INSTITUTE OF TECHNOLOGY** | BE IN INFORMATION SCIENCE

Graduated: 2013 | Major GPA: 3.67

Courses: Pattern Recognition, Digital Image Processing, Data Mining

## EXPERIENCE

**NVIDIA CORP. (AUTOMOTIVE)** | TECH LEAD - VIRTUALIZATION

Nov 2014 – Jul 2016 | Bangalore, India

- Responsible for delivering test strategies, and related automation tools for validating Nvidia's hypervisor technology deployed in Drive PX and Drive CX products and aimed at autonomous vehicles, clusters and infotainment systems
- Solely responsible for test development for System Software, Graphics, Multimedia, Resource Manager and Security in virtualized environments

**NVIDIA CORP. (AUTOMOTIVE)** | PROJECT LEAD (QA)

Oct 2015 – Feb 2016 | Bangalore, India

- Responsible for both hardware and software quality
- Led a team of six people in a fast paced project with ever changing requirements
- Deftly interfacing with a complex matrixed organization with more than five verticals contributing to the project

**NVIDIA CORP. (AUTOMOTIVE)** | EMBEDDED QA

Jul 2013 – Oct 2014 | Bangalore, India

- Developed a profiling tool to profile system stats
- Developed a tool to profile Tegra error registers
- Ported and enhanced a tool to report memory controller usage to a newer chip
- Numerous automation tools

## SELECT PROJECTS

**INDEXING VERY LARGE FILES** | C++, MAPREDUCE, MULTITHREADING

May 2015 – Jun 2015

Multithreaded server (self implemented) controlling distributed workloads using Mapreduce paradigm

**HTTP WEB PROXY AND CACHE** | C++, MULTITHREADING

Apr 2015 – May 2015

Scalable multithreaded server capable of blacklisting and proxy chaining

**SMART VIDEO SURVEILLANCE** | C++, OPENCV

Dec 2012 – Mar 2013

Undergraduate capstone project to detect unattended baggage and loitering people

**VOICE CONTROL FOR MEDIA PLAYER** | C++

Mar 2012 – Apr 2012

Media player control (like Play, Pause, Stop, Next, Previous) using voice commands