

Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	15128916
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	PEDRO FRAIRE SOLÍS		

1 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

2 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

3 Assignation

You are assigned to the Group 3, which is author to the Chapter 3 and reviewer to the chapter 4.

- 1. It is duty of the group members to divide work among themselves for writing and reviewing.
- 2. You should write in Microsoft Word. (A free copy of Microsoft office is available to the students of UAdeC.)
- 3. Text of each chapter should at-least contain 15 pages. (including the figures and tables, the chapter may be longer than 15 pages)

- 4. The page should use a margin of 1 inch on all the sides.
- 5. Use the font "Times New Roman", size 11 for normal text. The font size for the first heading should be 28, second heading should be 18.
- 6. Late submission will not be tolerated, it will lead to zero marks.
- 7. Authors and reviewers should exchange documents only through emails with a copy to the professor "gadis-at-uadec-dot-edu-dot-mx".
- 8. Work would be seen as a group task, any mistake in a group will effect all he members of the group.

- 1. This work contain a sum of 100 marks.
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 - (c) Each incorrect statement, explanation, text or image will reduce 5 marks to the reviewer and 5 marks to the author.
 - (d) Each spelling mistake will reduce 10 marks to reviewer and 10 marks to the author.



Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	15132525
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	JULIO CÉSAR LOZANO ALMAGUER		

6 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

7 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

8 Assignation

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Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	15158174
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	JORGE LUIS DÍAZ ENRÍQUEZ		

11 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

12 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

13 Assignation

You are assigned to the Group 4, which is author to the Chapter 4 and reviewer to the chapter 5.

- 1. It is duty of the group members to divide work among themselves for writing and reviewing.
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Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	15149897
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	JULIO CÉSAR GARCÍA CASTILLO		

16 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
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17 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

18 Assignation

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Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	15133897
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	VÍCTOR MANUEL GARCÍA CARRILLO		

21 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

22 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

23 Assignation

You are assigned to the Group 4, which is author to the Chapter 4 and reviewer to the chapter 5.

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Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	15132740
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	JOVANA SOLEDAD GARCÍA REYES		

26 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

27 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

28 Assignation

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Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	15141730
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	VÍCTOR MANUEL PUENTES RODRÍGUEZ		

31 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

32 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

33 Assignation

You are assigned to the Group 2, which is author to the Chapter 2 and reviewer to the chapter 3.

- 1. It is duty of the group members to divide work among themselves for writing and reviewing.
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Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	15153534
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	JOSÉ ANTONIO RINCÓN ACOSTA		

36 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

37 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

38 Assignation

You are assigned to the Group 2, which is author to the Chapter 2 and reviewer to the chapter 3.

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Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	15149344
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	FABIÁN ALONSO SOTO LUNA		

41 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

42 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

43 Assignation

You are assigned to the Group 2, which is author to the Chapter 2 and reviewer to the chapter 3.

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Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	15140545
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	MAYRA SELENE MIRELES CARDOZA		

46 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
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7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

47 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

48 Assignation

You are assigned to the Group 1, which is author to the Chapter 1 and reviewer to the chapter 2.

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Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	15315202
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	ALAN M. CABRERA MORA		

51 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

52 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

53 Assignation

You are assigned to the Group 5, which is author to the Chapter 5 and reviewer to the chapter 1.

- 1. It is duty of the group members to divide work among themselves for writing and reviewing.
- 2. You should write in Microsoft Word. (A free copy of Microsoft office is available to the students of UAdeC.)
- 3. Text of each chapter should at-least contain 15 pages. (including the figures and tables, the chapter may be longer than 15 pages)

- 4. The page should use a margin of 1 inch on all the sides.
- 5. Use the font "Times New Roman", size 11 for normal text. The font size for the first heading should be 28, second heading should be 18.
- 6. Late submission will not be tolerated, it will lead to zero marks.
- 7. Authors and reviewers should exchange documents only through emails with a copy to the professor "gadis-at-uadec-dot-edu-dot-mx".
- 8. Work would be seen as a group task, any mistake in a group will effect all he members of the group.

- 1. This work contain a sum of 100 marks.
- 2. 25 marks for writing.
- 3. 25 marks for reviewing.
- 4. 25 marks for the presentation (i.e. following all the instructions of fonts, writing caption to the figures and tables etc.).
- 5. 25 marks for the corrections.
- 6. Marks will be deducted for the following.
 - (a) Any kind of **plagiarism** in the text or images will be punished. Reviewers will be awarded **Zero** marks and the authors will be awarded 50% of marks.
 - (b) Sending document after the deadline will result in zero marks.
 - (c) Each incorrect statement, explanation, text or image will reduce 5 marks to the reviewer and 5 marks to the author.
 - (d) Each spelling mistake will reduce 10 marks to reviewer and 10 marks to the author.



Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	13056433
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	DANIEL ALEJANDRO CARRILLO HERNÁNDEZ		

56 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

57 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

58 Assignation

You are assigned to the Group 3, which is author to the Chapter 3 and reviewer to the chapter 4.

- 1. It is duty of the group members to divide work among themselves for writing and reviewing.
- 2. You should write in Microsoft Word. (A free copy of Microsoft office is available to the students of UAdeC.)
- 3. Text of each chapter should at-least contain 15 pages. (including the figures and tables, the chapter may be longer than 15 pages)

- 4. The page should use a margin of 1 inch on all the sides.
- 5. Use the font "Times New Roman", size 11 for normal text. The font size for the first heading should be 28, second heading should be 18.
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Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	15122162
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	ORLANDO BARBOZA GARCÍA		

61 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

62 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

63 Assignation

You are assigned to the Group 3, which is author to the Chapter 3 and reviewer to the chapter 4.

- 1. It is duty of the group members to divide work among themselves for writing and reviewing.
- 2. You should write in Microsoft Word. (A free copy of Microsoft office is available to the students of UAdeC.)
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Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	10069634
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	EDUARDO TORRES GOITIA		

66 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

67 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

68 Assignation

You are assigned to the Group 1, which is author to the Chapter 1 and reviewer to the chapter 2.

- 1. It is duty of the group members to divide work among themselves for writing and reviewing.
- 2. You should write in Microsoft Word. (A free copy of Microsoft office is available to the students of UAdeC.)
- 3. Text of each chapter should at-least contain 15 pages. (including the figures and tables, the chapter may be longer than 15 pages)

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Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	15157355
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	VICTOR SIFUENTES VARGAS		

71 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

72 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

73 Assignation

You are assigned to the Group 5, which is author to the Chapter 5 and reviewer to the chapter 1.

- 1. It is duty of the group members to divide work among themselves for writing and reviewing.
- 2. You should write in Microsoft Word. (A free copy of Microsoft office is available to the students of UAdeC.)
- 3. Text of each chapter should at-least contain 15 pages. (including the figures and tables, the chapter may be longer than 15 pages)

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Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	14576492
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	JONATHAN RODRÍGUEZ CHÁVEZ		

76 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

77 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

78 Assignation

You are assigned to the Group 1, which is author to the Chapter 1 and reviewer to the chapter 2.

- 1. It is duty of the group members to divide work among themselves for writing and reviewing.
- 2. You should write in Microsoft Word. (A free copy of Microsoft office is available to the students of UAdeC.)
- 3. Text of each chapter should at-least contain 15 pages. (including the figures and tables, the chapter may be longer than 15 pages)

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Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	15129708
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	LUIS FERNANDO CASTAÑEDA QUIROGA		

81 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

82 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

83 Assignation

You are assigned to the Group 2, which is author to the Chapter 2 and reviewer to the chapter 3.

- 1. It is duty of the group members to divide work among themselves for writing and reviewing.
- 2. You should write in Microsoft Word. (A free copy of Microsoft office is available to the students of UAdeC.)
- 3. Text of each chapter should at-least contain 15 pages. (including the figures and tables, the chapter may be longer than 15 pages)

- 4. The page should use a margin of 1 inch on all the sides.
- 5. Use the font "Times New Roman", size 11 for normal text. The font size for the first heading should be 28, second heading should be 18.
- 6. Late submission will not be tolerated, it will lead to zero marks.
- 7. Authors and reviewers should exchange documents only through emails with a copy to the professor "gadis-at-uadec-dot-edu-dot-mx".
- 8. Work would be seen as a group task, any mistake in a group will effect all he members of the group.

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 - (d) Each spelling mistake will reduce 10 marks to reviewer and 10 marks to the author.



Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	14317737
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	VANESA IRANÍ MORA MORENO		

86 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

87 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

88 Assignation

You are assigned to the Group 1, which is author to the Chapter 1 and reviewer to the chapter 2.

- 1. It is duty of the group members to divide work among themselves for writing and reviewing.
- 2. You should write in Microsoft Word. (A free copy of Microsoft office is available to the students of UAdeC.)
- 3. Text of each chapter should at-least contain 15 pages. (including the figures and tables, the chapter may be longer than 15 pages)

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Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	07272835
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	CASTREJÓN ALFARO SERGIO PABLO		

91 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

92 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

93 Assignation

You are assigned to the Group 5, which is author to the Chapter 5 and reviewer to the chapter 1.

- 1. It is duty of the group members to divide work among themselves for writing and reviewing.
- 2. You should write in Microsoft Word. (A free copy of Microsoft office is available to the students of UAdeC.)
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Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	08060551
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	MARTÍNEZ MURILLO BRANDON ALAN		

96 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

97 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

98 Assignation

You are assigned to the Group 5, which is author to the Chapter 5 and reviewer to the chapter 1.

- 1. It is duty of the group members to divide work among themselves for writing and reviewing.
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 - (c) Each incorrect statement, explanation, text or image will reduce 5 marks to the reviewer and 5 marks to the author.
 - (d) Each spelling mistake will reduce 10 marks to reviewer and 10 marks to the author.



Facultad de Ingeniería Mecánica y Eléctrica Unidad Torreón

Subject	Semiconductor physics	Group	4A
Degree	Electrical engineering	Date	15/05/2017
Exam / Homework	Exam 2	Registration #	07058521
Professor's name	Dr. Suresh Kumar Gadi	Marks Obtained	/10
Student's name	MÁRQUEZ RIVERA IVAN OSWALDO		

101 Important dates

Activity #	From	To	Responsible	Activity
1	27-Mar	28-Mar	Editor / professor	Group formation and assignment of the chapters.
2	28-Mar	07-Apr	Authors / student	Writing the chapter contents.
3	07-Apr	28-Apr	Reviewers / student	Review the chapter contents.
4	28-Apr	05-May	Authors / student	Author should make the corrections given by the reviewers.
5	05-May	08-May	Editor / professor	Review the book and give corrections to the authors.
6	08-May	11-May	Authors / student	Author should make the corrections suggested by editor.
7	11-May	15-May	Editor / professor	Final version is prepared and published on the website.

102 Chapters

- 1. Semiconductor materials
- 2. Diodes
- 3. Bipolar Junction Transistor
- 4. Field Effect Transistor
- 5. Experiment design to characterize semiconductor components

103 Assignation

You are assigned to the Group 5, which is author to the Chapter 5 and reviewer to the chapter 1.

- 1. It is duty of the group members to divide work among themselves for writing and reviewing.
- 2. You should write in Microsoft Word. (A free copy of Microsoft office is available to the students of UAdeC.)
- 3. Text of each chapter should at-least contain 15 pages. (including the figures and tables, the chapter may be longer than 15 pages)

- 4. The page should use a margin of 1 inch on all the sides.
- 5. Use the font "Times New Roman", size 11 for normal text. The font size for the first heading should be 28, second heading should be 18.
- 6. Late submission will not be tolerated, it will lead to zero marks.
- 7. Authors and reviewers should exchange documents only through emails with a copy to the professor "gadis-at-uadec-dot-edu-dot-mx".
- 8. Work would be seen as a group task, any mistake in a group will effect all he members of the group.

- 1. This work contain a sum of 100 marks.
- 2. 25 marks for writing.
- 3. 25 marks for reviewing.
- 4. 25 marks for the presentation (i.e. following all the instructions of fonts, writing caption to the figures and tables etc.).
- 5. 25 marks for the corrections.
- 6. Marks will be deducted for the following.
 - (a) Any kind of **plagiarism** in the text or images will be punished. Reviewers will be awarded **Zero** marks and the authors will be awarded 50% of marks.
 - (b) Sending document after the deadline will result in zero marks.
 - (c) Each incorrect statement, explanation, text or image will reduce 5 marks to the reviewer and 5 marks to the author.
 - (d) Each spelling mistake will reduce 10 marks to reviewer and 10 marks to the author.