```
QEMU
SeaBIOS (version rel-1.16.3-0-ga6ed6b701f0a-prebuilt.qemu.org)

iPXE (http://ipxe.org) 00:03.0 CA00 PCI2.10 PnP PMM+1EFD10B0+1EF310B0 CA00

Booting from Hard Disk...
cpu0: starting 0
sb: size 1000 nblocks 941 ninodes 200 nlog 30 logstart 2 inodestart 32 bmap star t 58
init: starting sh
12341140_KESHAU MISHRA$
```

Q2:

```
QEMU

SeaBIOS (version rel-1.16.3-0-ga6ed6b701f0a-prebuilt.qemu.org)

iPXE (http://ipxe.org) 00:03.0 CA00 PCI2.10 PnP PMM+1EFD10B0+1EF310B0 CA00

Booting from Hard Disk...
cpu0: starting 0
sb: size 1000 nblocks 941 ninodes 200 nlog 30 logstart 2 inodestart 32 bmap star t 58
init: starting sh
12341140_KESHAV MISHRA$ testtoupper
Before: Keshav Mishra
After: KESHAV MISHRA
pid 3 testtoupper: trap 14 err 5 on cpu 0 eip 0xfffffff addr 0xffffffff--kill p
roc
12341140_KESHAV MISHRA$
```

Q3:

Q4:

```
QEMU

SeaBIOS (version rel-1.16.3-0-ga6ed6b701f0a-prebuilt.qemu.org)

iPXE (http://ipxe.org) 00:03.0 CA000 PCI2.10 PnP PMM+1EFD10B0+1EF310B0 CA00

Booting from Hard Disk...
cpu0: starting 0
sb: size 1000 nblocks 941 ninodes 200 nlog 30 logstart 2 inodestart 32 bmap star t 58
init: starting sh
12341140_KESHAV MISHRA$ faulttest
Page faults before: 2
Page faults after: 6
12341140_KESHAV MISHRA$
```

```
🚞 LAB_EXAM_12341140_KeshavMishra — -zsh — 98×54
eshavmishra@Keshavs-MacBook-Air LAB_EXAM_12341140_KeshavMishra % vmmap 42739
                          a.out [42739]
Process:
                          /Users/USER/Desktop/*/a.out 0x104414000
Load Address:
Identifier:
                          a.out
                          ARM64
Code Type:
Platform:
Parent Process:
Target Type:
                          live task
                          2025-10-22 17:43:28.099 +0530 2025-10-22 17:43:12.341 +0530 macOS 15.6.1 (24G90)
Date/Time:
Launch Time:
OS Version:
Report Version:
Analysis Tool:
                          /usr/bin/vmmap
Physical footprint: 1009K
Physical footprint (peak): 1025K
Idle exit: untra
                                           untracked
Virtual Memory Map of process 42739 (a.out)
Output report format: 2.4 -- 64-bit process
VM page size: 16384 bytes
Collected with PhysFootprint mode enabled
==== Non-writable regions for process 42739
REGION TYPE
                                                                                [ VSIZE RSDNT DIRTY
                                                                                                                     SWAP] PRT/MAX SHRMOD PU
          REGION DETAIL
                                           104414000-104418000
                                                                                                 16K
                                                                                                                        OK1 r-x/r-x SM=COW
          DATA CONST
                                                                                                                         OK] r--/rw- SM=COW
          /Users/keshavmishra/Desktop/git/cs1301/LAB_EXAM_12341140_KeshavMishra/a.out memory 104428000-104430000 [ 32K 32K 32K 0K] r--/r-- SM=SHM metadata 104430000-104434000 [ 16K 16K 16K 0K] r--/rwx SM=SHM
shared memory
MALLOC metadata
         metadata 104430000-104434000
MallocHelperZone_9x104430000 zone structure
guard page 104438000-10443c000
guard page 104444000-104448000
guard page 104448000-104448000
guard page 104454000-104458000
guard page 104454000-104458000
guard page 104464000-104468000
metadata 104468000-104466000
metadata 104468000-1044670000
MALLOC guard page
MALLOC guard page
                                                                                                                         0K] ---/rwx SM=SHM
                                                                                                              9K
                                                                                                   9K
                                                                                                                              ---/rwx SM=SHM
                                                                                                                        0K] ---/rwx SM=SHM
0K] ---/rwx SM=SHM
                                                                                      16K
16K
                                                                                                  0K
0K
                                                                                                             0K
0K
 MALLOC guard page
MALLOC guard page
MALLOC guard page
                                                                                                                              ---/rwx SM=SHM
                                                                                                 0K
16K
                                                                                                            0K
16K
                                                                                      16K
16K
                                                                                                                              ---/rwx SM=SHM
r--/rwx SM=PRV
 MALLOC guard page
 MALLOC metadata
                                                                                                                         0K1
MALLOC metadata 10446c000-104470000
DefaultMallocZone_0x10446c000 zone structure
STACK GUARD 1679ec000-16b1f0000
                                                                                [ 56.0M
                                                                                                  0K
                                                                                                              0K
                                                                                                                        OK1 ---/rwx SM=NUL
          stack guard for thread 0
          19bccc000-19bd20000
/usr/lib/libobjc.A.dylib
19bd20000-19bdbc000
  TEXT
                                                                                    336K
                                                                                               336K
                                                                                                              0K
                                                                                                                        0K] r-x/r-x SM=COW
                                                                                                                         OK] r-x/r-x SM=COW
                                                                                    624K
           /usr/lib/dyld
```

Q6:

Frames = 3

```
Allocated page 5 at UA 0x8000
Allocated page 6 at UA 0x9000
Allocated page 7 at UA 0xa000
Allocated page 8 at UA 0xb000
Allocated page 9 at UA 0x0
Allocated page 9 at UA 0x0
Allocated page 10 at UA 0x1000
Allocated page 11 at UA 0x2000
Starting FIFO page replacement simulation (frames=3)...
Access page 1: MISS, replacing page -1
Access page 2: MISS, replacing page -1
Access page 3: MISS, replacing page -1
Access page 4: MISS, replacing page 1
Access page 2: MISS, replacing page 2
Access page 2: MISS, replacing page 3
Access page 5: MISS, replacing page 3
Access page 5: MISS, replacing page 4
Access page 6: MISS, replacing page 1
Access page 7: MISS, replacing page 1
Access page 8: MISS, replacing page 1
Access page 8: MISS, replacing page 1
Access page 6: MISS, replacing page 1
Access page 6: MISS, replacing page 1
Access page 6: MISS, replacing page 1
Access page 5: MISS, replacing page 1
```

## Frames = 4

```
QEMU-(Press ^ C G to release Mouse)

Allocated page 5 at UA 0x8000
Allocated page 6 at UA 0x9000
Allocated page 7 at UA 0x0000
Allocated page 8 at UA 0x0000
Allocated page 9 at UA 0x0
Allocated page 10 at UA 0x1000
Allocated page 11 at UA 0x2000
Starting FIFO page replacement simulation (frames=4)...
Access page 1: MISS, replacing page -1
Access page 2: MISS, replacing page -1
Access page 3: MISS, replacing page -1
Access page 4: MISS, replacing page -1
Access page 5: MISS, replacing page 1
Access page 7: HIT
Access page 8: MISS, replacing page 1
Access page 9: MISS, replacing page 1
Access page 1: MISS, replacing page 3
Access page 3: MISS, replacing page 3
Access page 4: MISS, replacing page 4
Access page 5: MISS, replacing page 4
Access page 5: MISS, replacing page 5
Access page 5: MISS, replacing page 1
FIFO simulation completed.
Total hits: 2
Total misses: 10
12341140_KESHAV MISHRA$
```