

Z80-512K is an open source hardware project Documentation and design files: https://github.com/skiselev/Z80-512K Licensed under CERN-OHL-S: https://cern-ohl.web.cern.ch Designed by Sergey Kiselev Sheet: File: Z80-512K.kicad_pcb Title: Z80-512K: RCBus Z80 CPU and Memory Module Size: A4 Date: 2024-01-03 KiCad E.D.A. kicad (6.0.2) **Rev: 2.0** Id: 1/1



