

Sheet: /Chipset/

File: Chipset.kicad\_sch

# **Title: 486 Sandy River Motherboard**

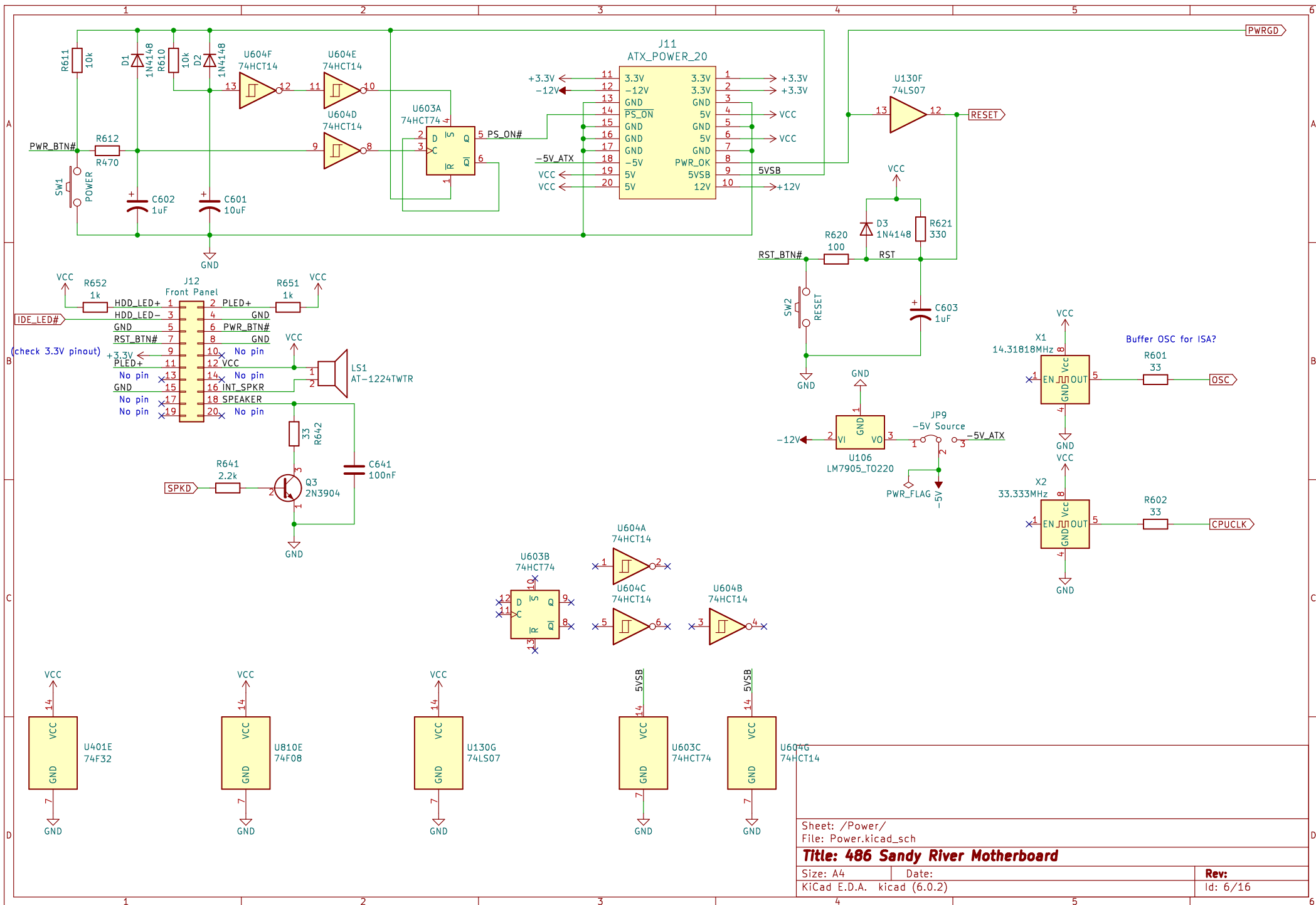
Size: A4

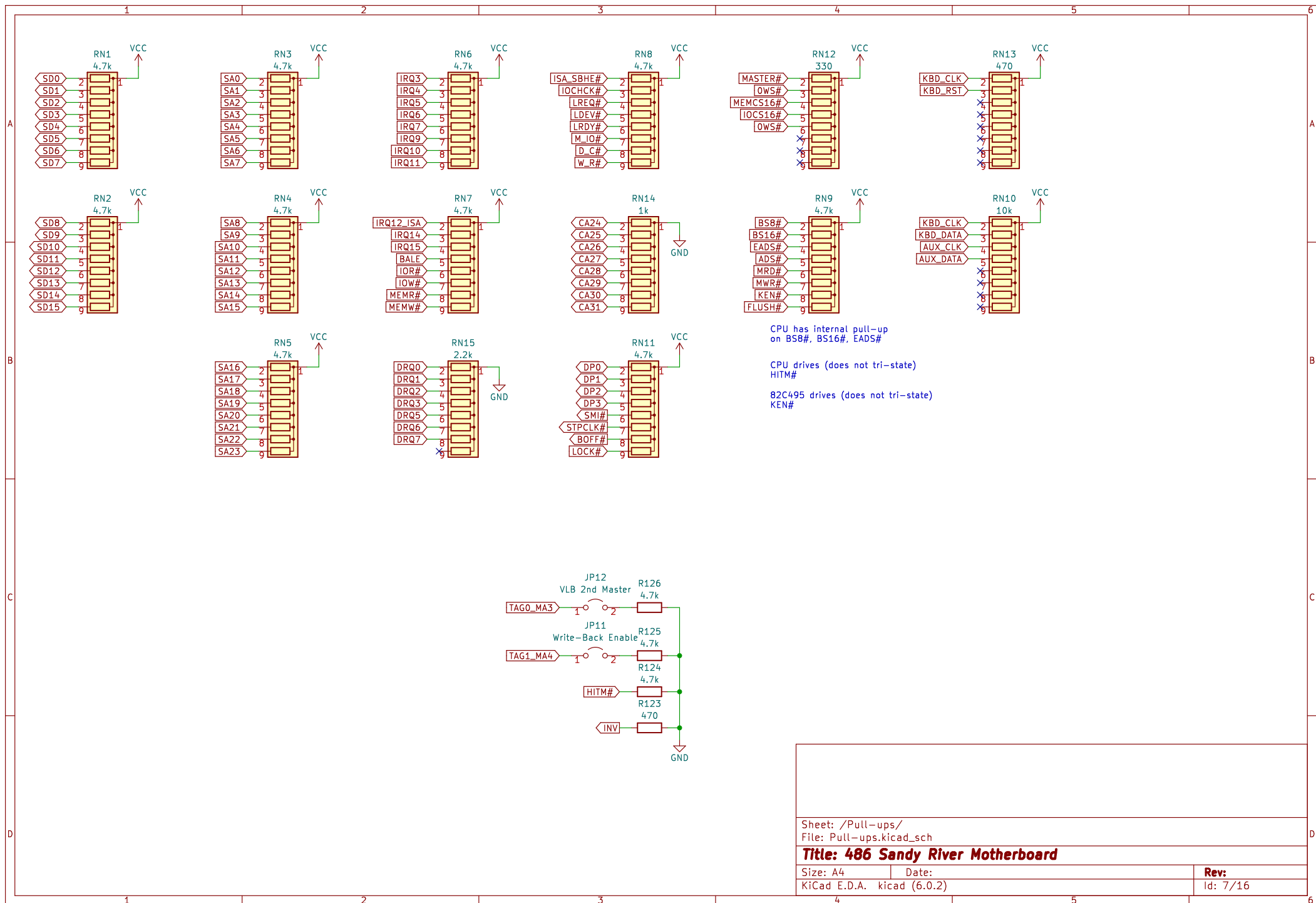
Date:

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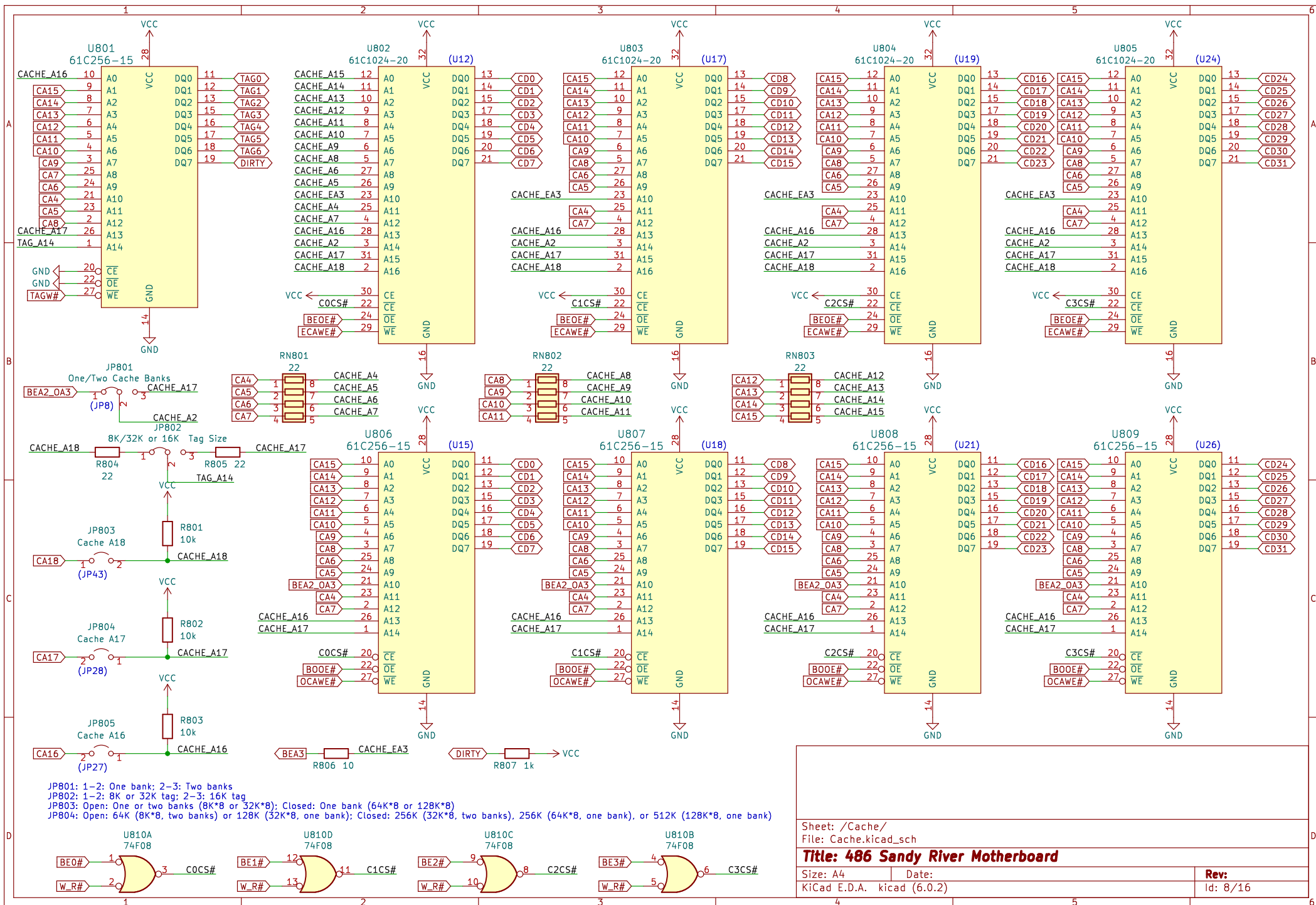
Rev:

Id: 5/16



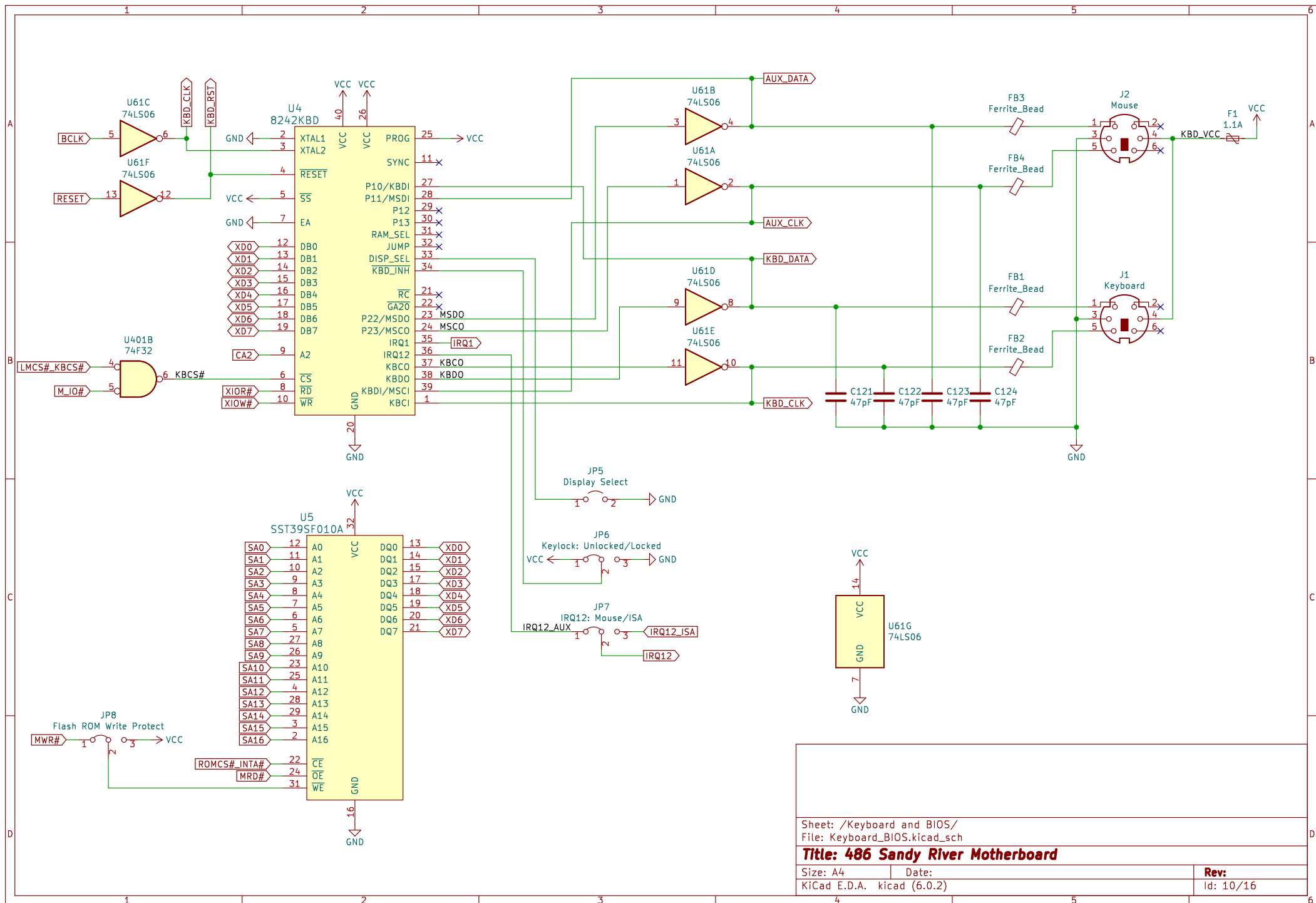


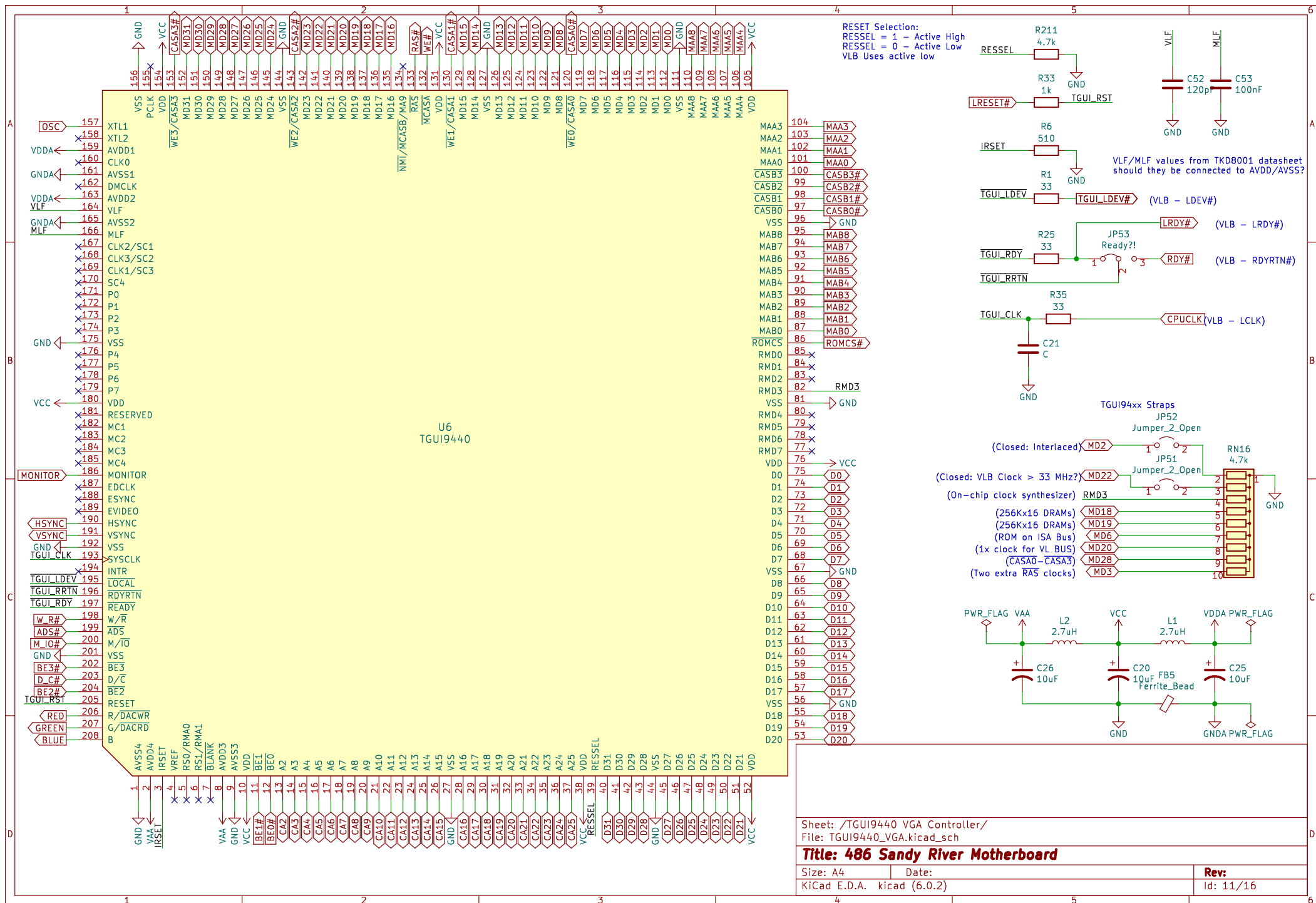






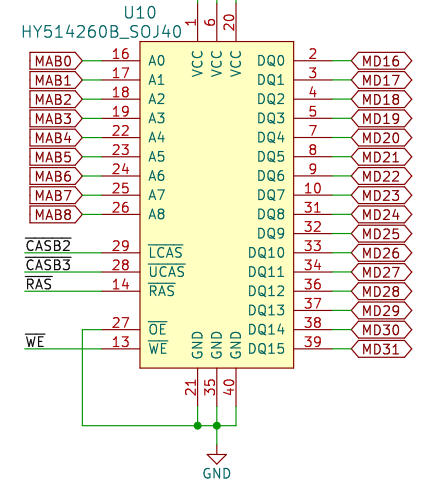
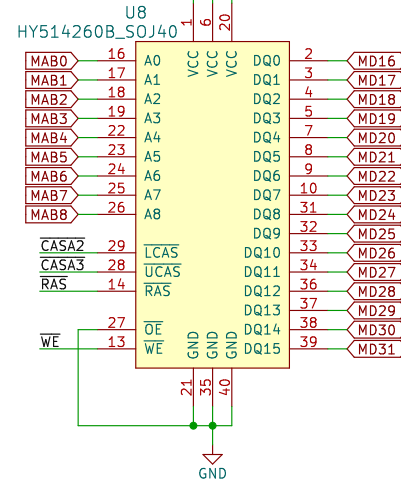
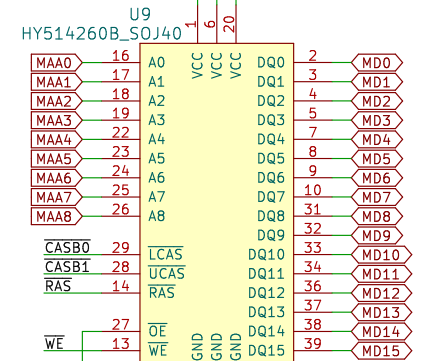
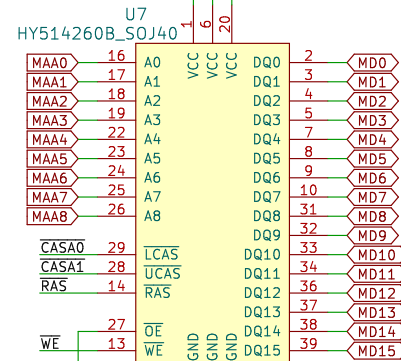
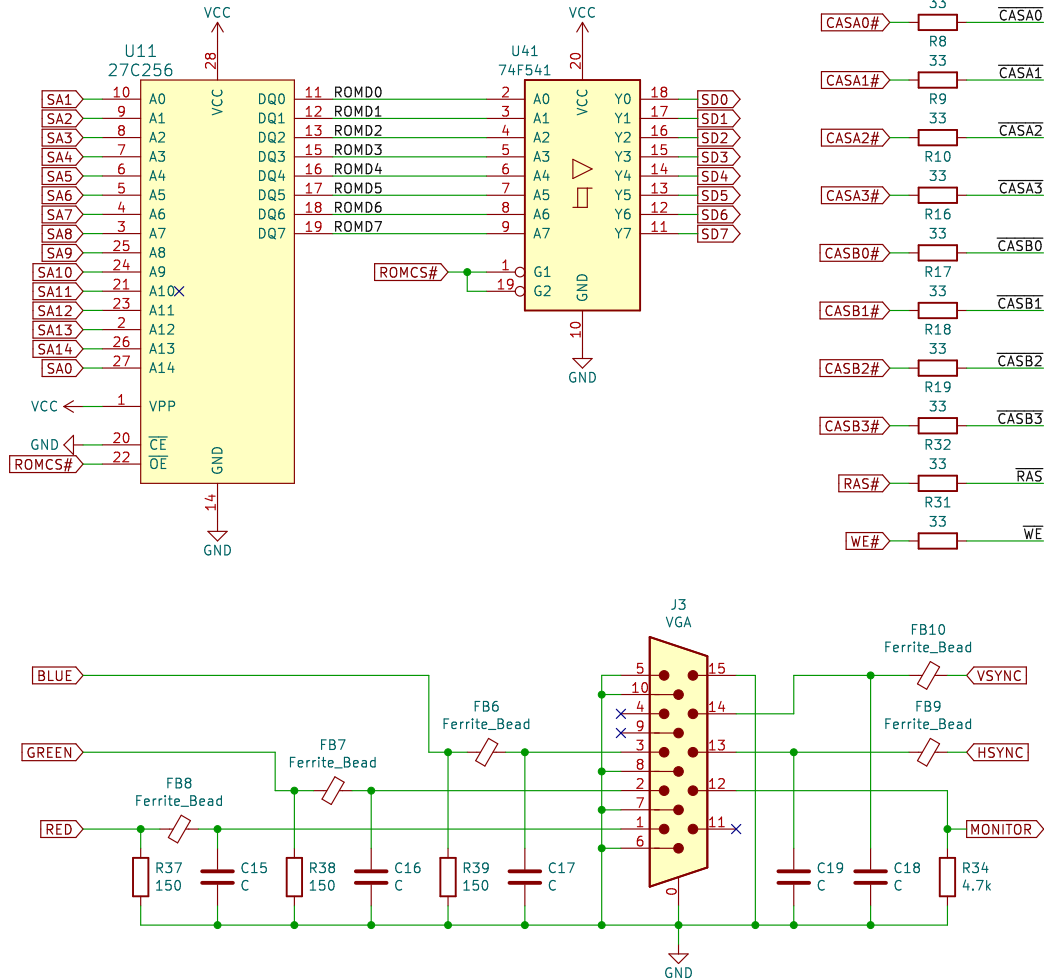






Trident VGA BIOS has even bytes at 0x0000-0x3FFF and odd bytes at 0x4000-0x7FFF. Therefore SA0 is connected to A14. It might be possible to move VGA BIOS to System BIOS ROM at 0xE0000, but that requires chipset initialization modification (evaluate).

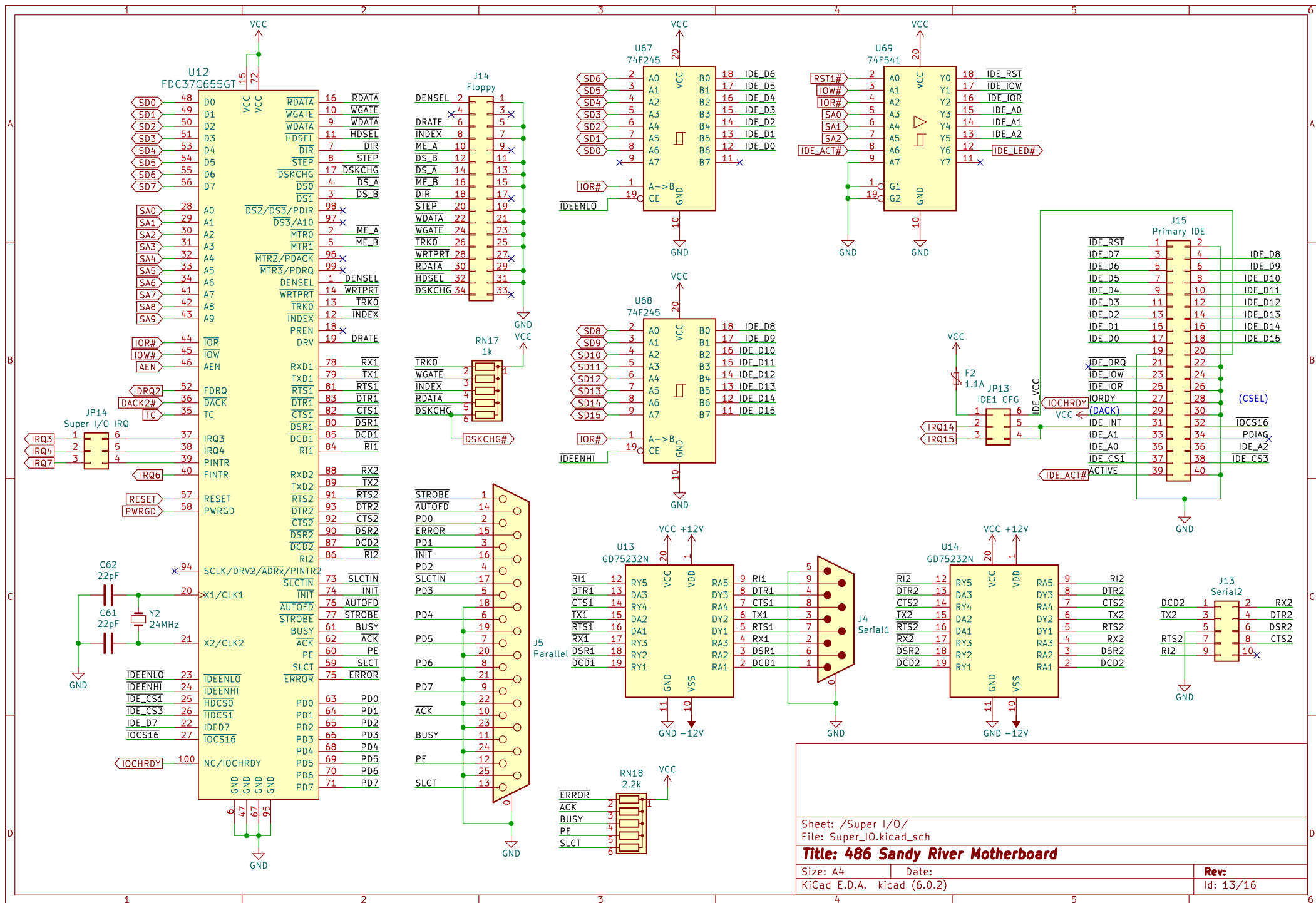
Replace with a resistor array



Sheet: /TGUI9440 VGA Memory/  
File: TGUI9440\_Mem.kicad\_sch

**Title: 486 Sandy River Motherboard**

Size: A4	Date:	Rev:
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Sheet: /Super I/O/

File: Super\_IO.kicad\_sch

# **Title: 486 Sandy River Motherboard**

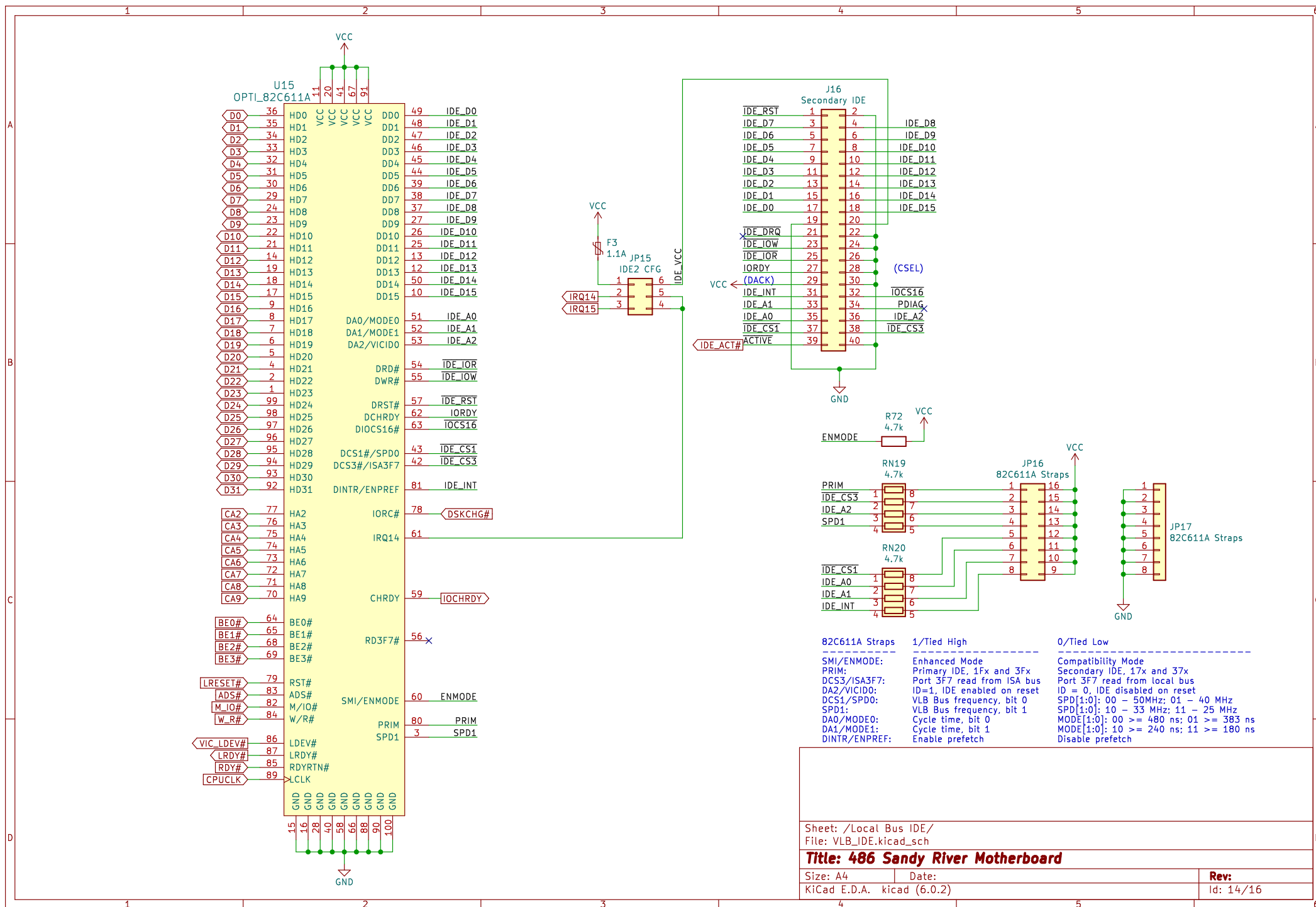
Size: A4

Date:

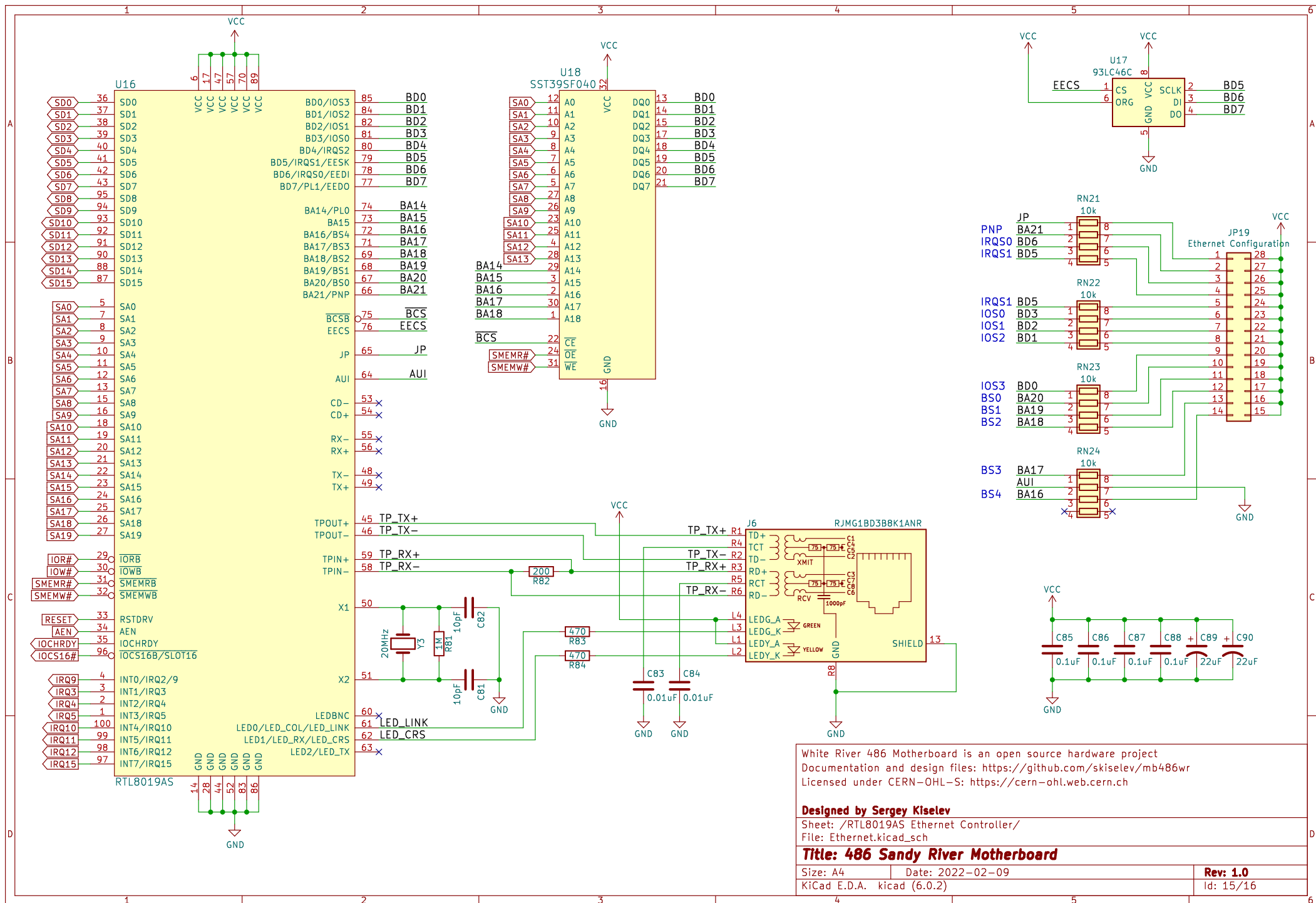
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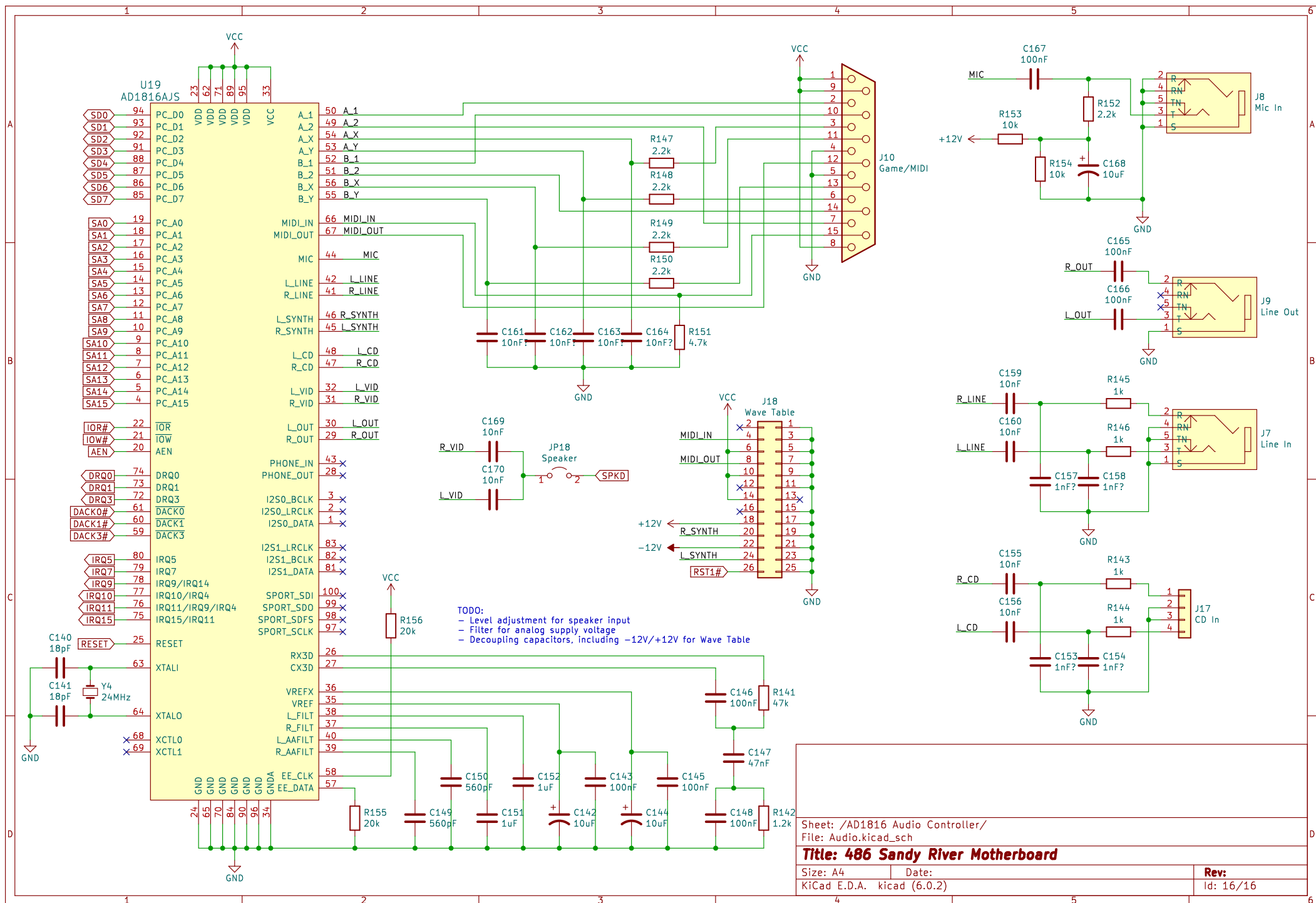
Rev:

Id: 13/16









Sheet: /AD1816 Audio Controller/  
File: Audio.kicad\_sch

# **Title: 486 Sandy River Motherboard**

Size: A4 Date:  
KiCad E.D.A. kicad (6.0.2)

Rev:  
Id: 16/16