

	1	2	3	4	5	6
A	<div>Processor</div> <div></div> <div>File: Processor.kicad_sch</div>	<div>Cache</div> <div></div> <div>File: Cache.kicad_sch</div>	<div>Local Bus IDE</div> <div></div> <div>File: VLB_IDE.kicad_sch</div>			
B	<div>ISA</div> <div></div> <div>File: ISA.kicad_sch</div>	<div>Buffers</div> <div></div> <div>File: Buffers.kicad_sch</div>	<div>RTL8019AS Ethernet Controller</div> <div></div> <div>File: Ethernet.kicad_sch</div>	<div>TODO:</div> <div><div>- Add address decoder for DRAMS#</div><div>- Add DACK decoder</div><div>- Remove Ethernet boot ROM</div><div>- Add 3 pin header for CPU fan – switchable between 5V and 12V with a jumper</div></div> <div>Check:</div> <div><div>- MP0 strap option (chipset pin 50 function)</div><div>- MP1 strap option (chipser pins 192 and 193 function)</div><div>- MP2 strap option (chipset pins 108 and 47 function)</div><div>- Chipset power management signals, pins: 62, 156, 160</div><div>- Processor RESET and SRESET connections</div><div>- (82C602) XD7, XD6 and XD4 strap options</div><div>- (82C602) XD4 strap – add 2.2k pull-down to enable RTC</div><div>- (82C602) Load capacitors for RTC crystal?</div><div>- Reset circuit (button + PWRGD)</div><div>- 22 ohm x 2 resistor arrays</div></div> <div>P24T Manual:</div> <div>CPU EWBE# – NC or tied LOW (internall pull-down)</div> <div>CPU BLEN# – NC or tied HIGH (internall pull-up)</div> <div>74xx Logic:</div> <div>74F244 x 2 – DRAM address and control buffers?</div> <div>74F138 – DACK decoder</div> <div>74LS157 – Interrupt selector</div> <div>7407 – Keyboard buffers; anything else?</div> <div>74F08 x 2 – Cache byte enable / chip select; what is the second chip?</div> <div>74LS14 – ?</div> <div>74F245 – CA[23:16] – LA[23:17].SA[19:16]</div> <div>Checked:</div> <div><div>- RTC crystal connection – what is the value of load capacitors?</div><div>- Cache data signals</div><div>- Cache control signals + cache configuration jumpers</div><div>- Cache address lines</div></div>		
C	<div>Chipset</div> <div></div> <div>File: Chipset.kicad_sch</div>	<div>Keyboard and BIOS</div> <div></div> <div>File: Keyboard_BIOS.kicad_sch</div>	<div>AD1816 Audio Controller</div> <div></div> <div>File: Audio.kicad_sch</div>			
D	<div>Power</div> <div></div> <div>File: Power.kicad_sch</div>	<div>TGUI9440 VGA Controller</div> <div></div> <div>File: TGUI9440_VGA.kicad_sch</div>	<div>TGUI9440 VGA Memory</div> <div></div> <div>File: TGUI9440_Mem.kicad_sch</div>			
	<div>Pull-ups</div> <div></div> <div>File: Pull-ups.kicad_sch</div>	<div>Super I/O</div> <div></div> <div>File: Super_IO.kicad_sch</div>				
	1	2	3	4	5	6

Sheet: /

File: mb486sr.kicad\_sch

Title: 486 Sandy River Motherboard

Size: A4

Date:

Rev:

KiCad E.D.A. kicad (6.0.2)

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