MLVM Design

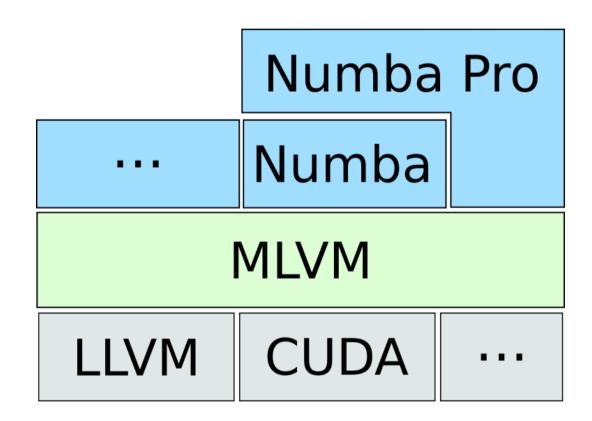
## Goals

- Common codegen and execution management for Numba, and other projects...
- Abstraction on top of LLVM
- Separation of frontend and backend
- Share features through extensions

# Design

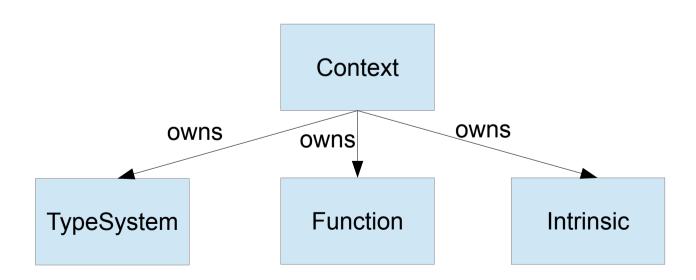
- Loose coupling of frontend and backend
- Provides basic default features
- Allows user to add features through extensions

## Abstraction of Low-Level Detail



## Frontend API

- Type is just a name
- Function can be overloaded
- Intrinsic is extensible



## C-like IR

- Like C without most control-flow constructs
  - only branch is left
- User configurable implicit casting
- Printable for debugging
- Use IR Builder to populate functions
  - just like Ilvm-py

# Extending the Frontend

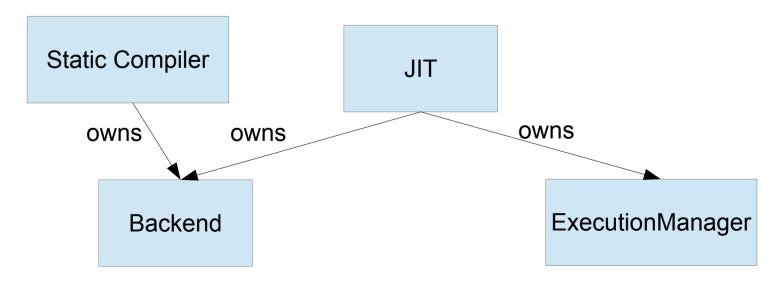
```
context = Context(TypeSystem())
context.install(ext_arraytype)
```

- This adds array-type extension
- Provides intrinsics:
  - array\_load(ary, idx)
  - array\_store(ary, val, idx)

```
define int32 foo (
     array float
                   in %arg 0,
     array float
                   in %arg 1,
     array float
                 out %arg 2,
        int32
                  in %arg 3,
 %const 0 = 0
                                                 ; int32
 %const 1 = 1
                                                 ; int32
 %const 2 = 1
                                                 ; int32
 %const 3 = 3.14
                                                 ; float
  %var 0 = \%const 0
                                                 ; int32
block 0:
     br block 1
block 1:
                       %var 0, %arg 3
    %9 = cmp.lt
                                                 ; pred
     br %9 [block 2, block 4]
block 2:
   %10 = cast.int32.address %var 0
                                                 : address
   %11 = call.intr array_load %arg_0, %10
                                                 ; float
   %12 = cast.int32.address %var 0
                                                 : address
   %13 = call.intr array_load %arg_1, %12
                                                 ; float
   %14 = add
                       %11, %13
                                                 ; float
   %15 = mul
                        %14, %const 3
                                                 : float
   %16 = cast.int32.address %var 0
                                                 : address
       call.intr array_store %arg_2, %15, %16
block 3:
```

## **Backend API**

- Backend implements compilation and linkage
- ExecutionManager implements JIT details
- JIT coordinates Backend and ExecutionManager
  - JIT can have multiple backends
- Static compiler



### Compile for execution:

```
backend = LLVMBackend()
manager = LLVMExecutionManager()
jit = JIT(manager, {": backend})
python_callable = jit.compile(funcdef)
```

#### **Install extension:**

backend.install(ext\_arraytype)

#### Static compilation:

```
backend = LLVMBackend(opt=LLVMBackend.OPT_MAXIMUM)
backend.install(ext_arraytype)
header = LLVMCWrapperGenerator()
compiler = Compiler(LLVMCompiler(), {": backend},
                   wrapper=header)
compiler.add_function(funcdef)
compiler.write_assembly(asmfile)
compiler.write_object(objfile)
header.write(headerfile)
```