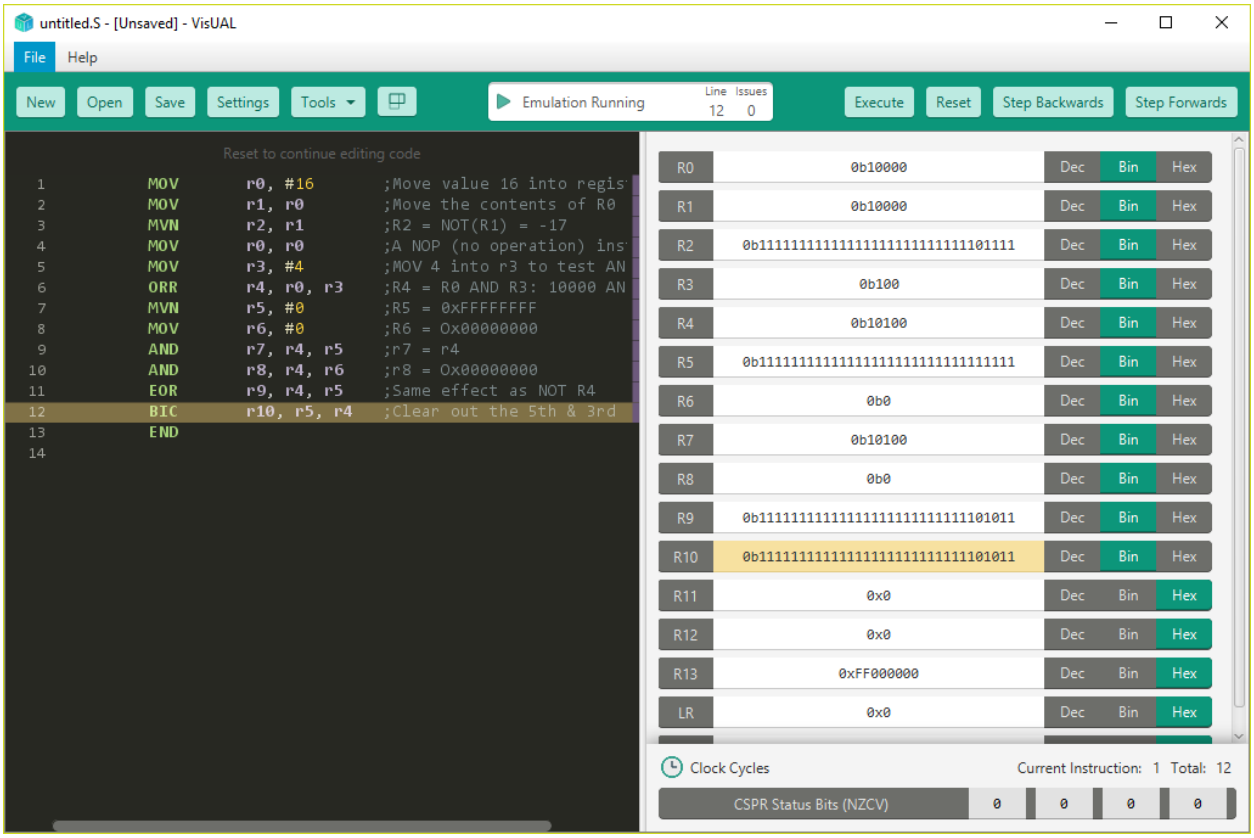
Stephen Oliver

Prof. Mitchel Fry

ASML2

26 January 2017

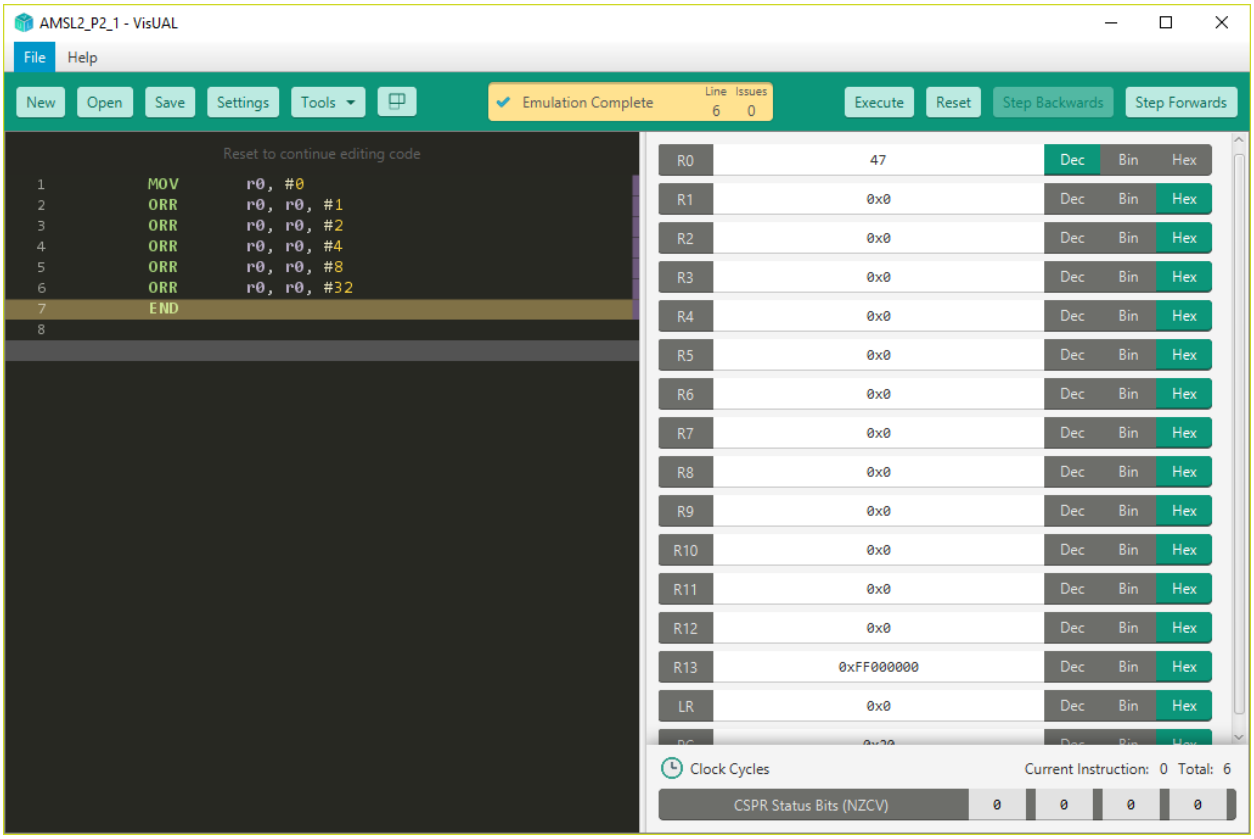
PART I:



PART II:

1)

```
MOV     r0, #0
ORR     r0, r0, #1
ORR     r0, r0, #2
ORR     r0, r0, #4
ORR     r0, r0, #8
ORR     r0, r0, #32
END
```



2)

```
MVN     r0, #0
EOR     r0, r0, #1
EOR     r0, r0, #2
EOR     r0, r0, #4
EOR     r0, r0, #8
EOR     r0, r0, #16
EOR     r0, r0, #32
EOR     r0, r0, #64
EOR     r0, r0, #128
END
```