

# MJPEG Decoder

## Version 01.00.05

### Release Notes

## July 2012

#### Build ID: 01.00.05.00

The release note is divided into the following sections:

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#### New In This Release

- Refer to Fixed In This Release section

#### Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MJPEG Decoder which runs on HDVICP2 and Media Controller based SoC	DM816x REV-A2 DDR2 EVM <a href="#">[User Guide]</a> <a href="#">[Data Sheet]</a>

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.19)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68



## Fixed In This Release

Defect ID	Description	Applicable Release Configuration
SDOCM00093149	Hang on Media controller can be observed, if SL2 is accessed through buffered region from Media controller	IVAHD_001

## Known Issues

Defect ID	Description	Applicable Release Configuration
SDOCM00086371	Decoding of corrupted JPEG image takes significantly higher mega cycles when error concealment is enabled	IVAHD_001