

MPEG4/H.263 Encoder

Version 01.00.00

Release Notes

January 2012

Build ID: 01.00.00.05

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- None

Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG4/H.263 Encoder, which runs on HDVICP2 and Media Controller based SOC's.	DM816x REV-A2 DDR2 EVM [Datasheet] [User Guide]

- This release is validated on DM816x REV-A2 DDR2 EVM using following components
 - Code Composer Studio (CCSv4) version 4.2.0.09000
 - Code Generation Tool version 4.5.1
 - Framework Component version 3.20.00.22
 - HDVICP2 API (01.00.00.19)
 - HDVICP2 CSP (Containing CSL) version 00.05.02.00
 - BIOS version 6.30.02.42
 - Codec Engine (CE) version 3.20.00.16
 - XDC tools version 3.20.04.68



Fixed In This Release

Defect ID	Description	Applicable Release Configuration
SDOCM00088283	Artifacts seen in the H.263 bit-stream when coded with low QP. These artifacts occur only in Intra macroblocks in Inter frames when coded with low QP value. It occurs mainly in the content where there is a sharp edge in one or more of the 8x8 blocks in the macroblock.	IVAHD_001

Known Issues

Defect ID	Description	Applicable Release Configuration
SDOCM00068422	H.241 and DP/RVLC may not work properly on simulator due to limitation in simulator.	IVAHD_001
SDOCM00076597	VBV Compliance failure	IVAHD_001
SDOCM00078497	Encoder on simulator generates a harmless warning of "M3_Video: Warning: Syncbox error while processing message "	IVAHD_001

MPEG4/H.263 Encoder

Version 01.00.00

Release Notes

December 2011

Build ID: 01.00.00.04

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- None

Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG4/H.263 Encoder, which runs on HDVICP2 and Media Controller based SOC's.	DM816x REV-A2 DDR2 EVM [Datasheet] [User Guide]

- This release is validated on DM816x REV-A2 DDR2 EVM using following components
 - Code Composer Studio (CCSv4) version 4.2.0.09000
 - Code Generation Tool version 4.5.1
 - Framework Component version 3.20.00.22
 - HDVICP2 API (01.00.00.19)
 - HDVICP2 CSP (Containing CSL) version 00.05.02.00
 - BIOS version 6.30.02.42
 - Codec Engine (CE) version 3.20.00.16
 - XDC tools version 3.20.04.68



Fixed In This Release

Defect ID	Description	Applicable Release Configuration
SDOCM00086049	In the User guide, explicitly mention that H241 packetization with H263 needs the datasync functionality of Slice mode needs to be enabled (outputDataMode to be Equal to IVIDEO_SLICEMODE)	IVAHD_001
SDOCM00085222	In the datasheet the version number is mentioned as 01.00.03. Needs to be 01.00.00	IVAHD_001
SDOCM00083991	Make batch file may not work on windows 7	IVAHD_001
SDOCM00083862	output buffer is locked by codec in erroneous scenario	IVAHD_001
SDOCM00083861	Codec Library size can be reduced as in release package some of the files of M3 are in debug mode	IVAHD_001
SDOCM00086527	Calling VIDENC2_control(XDM_GETSTATUS) after VIDENC2_create returned errorcode = 0x1 for MPEG4ENC for resolution 1080p @30fps	IVAHD_001

Known Issues

Defect ID	Description	Applicable Release Configuration
SDOCM00068422	H.241 and DP/RVLC may not work properly on simulator due to limitation in simulator.	IVAHD_001
SDOCM00076597	VBV Compliance failure	IVAHD_001
SDOCM00078497	Encoder on simulator generates a harmless warning of "M3_Video: Warning: Syncbox error while processing message "	IVAHD_001

MPEG4/H.263 Encoder

Version 01.00.00

Release Notes

September 2011

Build ID: 01.00.00.03

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Added support for enabling Mode B packetization with H.263 by providing additional information in the Data Sync call back function.

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MPEG4/H.263 Encoder, which runs on HDVICP2 and Media Controller based SOC's.	DM816x REV-A2 DDR2 EVM [Datasheet] [User Guide]

- This release is validated on DM816x REV-A2 DDR2 EVM using following components
 - Code Composer Studio (CCSv4) version 4.2.0.09000
 - Code Generation Tool version 4.5.1
 - Framework Component version 3.20.00.22
 - HDVICP2 API (01.00.00.19)
 - HDVICP2 CSP (Containing CSL) version 00.05.02.00
 - BIOS version 6.30.02.42
 - Codec Engine (CE) version 3.20.00.16
 - XDC tools version 3.20.04.68



Copyright © 2011, Texas Instruments Incorporated

Fixed In This Release

Defect ID	Description	Applicable Release Configuration
SDOCM00083588	MPEG4ENC_TI_StaticTableSection makes the const section attribute to be rw.	IVAHD_001

Known Issues

Defect ID	Description	Applicable Release Configuration
SDOCM00068422	H.241 and DP/RVLC may not work properly on simulator due to limitation in simulator.	IVAHD_001
SDOCM00076597	VBV Compliance failure	IVAHD_001
SDOCM00078497	Encoder on simulator generates a harmless warning of "M3_Video: Warning: Syncbox error while processing message "	IVAHD_001

MPEG4/H.263 Encoder

Version 01.00.00

Release Notes

July 2011

Build ID: 01.00.00.02

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- No new features but a bug fix release.

Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG4/H.263 Encoder, which runs on HDVICP2 and Media Controller based SOC's.	DM816x REV-A2 DDR2 EVM [Datasheet] [User Guide]

- This release is validated on DM816x REV-A2 DDR2 EVM using following components
 - Code Composer Studio (CCSv4) version 4.2.0.09000
 - Code Generation Tool version 4.5.1
 - Framework Component version 3.20.00.22
 - HDVICP2 API (01.00.00.19)
 - HDVICP2 CSP (Containing CSL) version 00.05.02.00
 - BIOS version 6.30.02.42
 - Codec Engine (CE) version 3.20.00.16
 - XDC tools version 3.20.04.68



Fixed In This Release

Defect ID	Description	Applicable Release Configuration
SDOCM00082002	MPEG-4 Encoder skips all frames after the first frame when bitrate is too low and early skip is enabled.	IVAHD_001
SDOCM00082247	In Low Latency operation, last NAL of the picture should get notified to application through call back function	IVAHD_001
SDOCM00082253	Documentation Error: Encoder userGuide refers IVIDENC1 elements	IVAHD_001

Known Issues

Defect ID	Description	Applicable Release Configuration
SDOCM00068422	H.241 and DP/RVLC may not work properly on simulator due to limitation in simulator.	IVAHD_001
SDOCM00076597	VBV Compliance failure	IVAHD_001
SDOCM00078497	Encoder on simulator generates a harmless warning of "M3_Video: Warning: Syncbox error while processing message "	IVAHD_001