

Paul Chukwu

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Professional Summary

Engineering student at uOttawa with expertise in hardware design and development, skilled in **Python, Java, C, C++, Verilog, System Verilog, VHDL, and Assembly**. Proficient in **Quartus-II Pro** with over **20+ FPGA** projects, including pin assignments, task programming, and working with various FPGAs during the Hack the Hill(Hackathon) sponsored by Ross Videos, where I tackled vector manipulation and display creation challenges. Experienced in designing digital systems and solving complex hardware problems, I am eager to contribute my skills in hardware design and FPGA development to drive innovation.

Projects

- **Shell Eco Marathon:** Developed telemetry system in C/C++, implemented speedometer, including a button reactor using **interrupts and time delay**.
- **UART Communication:** Built structural **UART** transmitter/receiver in **VHDL**, integrating **FSM** and **ASM** concepts.
- **Ross Video Hackathon:** Developed 1080x720 video display system using **System Verilog**, mastering video timing.
- **Recycling Solution:** Created automated recycling device with CAD design, **Python/Java** software.
- **QNX Hackathon:** Implemented the QNX software to form a UART system, then created CLI games.

Education

BASc in Computer Engineering, uOttawa (Expected 06/2026)

- Term Dean's List, Honor Roll, UOSU Scholarship for Innovation in Sustainability (2024)
- Relevant coursework: FPGA, Quartus-II Pro, Microcontrollers, VHDL/System Verilog, Circuit Design

Work Experience

Intern, Snappy Techs (08/2021 - 01/2023)

- Assisted staff in technical projects, gaining hands-on experience in software and hardware.

Skills

- Languages: **VHDL, System Verilog, C, C++**
- Tools: **Quartus-II Pro, FPGA, Microcontrollers, CAD**

Achievements

- 1st place: Eco Sorter (Self-Recyclable Bin) | 2nd place: Hack the Hill FPGA Project
- IEEE Member | Six Sigma, CPR/AED, First Aid Certified

Interests: Coding, Circuit Building, Gym, Mentorship, Creative Writing

References available upon request.