

**Search for bosonic resonances decaying
via a vector-like quark into the
all-hadronic final state and
bumpbonding interconnection
technology for Phase-I Upgrade of the
CMS experiment**

PHD-Thesis

submitted by

Simon Kudella

to Prof. Dr. U. Husemann

Institute for Data Processing and Electronics (IPE)

Second examiner Prof. Dr. U. Husemann

Institut für Experimentelle Kernphysik (IEKP)

DEPARTMENT OF PHYSICS

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Dissertation

vorgelegt von

Simon Kudella

Prof. Dr. U.Husemann

Institut für Experimentelle Teilchenphysik (ETP) Korreferent Prof. Dr. M. Weber

Institut für Prozessdatenverarbeitung und Elektronik (IPE)

FAKULTÄT FÜR PHYSIK

KARLSRUHER INSTITUT FÜR TECHNOLOGIE

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Deutsche Zusammenfassung

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Summary and conclusion

Performance upgrades of colliders always require the development of new particle detectors, to deal with the new challenges coming with such an upgrade. For the Compact Muon Solenoid (CMS) experiment, the upcoming upgrades of the Large Hadron Collider (LHC) to collision energies of 14 TeV and instantaneous luminosities of $\mathcal{L} = 2 \cdot 10^{34} \text{ cm}^{-2}\text{s}^{-1}$ bring several challenges concerning the particle flux and radiation damage in the detector. To deal with them, the pixel detector of the CMS experiment needs to be exchanged with an upgraded version (CMS Phase I Upgrade). The fourth layer of the new pixel detector will be produced by a German consortium including Karlsruhe Institute of Technology (KIT).

Pixel detectors provide high granularity and good spatial resolution of particle positions, but hybrid sensor-readout solutions require a high density interconnection technology. The bump bonding interconnection technology is able to provide such a density as well as a good mechanical connection between Readout Chip (ROC) and sensor.

Typical bump bonding technologies are based on lithographic bump deposition processes of for example indium or tin-lead solder bumps. Such bump bonding processes are used for the production of the new CMS pixel detector for the Phase I Upgrade. Half of the new fourth pixel layer will be produced at KIT, which has decided to use RTI International as the external vendor for the bumping (see sec. ??). The bonding will be performed in-house using a Finetech flip-chip bonder that performs both chip placement and reflow process (see sec. ??).

For the production of the pixel detector modules for the CMS Phase I Upgrade, a cleaning procedure was developed to remove the thick photo-resist protection layer from RTI on top of the bumps (see ch. ??) placed there to protect the bumps during transport. The cleaning procedure is based on chemical baths of acetone, isopropyl alcohol and water. To handle the bumped ROCs during the cleaning process, special mechanics were designed, including a cleaning tray, a vacuum system, and a grid which keeps the chips from floating. The cleaning process was tuned and the inspections by scanning electron microscope and energy dispersive X-ray spectroscopy showed that there are no photo-resist residuals larger than $1 \mu\text{m}$ left on the bumps or the chip surface after cleaning.

The standard lithographic bump deposition processes have the disadvantage that they can only be performed on wafer level and are therefore cost-intensive and lack flexibility, especially in the R&D phase of new particle detectors. For this reason, new bump

bonding technologies that allow cheaper and more flexible development of pixel detectors are being investigated.

KIT has decided to develop a gold-stud bump bonding process parallel to the production of the new pixel detector. The gold-stud bump bonding technology is a possible candidate for the development of new pixel detectors. Here, a ball wire bonder places gold-stud bumps onto metal pads by thermo-ultrasonic bonding. The flip-chip bonding process is then performed by a flip-chip bonder using thermocompression. Therefore, the gold-stud bump bonding process does not require any lithography at all.

The gold-stud bumping process at KIT is performed using a Kulicke & Soffa IConn ball wire bonder. After a detailed investigation of the process sequence, the bonding parameters and their influence on the mechanical strength and the shape of the gold-stud bumps were studied (see sec. ??).

To optimize the bump connection to the aluminum surface of the metal pads, a region of high mechanical strength and stability was determined by a systematic variation of the bonding parameters. This study led to a stable region around a bonding force of 9 g, an USG¹ current of 30 mA and a bonding time of 8 ms, allowing the placement of 30 μm bumps with a mechanical strength of 8.7 g (see sec. ??). To place bumps on small passivation openings of 15 μm , the parameters needed to be adjusted towards larger USG power, which led to bump diameters of 35 μm on passivation openings of 15 μm or 18 μm .

In a second optimization step, the long-term stability of the bumping process was iteratively tuned. Therefore, the parameters defining the wire shear process were varied to provide a correct wire shear and wire re-feeding process. This optimization allowed placing more than 4000 gold-stud bumps without any interruption of the process (see sec. ??) which corresponds to the bumping of a full CMS pixel single sensor or CMS pixel ROC within five minutes.

During the process optimization an unknown error occurred. The so-called “wire-ripping and sparking error” (see sec. ??) caused the ball wire bonder to apply a voltage of 5000 V to a wire connected to a gold-stud bump. This destroyed almost all the electric circuits on the chip. The error was solved by using a newer and harder wire. To protect the material from the EFO voltage in the future, an insulation layer made of ceramics was designed. It is placed beneath the chip to insulate it from the ground potential of the bonding table. Although the insulation layer cannot protect the Pixel Unit Cell hit by the high voltage pulse, it does protect the rest of the chip.

As another application for gold-stud bumps, a gold-stud bump UBM layer was designed. This layer can be used to create a solder wettable gold surface for power electronics. The bonding parameters were tuned to create very flat bumps with a thickness of only 6 μm . In the context of this application, the vacuum jig of the bonder has been improved by designing an adapter plate that allows fixing smaller dies (see sec. ??).

Further-more, the flip-chip bonding of gold-stud bumped chips was investigated. The flip-chip bonding at KIT is performed with a Finetech FINEPLACERTM femto. The goal of the first bonding tests was to reduce the bonding temperature. A reduction of the bonding temperature to 250 °C for 60 s of bonding time was achieved, avoiding any electromigration inside the sensor or chip. Cross-sections through a flip-chip bonded

¹Ultrasonic Generator (USG)

assembly showed a good metallic connection between the gold-stud bumps (see sec. ??) but they also showed a systematic misalignment of the flip-chip bonded chips. This is due to the asymmetric and not completely flat top of the gold-stud bumps, which causes horizontal forces during the bonding and a shift of the chips. In a second step, the bonding force was decreased to reduce these horizontal forces during bonding. To provide good connections of all bumps, the bonding force was set to a value of 250 N (see sec. ??).

Since the reduction of the bonding force was not sufficient to remove the systematic misalignment, an additional step was introduced into the bump bonding process. In this step, the top of the gold-stud bumps is flattened by pressing the bumped side of the chip onto the bonding table of the femto. The parameters of this procedure were tuned to a bonding force of 400 N and a bonding time of 50 s. The bonding temperature was set to 40 °C to avoid any connection between bumps and bonding table. The flattening allows creating a smooth and planar surface of gold-stud bumps, drastically decreasing the systematic misalignment of the chips (see sec. ??).

The assemblies show typical pull forces of approximately 9 – 10 kg (2.2 – 2.4 g per bump). Electrical tests of the connections were only possible with a radioactive source inducing charge into the sensor bulk. They showed a good electrical connection for most of the bumps. Unfortunately, these tests also showed a rotation of the chips during bonding. To solve this problem, it is necessary to improve the vacuum system of the femto. Since the femto is needed for the production of the pixel detector modules for the CMS Phase I Upgrade, this improvement could not be implemented during this thesis.

In addition to the gold-to-gold process, alternative bump bonding technologies like SnPb solder to gold-stud bumps, Pre-coated Powder Sheet (PPS) solder bumps on gold-stud bumps or anisotropic conductive gluing foils were described and partially tested. These techniques also show great potential for the R&D of new pixel detectors (see sec. ??).

Although the investigations on gold-stud bump bonding are very promising, future investigations could even improve the process. On the bumping side of the process, only minor parts are not optimized yet. With the change to a harder wire, a second systematic process optimization of the mechanical strength is needed to find the optimum parameters. Further more, different wire types can be tested to find the ideal bonding wire.

On the flip-chip bonding side, an additional reduction of the bonding temperature could allow the usage of gold-stud bump bonding for irradiation studies. The flattening process can be further improved by pressing the bumps onto a polished glass plate instead of the bonding table, avoiding any asperities in the flattening process. To avoid any systematic misalignment and rotational movement the vacuum jig and the pick-up tool of the femto need an upgrade. With these improvements and upgrades, a fully working gold-stud bump bonding process for High-Energy Physics (HEP) particle detectors should be easily possible.

List of Acronyms

ACAB Anisotropic Conductive Adhesive Bonding
ADC Analogue-Digital Converter
ALICE A Large Ion Collider Experiment
APD Avalanche Photo Diodes
ASIC Application-Specific Integrated Circuit
ATLAS A Toroidal LHC ApparatuS
BCB Benzocyclobutene
BD Bump Diameter
BH Bump Height
BPIX Barrel Pixel Detector
BSH Bump Shoulder Height
C4 Controlled Collapse Chip Connection
CA Chamfer Angle
CERN Conseil Européen pour la Recherche Nucléaire
CD Chamfer Diameter
CMOS Complementary Metal-Oxide-Semiconductor
CMS Compact Muon Solenoid
CSC Cathode Strip Chamber
CV Contact Velocity
DAC Digital-Analogue Converter
DC Drift Cell
DESY Deutsches Elektronen-Synchrotron
ECAL Electromagnetic Calorimeter
EDX Energy-Dispersive X-ray
EFO Electric-Flame-Off
ESD Electrostatical Discharges
ETH Eidgenössische Technische Hochschule Zürich
FA Face Angle
FAB Free Air Ball
FODO Focussing, nOthing, Defocussing, nOthing
FPGA Field-Programmable Gate Array
FPIX Forward Pixel Detector
FST Force Sensor threshold
HD Hole Diameter
HCAL Hadronic Calorimeter
HDI High Density Interconnect
HEP High-Energy Physics
HLT High Level Trigger
HPD Hybrid Photo Diode

HPK Hamamatsu Hotonikusu Kabushiki kaisha
IEKP Institut für Experimentelle Kernphysik
IPE Institute for Data Processing and Electronics
KIT Karlsruhe Institute of Technology
L1 Level 1 Trigger
LED Light-Emitting Diode
LEP Large Electron-Positron Collider
LEIR Low Energy Ion Ring
LHC Large Hadron Collider
LHCb Large Hadron Collider beauty
LHCf Large Hadron Collider forward
LINAC Linear Accelerator
LS1 Long Shutdown 1
LS2 Long Shutdown 2
MIP minimum ionizing particle
MoEDAL Monopole and Exotics Detector at the LHC
Nd-YAG Neodymium-doped Yttrium Aluminum Garnet
POM Polyoxymethylen
PPS Pre-coated Powder Sheet
PS Proton Synchrotron
PSB Proton Synchrotron Booster
PSI Paul Scherrer Institute
PTFE Polytetrafluoroethylene
PUC Pixel Unit Cell
QCD Quantum Chromo dynamics
ROC Readout Chip
RPC Resistive Plate Chamber
RTI Research Triangle Institute
RWTH Rheinisch-Westfälische Technische Hochschule Aachen
SEM Scanning Electron Microscopy
SD Smooth Distance
SH Separation Height
SPS Super Proton Synchrotron
TBM Token Bit Manager
TEC Tracker EndCap
T Tip diameter
TIB Tracker Inner Barrel
TID Tracker Inner Disk
TIP Tool Inflection Point
TOB Tracker Outer Barrel
TOTEM Total Elastic and Diffractive Cross Section Measurement
UBM Under Bump Metallization
UHH University of Hamburg
USG Ultrasonic Generator
VPT Vacuum Phototriodes
VTI Valtion Teknillinen Tutkimuskeskus
WD Wire Diameter

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Erklärung

Hiermit versichere ich, die vorliegende Arbeit selbständig angefertigt, alle dem Wortlaut oder Sinn nach entnommenen Inhalte anderer Werke an den entsprechenden Stellen unter Angabe der Quellen kenntlich gemacht und keine weiteren Hilfsmittel verwendet zu haben.

Simon Kudella
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