# JM1

**JAMMA_CONN**

| Pin | Left signal | Left name | Right name | Right signal | Pin |
|---|---|---|---|---|---|
| 1 | VSS | GND | GND | | A |
| 2 | | VSSA | GND | VSS | B |
| 3 | VCC | VDDA | | | C |
| 4 | −5V | +5V | +5V | VCC | D |
| 5 | +12V | −5V | −5V | PWR_FLAG | E |
| 6 | | +12V | +12V | | F |
| 8 | COIN_CNT_1 | COIN_CNT_1 | COIN_CNT_2 | COIN_CNT_2 | J |
| 9 | N.C0 | NC | NC | N.C3 | K |
| 10 | SPEAKER(+) | SPEAKER_(+) | SPEAKER_(−) | SPEAKER(−) | L |
| 11 | N.C1 | NC | NC | | M |
| 12 | RED | RED | GREEN | GREEN | N |
| 13 | BLUE | BLUE | SYNC | SYNC | P |
| 14 | VSS | VIDEO_GND | SERVICE | SERVICE | R |
| 15 | | NC | | N.C4 | S |
| 16 | COIN_1 | COIN_1 | COIN_2 | COIN_2 | T |
| 17 | 1P_START | 1P_START | 2P_START | 2P_START | U |
| 18 | 1P_UP | 1P_UP | 2P_UP | 2P_UP | V |
| 19 | 1P_DOWN | 1P_DOWN | 2P_DOWN | 2P_DOWN | W |
| 20 | 1P_LEFT | 1P_LEFT | 2P_LEFT | 2P_LEFT | X |
| 21 | 1P_RIGHT | 1P_RIGHT | 2P_RIGHT | 2P_RIGHT | Y |
| 22 | 1P_BNT_1 | 1P_BTN_1 | 2P_BTN_1 | 2P_BNT_1 | Z |
| 23 | 1P_BNT_2 | 1P_BTN_2 | 2P_BTN_2 | 2P_BNT_2 | a |
| 24 | 1P_BNT_3 | 1P_BTN_3 | 2P_BTN_3 | 2P_BNT_3 | b |
| 25 | 1P_BNT_4 | 1P_BTN_4 | 2P_BTN_4 | 2P_BNT_4 | c |
| 26 | 1P_SELECT | 1P_SELECT | 2P_SELECT | 2P_SELECT | d |
| 27 | | GND | GND | | e |
| 28 | VSS | GND | GND | VSS | f |

VCC
VSS
ZD1 BZD27
PWR_FLAG

# CABINET I/O

**RC Filters**
J5 J2 J1 R_P
R_Pack08

**SETA-X1-004**

P[23..10]
H3

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## main

Archivo: main.kicad_sch

| | |
|---|---|
| YM_CS | GFX_WR |
| S_IORQ | SND_WAIT |
| WDRES | SND_INT |
| | MAIN_D[7..0] |
| VBLK | MAIN_A[15..0] |
| SHD[7..0] | GFX_A[11..0] |
| MAIN_WR | SPBANK1 |
| | MAIN_RD |
| WR | CLK_SND |
| | XTAL2 |
| H[4..0] | RAM_WE |
| SND_RD | PHM |
| SND_D[7..0] | RESET |
| SND_MREQ | WATCHDOG |
| SND_WR | RAM_CS1 |
| | GFXA_CS |
| | GFXB_CS |
| | GFXC_CS |
| | GFXD_CS |
| | XTAL1 |
| | MAB_CE |
| | MBB_CE |
| | SND_SELECT |
| | BGTRNSP |

## mcu

Archivo: mcu.kicad_sch

| | |
|---|---|
| RESET | SND_D[7..0] |
| SND_CS | SND_RD |
| COIN_LOCK | P26_DRQ |
| CI/O1 | |
| CI/O2 | SND_WR |
| XTAL1 | CNN1A |
| XTAL2 | CNN1B |
| N.C0 | COIN_1 |
| N.C3 | COIN_2 |
| RST | COIN_CNT_1 |
| SND_A[15..0] | COIN_CNT_2 |
| P[23..10] | |

## gfx

Archivo: gfx.kicad_sch

| | |
|---|---|
| MAIN_WR | VSYNC |
| GFX_WR | DATAEN |
| GFXA_CS | GFX_CLK |
| GFXB_CS | VBLK |
| GFXC_CS | H[4..0] |
| GFXD_CS | DBB[7..0] |
| MAIN_RD | GDA[7..0] |
| MAB_CE | GDB[7..0] |
| MBB_CE | GDC[7..0] |
| SPBANK1 | GDD[7..0] |
| MAIN_A[15..0] | HBLK |
| MAIN_D[7..0] | WR |
| GFX_A[11..0] | WDRES |
| | HC[8..0] |

## colmix

Archivo: colmix.kicad_sch

| | |
|---|---|
| VSYNC | SYNC |
| GFX_CLK | RED |
| BGTRNSP | GREEN |
| DBB[7..0] | BLUE |
| GDA[7..0] | |
| GDB[7..0] | |
| GDC[7..0] | |
| GDD[7..0] | |
| XTAL1 | |
| HBLK | |
| DATAEN | |
| HC[8..0] | |

## watchdog

Archivo: watchdog.kicad_sch

| | |
|---|---|
| WATCHDOG | RESET |
| +12V | COIN_LOCK |
| P26_DRQ | |
| CNN1B | |
| CNN1A | |

## sound

Archivo: sound.kicad_sch

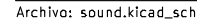| | |
|---|---|
| SND_A[15..0] | SHD[7..0] |
| SND_D[7..0] | SND_MREQ |
| SND_SELECT | S_IORQ |
| SND_WR | YM_CS |
| SND_RD | SND_CS |
| CLK_SND | RST |
| RESET | |
| SND_WAIT | |
| SND_INT | |
| MAIN_RD | |
| N.C1 | |
| SPEAKER(−) | |
| SPEAKER(+) | |
| MAIN_A[13..0] | |
| PHM | |
| RAM_WE | |
| RAM_CS1 | |

## capacitors

Archivo: capacitors.kicad_sch

JT

VCC

PWR_FLAG

C1
1000µF

C22 104  C23 104  C24 104  C25 104  C26 104  C27 104  C28 104  C29 104  C30 104  C31 104  C32 104  C33 104  C34 104  C35 104  C36 104  C37 104

8  V+  H12C

4  V-  NJM4558

PWR_FLAG

VSS

| H5E 74LS02 | B7C 74LS74 | B4C 74LS74 | B3C 74LS74 | B2E 74LS08 | G13G 74LS04 | C6E 74LS32 | B6E 74LS08 | B1G 74LS04 |

JT

G13F
74LS04
13 12

G13B
74LS04
3 4

G13A
74LS04
1 2

R1MΩ
R18

R12
R2.2KΩ

CC8
C683J

G14    74HC4024
VCC
14
CP 1
MR 2
GND 7

Q0 12
Q1 11
Q2 9
Q3 6
Q4 5
Q5 4
Q6 3

WATCHDOG

VCC

VSS

G13D
74LS04
9 8

JP1
1 2
Jumper_2_Bridged

JJ1
1 CNN1B
2 VSS
3 CNN1A
4
Conn_01x04_Male

ZD2
BZD27C

NO_MOUNTED
+12V

CC2
4.7µF
VSS

R14
R100Ω

VCC

R13
R100Ω

SW1
SW_Push

CC7
4.7µF

VSS

J8    TL7705A
SENSE 7
RESIN 2
CT 3
GND 4

VCC 8
RESET 5
RESET 6
REF 1

VCC

R16
1KΩ

RESET

P26_DRQ

CC6
C104

R15
R1KΩ

74LS02
H5A
1
3
2

COIN_LOCK

JT

VCC

SND_A[15..0]  SND_A0  9  A0
P[23..10]

INTEL8742
G3

26  VDD
40  VCC

D0  12  SND_D0
D1  13  SND_D1
D2  14  SND_D2
D3  15  SND_D3
D4  16  SND_D4
D5  17  SND_D5
D6  18  SND_D6
D7  19  SND_D7

SND_D[7..0]

P10  27  P10
P11  28  P11
P12  29  P12
P13  30  P13
P14  31  P14
P15  32  P15
P16  33  P16
P17  34  P17
P20  21  P20
P21  22  P21
P22  23  P22
P23  24  P23
25  PROG
36  P25_IBF

P24_OBF  35
P26_DRQ  37
P27_DACK  38
RD  9
WR  10
SYNC  11

P26_DRQ
SND_RD
SND_WR

TEST0  1  TEST0
TEST1  39  TEST1
XTAL1  2  XTAL1
XTAL2  3  XTAL2
RST  4  RESET
VCC  5  SS
SND_CS  6  CS
VSS  7  EA

8-BIT SLAVE
MICROCONTROLLER

20  VSS

VSS

RESET

11
12
H5D
74LS02
13

5
6
H5B
74LS02
4

9
8
74LS02
H5C
10

JJ10

COIN_2  28  COIN_2  COINOUT2  26  TEST1
COIN_1  27  COIN_1  COINOUT1  25  TEST0
CI/O2  14  UK  COIN_CNT_1  16  COIN_CNT_1
CI/O1  3  UK  COIN_CNT_2  4  COIN_CNT_2
COIN_LOCK  23  COIN1_LOCK  LOCK-OUT1  19  N.CO
17  OBF  LOCK-OUT2  18  N.C3
20  DACK  CNN1  2  CNN1A
8  IBF  CNN2  10  CNN1B
13  UK  UK.N.C  7
11  VSS  UK.NC  12
6  VSS  UK.NC  22
VSS  15  VSS
1  VSS  VCC  5  VCC
24  VSS  VCC  9
PC050CM  VCC  21

JT