

| Memory Location | Machine code : Instr (Or Immediate number)           | TEMP  | Binary code      |
|-----------------|--|---|------------------|
| 0               | Reserved (Trap Code Table Entry)                     | Reserved  | 0000000000000000 |
| 1               | Reserved (Machine fault Table Entry)                 |   | 0000000000000000 |
| 2               | Reserved (PC+1 for Trap)                             |   | 0000000000000000 |
| 3               | Reserved (HALT)                                      |   | 0000000000000000 |
| 4               | Reserved (Machine Fault)                             |   | 0000000000000000 |
| 5               | Reserved (HALT)                                      |   | 0000000000000000 |
| 6               | LDX IX0, MEM[21]                                     | Direct Addressing Location<br><br>DO NOT WASTE FOR INSTR! | 1010010001010101 |
| 7               | JMA IX0, 14 (AFTER THE INSTR, THE SPACE USE AS SWAP) |   | 0011010001001110 |
| 8               | 0 (RUNNING STACK DEPTH, DEFAULT 0)                   |   | 0000000000000000 |
| 9               | RUNNING STACK SPACE BELOW (INCLUDING THIS LINE)      |   | 0000000000000000 |
| 10              |  |   | 0000000000000000 |
| 11              |  |   | 0000000000000000 |
| 12              |  |   | 0000000000000000 |
| 13              |  |   | 0000000000000000 |
| 14              |  |   | 0000000000000000 |
| 15              |  |   | 0000000000000000 |
| 16              |  |   | 0000000000000000 |
| 17              |  |   | 0000000000000000 |
| 18              | 251  |   | 0000000011111011 |
| 19              | 0  |   | 0000000000000000 |
| 20              | 1000   |   | 0000001111101000 |
| 21              | 210  |   | 0000000011010010 |
| 22              | 177  |   | 0000000010110001 |
| 23              | 65535  |   | 1111111111111111 |
| 24              | 145  |   | 0000000010010001 |
| 25              | 105  |   | 0000000001101001 |
| 26              | 500  |   | 0000000111110100 |
| 27              | 20   |   | 0000000000010100 |
| 28              | 67   |   | 0000000001000011 |
| 29              | swap space   |   | 0000000000000000 |

Running  
Stack Space :  
to store PC  
for each  
subroutine.

|    |                 |                      |                  |
|----|-----------------|----------------------|------------------|
| 30 | swap space      |                      | 0000000000000000 |
| 31 | 32              |                      | 0000000000100000 |
| 32 | LDR R0,MEM[8]   |                      | 0000010000001000 |
| 33 | AIR R0,1        |                      | 0001100000000001 |
| 34 | STR R0,MEM[8]   |                      | 0000100000001000 |
| 35 | LDA R0,9        | SAVE PC(R3) TO       | 0000110000001001 |
| 36 | AMR R0, MEM[8]  | RUNNING              | 0001000000001000 |
| 37 | STR R0,MEM[7]   | STACK                | 0000100000000111 |
| 38 | LDX IX2,MEM[7]  |                      | 1010010011000111 |
| 39 | STR R3,IX2,0    |                      | 0000101111000000 |
| 40 | SIR R1,30       |                      | 0001110100011110 |
| 41 | SIR R1,18       |                      | 0001110100010010 |
| 42 | LDX IX0,MEM[31] | <b>Subroutine 1:</b> | 1010010001011111 |
| 43 | JGE R1, IX0, 13 | CHECK IF R1 IS '0'   | 0100010101001101 |
| 44 | HALT            | '9'(ASCII)           | 0000000000000000 |
| 45 | SIR R1,10       | IF NOT,HALT          | 0001110100001010 |
| 46 | JGE R1,IX0, 12  | IF IS, RETURN        | 0100010101001100 |
| 47 | AIR R1,10       | NORMALLY             | 0001100100001010 |
| 48 | AIR R1,18       | WITH NO              | 0001100100010010 |
| 49 | AIR R1,30       | RETURN VALUE         | 0001100100011110 |
| 50 | LDA R0,9        |                      | 0000110000001001 |
| 51 | AMR R0, MEM[8]  |                      | 0001000000001000 |
| 52 | STR R0,MEM[7]   |                      | 0000100000000111 |
| 53 | LDX IX2,MEM[7]  | RETURN PC(R3)        | 1010010011000111 |
| 54 | LDR R3,IX2,0    | TO                   | 0000011111000000 |
| 55 | LDR R0,MEM[8]   | RUNNING              | 0000010000001000 |
| 56 | SIR R0,1        | STACK                | 0001110000000001 |
| 57 | STR R0,MEM[8]   |                      | 0000100000001000 |
| 58 | RFS             |                      | 0011110000000000 |
| 59 | LDR R0,MEM[8]   |                      | 0000010000001000 |
| 60 | AIR R0,1        |                      | 0001100000000001 |
| 61 | STR R0,MEM[8]   |                      | 0000100000001000 |

|    |                   |                |                  |
|----|-------------------|----------------|------------------|
| 62 | LDA R0,9          | SAVE PC(R3) TO | 0000110000001001 |
| 63 | AMR R0, MEM[8]    | RUNNING        | 0001000000001000 |
| 64 | STR R0,MEM[7]     | STACK          | 0000100000000111 |
| 65 | LDX IX2,MEM[7]    |                | 1010010011000111 |
| 66 | STR R3,IX2,0      |                | 0000101111000000 |
| 67 | IN R1,KEYBOARD    |                | 1111010100000000 |
| 68 | LDA R0,10         |                | 0000110000001010 |
| 69 | LDX IX0,MEM[31]   |                | 1010010001011111 |
| 70 | TRR R0,R1         |                | 0101100001000000 |
| 71 | JCC CC(3), IX0,12 |                | 0011001101001100 |
| 72 | JSR IX0,0         |                | 0011100001000000 |
| 73 | SIR R1,30         |                | 0001110100011110 |
| 74 | SIR R1,18         |                | 0001110100010010 |
| 75 | STR R1,MEM[30]    |                | 0000100100011110 |
| 76 | IN R1,KEYBOARD    |                | 1111010100000000 |
| 77 | LDA R0=10         |                | 0000110000001010 |
| 78 | TRR R0,R1         | Subroutine 2:  | 0101100001000000 |
| 79 | LDX IX0,MEM[28]   | READ A NUMBER  | 1010010001011100 |
| 80 | JCC CC(3), IX0,29 | FROM IO        | 0011001101011101 |
| 81 | LDX IX0,MEM[31]   | CHECK IF LEGIT | 1010010001011111 |
| 82 | JSR IX0,0         | CONVERT TO     | 0011100001000000 |
| 83 | SIR R1,30         | INTEGER        | 0001110100011110 |
| 84 | SIR R1,18         | STORE TO       | 0001110100010010 |
| 85 | STR R1,MEM[29]    | MEM[30]        | 0000100100011101 |
| 86 | LDR R0,MEM[30]    |                | 0000010000011110 |
| 87 | LDA R2=10         |                | 0000111000001010 |
| 88 | MLT R0,R2         |                | 0101000010000000 |
| 89 | LDX IX0,MEM[31]   |                | 1010010001011111 |
| 90 | JNE R0,IX0,12     |                | 0010110001001100 |
| 91 | AMR R1,MEM[29]    |                | 0001000100011101 |
| 92 | JCC CC(0),IX0,12  |                | 0011000001001100 |
| 93 | STR R1,MEM[30]    |                | 0000100100011110 |

|     |                  |                      |                  |
|-----|------------------|----------------------|------------------|
| 94  | LDX IX0, MEM[28] |                      | 1010010001011100 |
| 95  | JMA IX0, 9       |                      | 0011010001001001 |
| 96  | LDA R0, 9        |                      | 0000110000001001 |
| 97  | AMR R0, MEM[8]   |                      | 0001000000001000 |
| 98  | STR R0, MEM[7]   |                      | 0000100000000111 |
| 99  | LDX IX2, MEM[7]  | RETURN PC(R3)<br>TO  | 1010010011000111 |
| 100 | LDR R3, IX2, 0   | RUNNING              | 0000011111000000 |
| 101 | LDR R0, MEM[8]   | STACK                | 0000010000001000 |
| 102 | SIR R0, 1        |                      | 0001110000000001 |
| 103 | STR R0, MEM[8]   |                      | 0000100000001000 |
| 104 | RFS              |                      | 0011110000000000 |
| 105 | LDR R0, MEM[8]   |                      | 0000010000001000 |
| 106 | AIR R0, 1        |                      | 0001100000000001 |
| 107 | STR R0, MEM[8]   |                      | 0000100000001000 |
| 108 | LDA R0, 9        | SAVE PC(R3) TO       | 0000110000001001 |
| 109 | AMR R0, MEM[8]   | RUNNING              | 0001000000001000 |
| 110 | STR R0, MEM[7]   | STACK                | 0000100000000111 |
| 111 | LDX IX2, MEM[7]  |                      | 1010010011000111 |
| 112 | STR R3, IX2, 0   |                      | 0000101111000000 |
| 113 | LDX IX0, MEM[31] |                      | 1010010001011111 |
| 114 | JSR IX0, 27      | <b>Subroutine 3:</b> | 0011100001011011 |
| 115 | LDR R0, MEM[27]  | READ 20              | 0000010000011011 |
| 116 | AMR R0, MEM[26]  | NUMBERS              | 0001000000011010 |
| 117 | STR R0, MEM[7]   | SAVE TO              | 0000100000000111 |
| 118 | LDX IX0, MEM[7]  | MEM[500~520]         | 1010010001000111 |
| 119 | LDR R0, MEM[30]  | SAVE IN REVERSE      | 0000010000011110 |
| 120 | STR R0, IX0, 0   | ORDER                | 0000100001000000 |
| 121 | LDR R0, MEM[27]  | THAT IS:             | 0000010000011011 |
| 122 | SIR R0, 1        | THE 1ST              | 0001110000000001 |
| 123 | STR R0, MEM[27]  | NUMBER               | 0000100000011011 |
| 124 | LDX IX0, MEM[25] | READED               | 1010010001011001 |
| 125 | JNE R0, IX0, 8   | SAVE TO              | 0010110001001000 |
|     |                  | MEM[520]             |                  |

|     |                 |                         |                  |
|-----|-----------------|-------------------------|------------------|
| 126 | LDA R0,9        |                         | 0000110000001001 |
| 127 | AMR R0, MEM[8]  |                         | 0001000000001000 |
| 128 | STR R0,MEM[7]   |                         | 0000100000000111 |
| 129 | LDX IX2,MEM[7]  | RETURN PC(R3)<br>TO     | 1010010011000111 |
| 130 | LDR R3,IX2,0    | RUNNING                 | 0000011111000000 |
| 131 | LDR R0,MEM[8]   | STACK                   | 0000010000001000 |
| 132 | SIR R0,1        |                         | 0001110000000001 |
| 133 | STR R0,MEM[8]   |                         | 0000100000001000 |
| 134 | RFS             |                         | 0011110000000000 |
| 135 | LDR R0,MEM[8]   |                         | 0000010000001000 |
| 136 | AIR R0,1        |                         | 0001100000000001 |
| 137 | STR R0,MEM[8]   |                         | 0000100000001000 |
| 138 | LDA R0,9        | SAVE PC(R3) TO          | 0000110000001001 |
| 139 | AMR R0, MEM[8]  | RUNNING                 | 0001000000001000 |
| 140 | STR R0,MEM[7]   | STACK                   | 0000100000000111 |
| 141 | LDX IX2,MEM[7]  |                         | 1010010011000111 |
| 142 | STR R3,IX2,0    |                         | 0000101111000000 |
| 143 | STR R2,MEM[7]   |                         | 0000101000000111 |
| 144 | SMR R1,MEM[7]   |                         | 0001010100000111 |
| 145 | LDX IX0,MEM[24] |                         | 1010010001011000 |
| 146 | JGE R1, IX0, 12 |                         | 0100010101001100 |
| 147 | AMR R1,MEM[7]   | RESTORE THE VALUE OF R1 | 0001000100000111 |
| 148 | LDA R0,9        |                         | 0000110000001001 |
| 149 | AMR R0, MEM[8]  |                         | 0001000000001000 |
| 150 | STR R0,MEM[7]   |                         | 0000100000000111 |
| 151 | LDX IX2,MEM[7]  | RETURN PC(R3)<br>TO     | 1010010011000111 |
| 152 | LDR R3,IX2,0    | RUNNING                 | 0000011111000000 |
| 153 | LDR R0,MEM[8]   | STACK                   | 0000010000001000 |
| 154 | SIR R0,1        |                         | 0001110000000001 |
| 155 | STR R0,MEM[8]   |                         | 0000100000001000 |
| 156 | RFS(IMM=1)      | R2 IS BIGGER,R0=1       | 0011110000000001 |
| 157 | AMR R1,MEM[7]   | RESTORE THE VA          | 0001000100000111 |

**Subroutine 4:**  
 COMPARE R1,R2  
 IF R1 IS BIGGER  
 RETURN 0(R0)  
 IF R2 IS BIGGER  
 OR EQUAL  
 RETURN 1(R0)

|     |                 |                   |                  |
|-----|-----------------|-------------------|------------------|
| 158 | LDA R0,9        |                   | 0000110000001001 |
| 159 | AMR R0, MEM[8]  |                   | 0001000000001000 |
| 160 | STR R0,MEM[7]   |                   | 0000100000000111 |
| 161 | LDX IX2,MEM[7]  | RETURN PC(R3)     | 1010010011000111 |
| 162 | LDR R3,IX2,0    | TO                | 0000011111000000 |
| 163 | LDR R0,MEM[8]   | RUNNING           | 0000010000001000 |
| 164 | SIR R0,1        | STACK             | 0001110000000001 |
| 165 | STR R0,MEM[8]   |                   | 0000100000001000 |
| 166 | RFS(IMM=0)      | R1 IS BIGGER,R0=0 | 0011110000000000 |
| 167 | LDR R0,MEM[8]   |                   | 0000010000001000 |
| 168 | AIR R0,1        |                   | 0001100000000001 |
| 169 | STR R0,MEM[8]   |                   | 0000100000001000 |
| 170 | LDA R0,9        | RETURN PC(R3)     | 0000110000001001 |
| 171 | AMR R0, MEM[8]  | TO                | 0001000000001000 |
| 172 | STR R0,MEM[7]   | RUNNING           | 0000100000000111 |
| 173 | LDX IX2,MEM[7]  | STACK             | 1010010011000111 |
| 174 | STR R3,IX2,0    |                   | 0000101111000000 |
| 175 | LDA R0,20       |                   | 0000110000010100 |
| 176 | STR R0,MEM[27]  |                   | 0000100000011011 |
| 177 | LDR R0,MEM[26]  |                   | 0000010000011010 |
| 178 | AMR R0,MEM[27]  |                   | 0001000000011011 |
| 179 | STR R0,MEM[7]   |                   | 0000100000000111 |
| 180 | LDX IX0,MEM[7]  |                   | 1010010001000111 |
| 181 | LDR R1,IX0,0    |                   | 0000010101000000 |
| 182 | LDR R2,MEM[30]  |                   | 0000011000011110 |
| 183 | LDX IX0,MEM[25] |                   | 1010010001011001 |
| 184 | JSR IX0,30      |                   | 0011100001011110 |
| 185 | LDX IX0,MEM[22] |                   | 1010010001010110 |
| 186 | JZ R0,IX0,15    |                   | 0010100001001111 |
| 187 | STR R1,MEM[7]   |                   | 0000100100000111 |
| 188 | SMR R2,MEM[7]   |                   | 0001011000000111 |
| 189 | STR R2,MEM[7]   |                   | 0000101000000111 |

```

190 LDX IX0, MEM[22]
191 JMA IX0, 20
192 STR R2, MEM[7]
193 SMR R1, MEM[7]
194 STR R1, MEM[7]
195 LDX IX0, MEM[22]
196 JMA IX0, 20
197 LDR R1, MEM[23]
198 LDR R2, MEM[7]
199 LDX IX0, MEM[25]
200 JSR IX0, 30
201 LDX IX0, MEM[21]
202 JNE R0, IX0, 0
203 STR R2, MEM[23]
204 LDR R0, MEM[26]
205 AMR R0, MEM[27]
206 STR R0, MEM[7]
207 LDX IX0, MEM[7]
208 LDR R1, IX0, 0
209 STR R1, MEM[29]
210 LDR R0, MEM[27]
211 SIR R0, 1
212 STR R0, MEM[27]
213 LDX IX0, MEM[22]
214 JGE R0, IX0, 0
215 LDA R0, 9
216 AMR R0, MEM[8]
217 STR R0, MEM[7]
218 LDX IX2, MEM[7]
219 LDR R3, IX2, 0
220 LDR R0, MEM[8]
221 SIR R0, 1

```

```

RETURN PC(R3)
TO
RUNNING
STACK

```

```

Subroutine 5:
  REVERSE
  VISITING
  MEM500~520]
  FIND THE
  NEAREST
  NUMBER
  OF MEM[30]
  PLACE IT TO
  MEM[29]

```

```

1010010001010110
0011010001010100
0000101000000111
0001010100000111
0000100100000111
1010010001010110
0011010001010100
0000010100010111
0000011000000111
1010010001011001
0011100001011110
1010010001010101
0010110001000000
0000101000010111
0000010000011010
0001000000011011
0000100000000111
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0000010101000000
0000100100011101
0000010000011011
0001110000000001
0000100000011011
1010010001010110
0100010001000000
0000110000001001
0001000000001000
0000100000000111
1010010011000111
0000011111000000
0000010000001000
0001110000000001

```

|     |                 |  |                  |
|-----|-----------------|--|------------------|
| 222 | STR R0,MEM[8]   |  | 0000100000001000 |
| 223 | RFS             |  | 0011110000000000 |
| 224 | LDX IX0,MEM[25] | CALL READ_20_NUMBER                    | 1010010001011001 |
| 225 | JSR IX0,0       |  | 0011100001000000 |
| 226 | LDX IX0,MEM[31] | CALL READ_A_NUMBER                     | 1010010001011111 |
| 227 | JSR IX0,27      |  | 0011100001011011 |
| 228 | LDX IX0,MEM[24] | CALL FIND_NUMBER                       | 1010010001011000 |
| 229 | JSR IX0,22      |  | 0011100001010110 |
| 230 | LDX IX0,MEM[21] |  | 1010010001010101 |
| 231 | JMA IX0,23      |  | 0011010001010111 |
| 232 |                 |  | 0000000000000000 |
| 233 | LDR R0,MEM[29]  |  | 0000010000011101 |
| 234 | LDA R2,10       |  | 0000111000001010 |
| 235 | DVD R0,R2       |  | 0101010010000000 |
| 236 | LDR R2,MEM[19]  |  | 0000011000010011 |
| 237 | AIR R2,1        |  | 0001101000000001 |
| 238 | STR R2,MEM[19]  |  | 0000101000010011 |
| 239 | LDR R2,MEM[20]  |  | 0000011000010100 |
| 240 | AMR R2,MEM[19]  |  | 0001001000010011 |
| 241 | STR R2,MEM[7]   |  | 0000101000000111 |
| 242 | LDX IX0,MEM[7]  |  | 1010010001000111 |
| 243 | AIR R1,30       | MAIN                                   | 0001100100011110 |
| 244 | AIR R1,18       |  | 0001100100010010 |
| 245 | STR R1,IX0,0    |  | 0000100101000000 |
| 246 | LDX IX0,MEM[21] |  | 1010010001010101 |
| 247 | JNE R0,IX0,24   |  | 0010110001011000 |
| 248 | LDR R0,MEM[20]  |  | 0000010000010100 |
| 249 | AMR R0,MEM[19]  |  | 0001000000010011 |
| 250 | STR R0,MEM[20]  |  | 0000100000010100 |
| 251 | LDX IX0,MEM[20] | READ NUMBER REVERSE ORDER              | 1010010001010100 |
| 252 | LDR R0,IX0,0    |  | 0000010001000000 |
| 253 | OUT R0,PRINTER  | Print the result to the consle display | 1111100000000001 |



|     |                 |                  |
|-----|-----------------|------------------|
| 254 | LDR R0,MEM[20]  | 0000010000010100 |
| 255 | SIR R0,1        | 0001110000000001 |
| 256 | STR R0,MEM[20]  | 0000100000010100 |
| 257 | LDR R0,MEM[19]  | 0000010000010011 |
| 258 | SIR R0,1        | 0001110000000001 |
| 259 | STR R0,MEM[19]  | 0000100000010011 |
| 260 | LDX IX0,MEM[18] | 1010010001010010 |
| 261 | JNE R0,IX0,0    | 0010110001000000 |
| 262 | HALT            | 0000000000000000 |
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