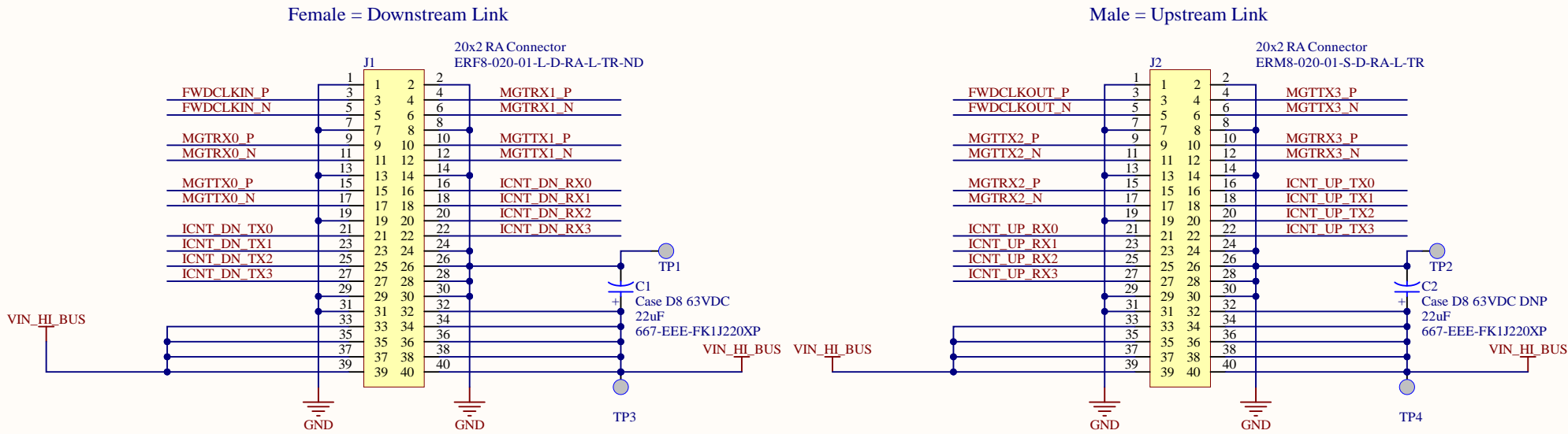


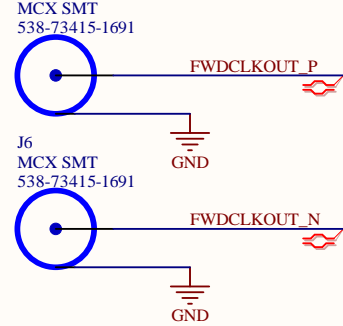
Loopback Ties

MGTTX0_P	R2	0	MGT_TIE0_P	R3	0	MGTRX0_P
MGTTX0_N	R4	0	MGT_TIE0_N	R5	0	MGTRX0_N
MGTTX1_P	R6	0	MGT_TIE1_P	R7	0	MGTRX1_P
MGTTX1_N	R8	0	MGT_TIE1_N	R9	0	MGTRX1_N
MGTTX2_P	R10	0	MGT_TIE2_P	R11	0	MGTRX2_P
MGTTX2_N	R12	0	MGT_TIE2_N	R13	0	MGTRX2_N
MGTTX3_P	R15	0	MGT_TIE3_P	R16	0	MGTRX3_P
MGTTX3_N	R17	0	MGT_TIE3_N	R18	0	MGTRX3_N

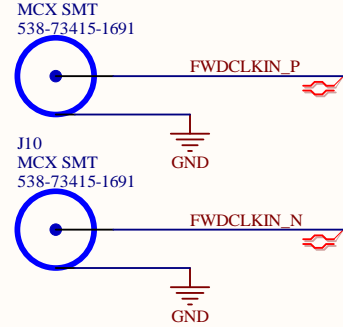
Title IRIS-EXT-04-BRK Breakout MGTs		
Size A3	Number	Revision A
Date:	2/05/2020	Sheet of Drawings
File:	C:\Users\...\IRIS-EXT-04-BRK_GTX-Breakout_Sch.Dwg	Doc



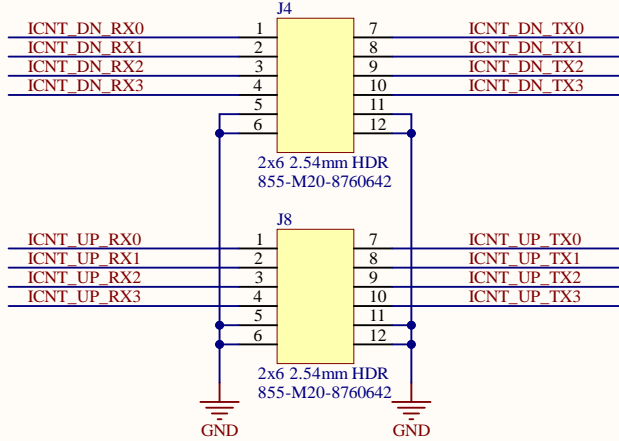
### Apply Reference Clock



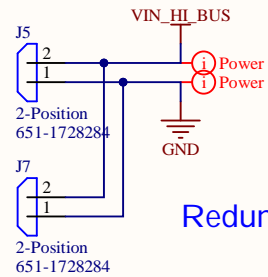
### Measure Reference Clock



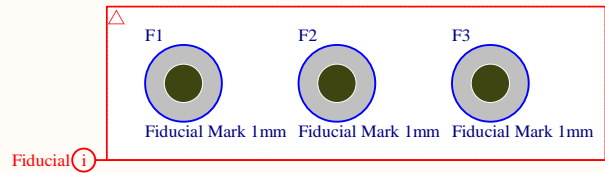
### GPIO Breakout



### DC Power Screw Terminal



### Redundancy



Each ERF/ERM pin is rated for 2.2A current  
200 VAC max 280 VDC max

Title		
IRIS-EXT-04-BRK Breakout Top		
Size	Number	Revision
A3		A
Date:	2/05/2020	Sheet of
File:	C:\Users\...\IRIS-EXT-04-BRK_Top.SchDoc	Drawn By: