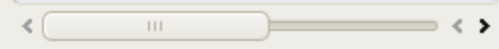


Workspace

jack/spool/qsapeng/workspace

Name	Size	Type
repository		Fol
inverter....	12 KB	info
test.info	5 KB	info
totem.info	3 KB	info



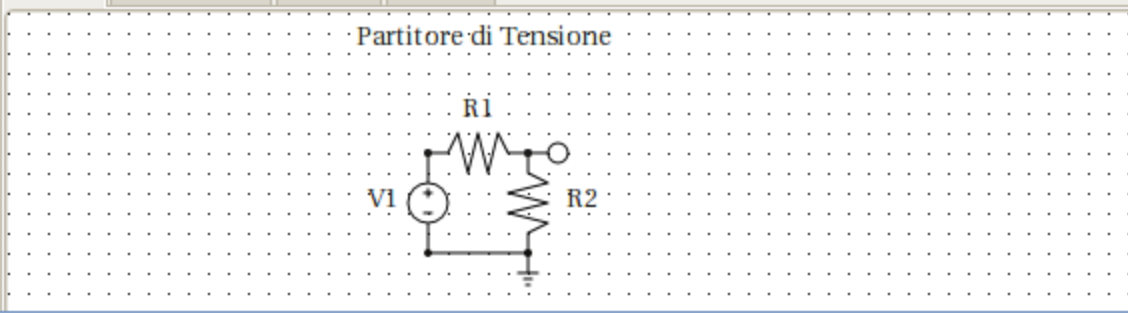
Components

- Resistor
- Conductance
- Inductor
- Capacitor
- Voltage Source
- Current Source
- VCVS
- CCVS

Messages

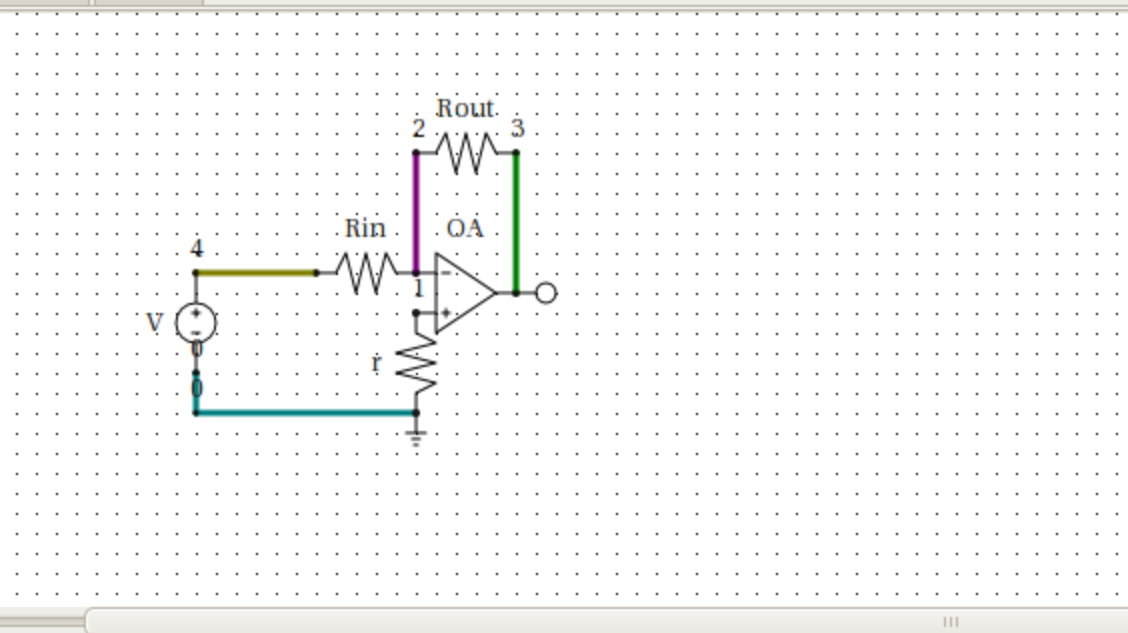
[INFO] Loading file...
 [INFO] assigning nodes...
 [INFO] Saving file...
 [INFO] assigning nodes...
 [INFO] assigning nodes...

Editor Workplane Result Netlist



inverter.info

plane Result Netlist



Properties

Property	Value
▼ Circuit	
▼ Operational Amplifier	
Op...	OA
▼ Resistor	
▼ Re...	Rout
...	<input checked="" type="checkbox"/> True
...	1.00
▼ Re...	r
...	<input checked="" type="checkbox"/> True
...	1.00
▼ Re...	Rin
...	<input checked="" type="checkbox"/> True
...	1.00
▼ Voltage Source	
▼ Vol...	V
...	<input checked="" type="checkbox"/> True
...	1.00