

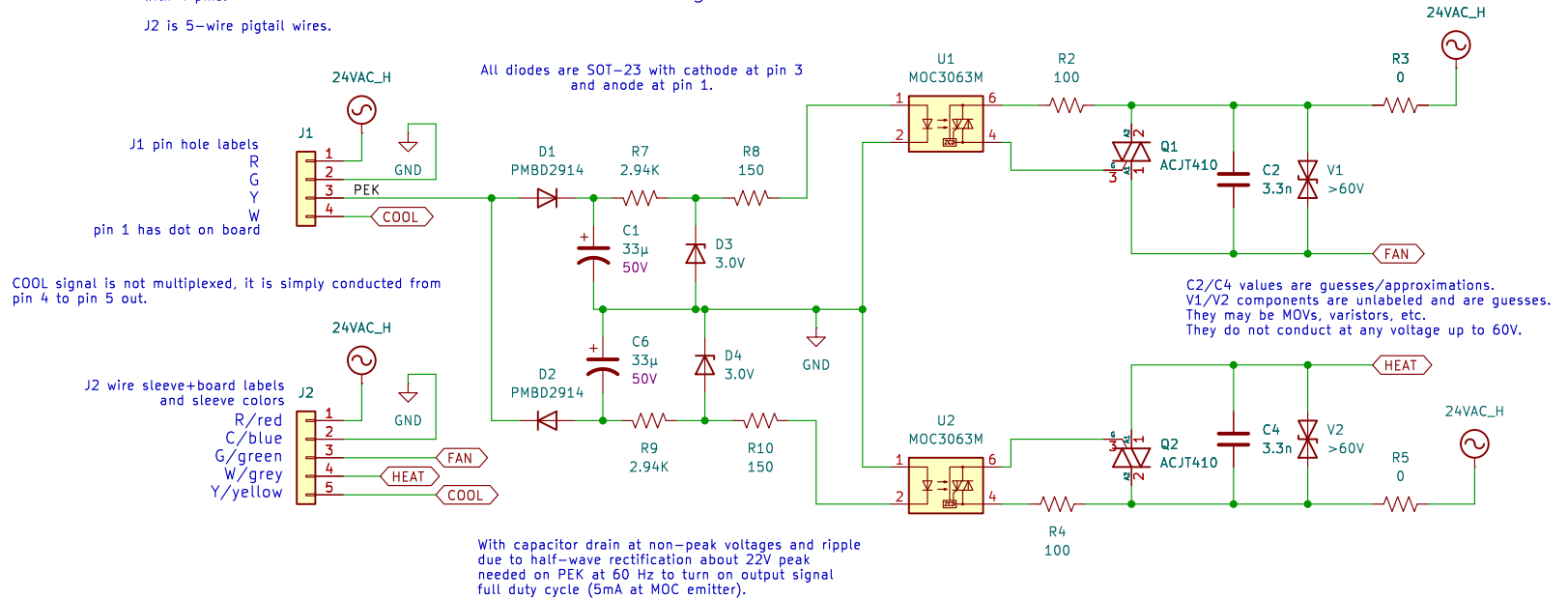
GND is AC neutral

Home HVAC signaling is 24VAC_{rms} (unregulated) ladder logic.

On PEK line, a positive half-wave rectified AC signal of sufficient magnitude turns on U1 which will supply AC from J2 pin 1 to J2 pin 3 at next zero crossing. A negative half-wave rectified signal will do the same at J2 pin 4 from J2 pin 1. Applying a full AC signal will turn both on at the same time.

J1 is push-in spring contact wire connector with 4 pins.

J2 is 5-wire pigtail wires.



COOL signal is not multiplexed, it is simply conducted from pin 4 to pin 5 out.

J2 wire sleeve+board labels and sleeve colors
R/red
C/blue
G/green
W/grey
Y/yellow

All diodes are SOT-23 with cathode at pin 3 and anode at pin 1.

C2/C4 values are guesses/approximations. V1/V2 components are unlabeled and are guesses. They do not conduct at any voltage up to 60V.

With capacitor drain at non-peak voltages and ripple due to half-wave rectification about 22V peak needed on PEK at 60 Hz to turn on output signal full duty cycle (5mA at MOC emitter).

As PEK is likely derived from the AC source with a diode 22.7V peak would be needed on AC source before rectification.

Approximate max current on U1 emitter is about 12mA. This is if D3 cathode rises to 3.06V (spec).

Representative doorbell transformer checked is about 19.2V_{rms} or 54V pk-to-pk.

Turn on current (I_{rr}) of U1 emitter is 5mA.

At 6mA:
pin 2 of U1 is at 0V (ref)
pin 1 of U1 is at 1.5V (max MOC3063M spec)
drop across R8 is 900mV (I_R)
cathode of D3 is at 2.40V (sum)
drop across R8 is 14.94V (I_R)
cathode of D1 is at 17.34V (sum)
drop across D1 is 855mV (spec max at 10mA)
anode of D1 is at 18.89V (sum)

So PEK must go to at least 18.89V peak to turn on U1 and FAN.

Ripple at 6mA on C1 is 3.03V @ 60Hz (6mA drain 1/60th sec).

So PEK must be at +21.92VAC peak, 15.5VAC_{rms} @ 60Hz with positive half wave rectification to turn on U1 and FAN and have them remain on through the AC cycle. Same value with negative half wave rectification to turn on U2 and HEAT.

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