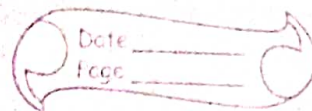


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Compiler Design Assignment - 2

(1.) Generate three address code for the following:-

(i) begin

add = 0

i = 1

j = 1

do

begin

add = add + a[i,j] * b[j,i]

i = i + 1

j = j + 1

end

while i <= 20 and j <= 20

end

Solⁿ: (i) Assuming 'a' and 'b' are arrays of size [20, 20] and of 4 bytes per word, and ~~the~~ ~~matrix~~ the matrices follow row major order, the three address code for the given code can be written as:

1. $add = 0$
2. $i = 1$
3. $j = 1$
4. $t1 = i * 20$
5. $t1 = t1 + j$
6. $t1 = t1 + 4$
7. $t2 = \text{addr}(a) - 84$
8. $t3 = t2[t1]$
9. $t4 = j * 20$
10. $t4 = t4 + i$
11. $t4 = t4 * 4$
12. $t5 = \text{addr}(b) - 84$
13. $t6 = t5[t4]$
14. $t7 = t3 * t6$
15. $t7 = add + t7$
16. $t8 = i + 1$
17. $i = t8$
18. $t9 = j + 1$
19. $j = t9$
20. if $i \leq 20$ goto (22)
21. goto NEXT
22. if $j \leq 20$ goto (4)
23. goto NEXT

(ii) while ($A < C$ and $B > D$) do
 if $A = 1$ then $C = C + 1$
 else

 while $A \leq D$ do
 $A = A + 3$

Soln: 1 (ii) Three address code for the given code is:

1. if ($A < C$) goto (3)
2. goto (15)
3. if ($B > D$) goto (5)
4. goto (15)
5. if ($A = 1$) goto (7)
6. goto (10)
7. $T1 = C + 1$
8. $C = T1$
9. goto (1)
10. if ($A \leq D$) goto (12)
11. goto (1)
12. $T2 = A + 3$
13. $A = T2$
14. goto (10)
15. goto NEXT

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Q12) For the code below what are the cases when we need backpatching?

```
(A) E1 = i >= j;  
    E2 = j != i;  
    E3 = i <= k;  
    if (E2 && E1 && E3)  
        // True section?  
    else  

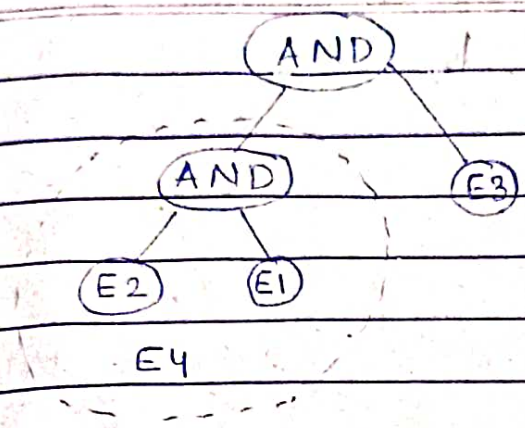
```

Solⁿ: 2(A). Three address code for the boolean expression $E2 \&\& E1 \&\& E3$, assuming initial line number to be 1001, ~~the code~~ can be written as:

```
101. if E2 goto ____  
102   goto ____  
103   if E1 goto ____  
104   goto ____  
105   if E3 goto ____  
106   goto ____
```

Let $(E2 \&\& E1)$ be $E4$.

Binary tree to calculate the backpatching can be given as follows:



~~AND~~

From address word:

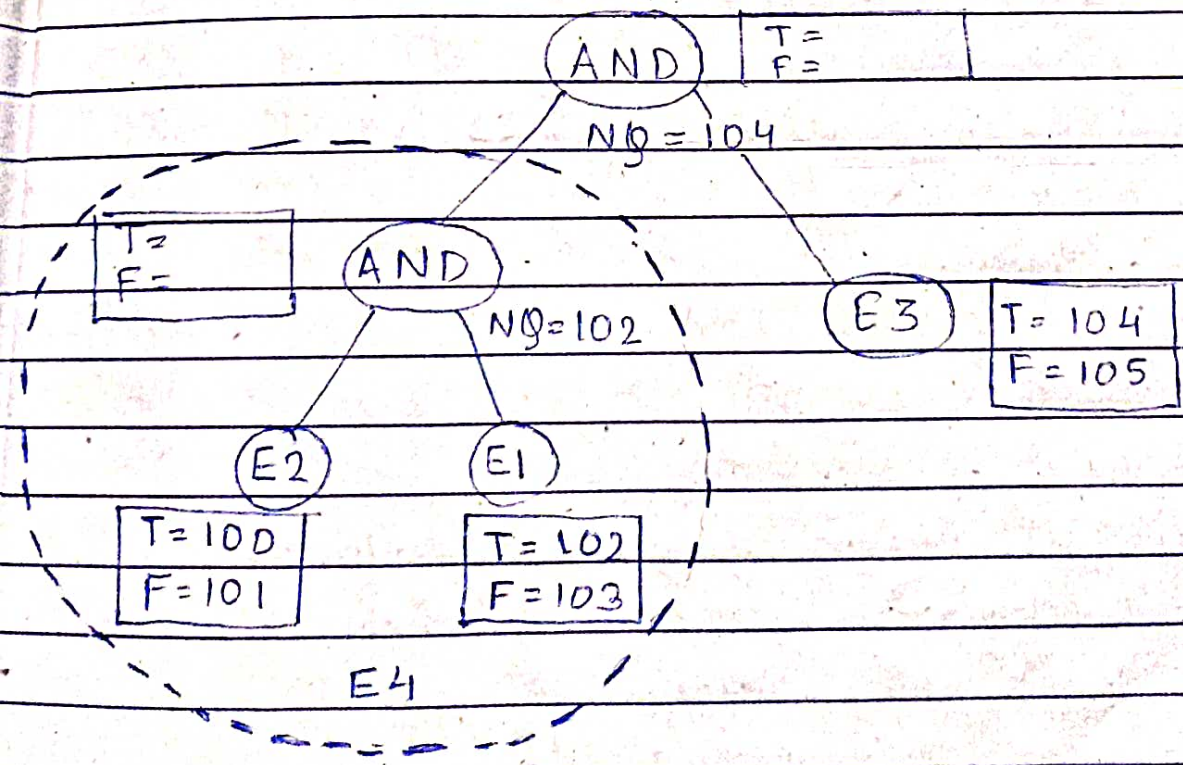
for $E1 \Rightarrow T=102, F=103$

for $E2 \Rightarrow T=100, F=101$

for $E3 \Rightarrow T=104, F=105$

The next quadruple for $E4$ is 102.

The next quadruple for $E4 \& E3$ is 104.



Truth table for $E_4 = E_2 \wedge E_1$ is:

E_2	E_1	AND
0	0	0
0	1	0
1	0	0
1	1	1

Therefore, backpatching is required when $E_2 = 1$ (true). So, backpatch statement for E_4 will be: Backpatch(100, 102)

Truth list for $E_4 = 100, 102$.

Since we backpatched (100, 102), truth list will be, only 102.

False list for $E_4 = 101, 103$.

Similarly, truth table for $E_4 \wedge E_3$ is:

E_4	E_3	AND
0	0	0
0	1	0
1	0	0
1	1	1

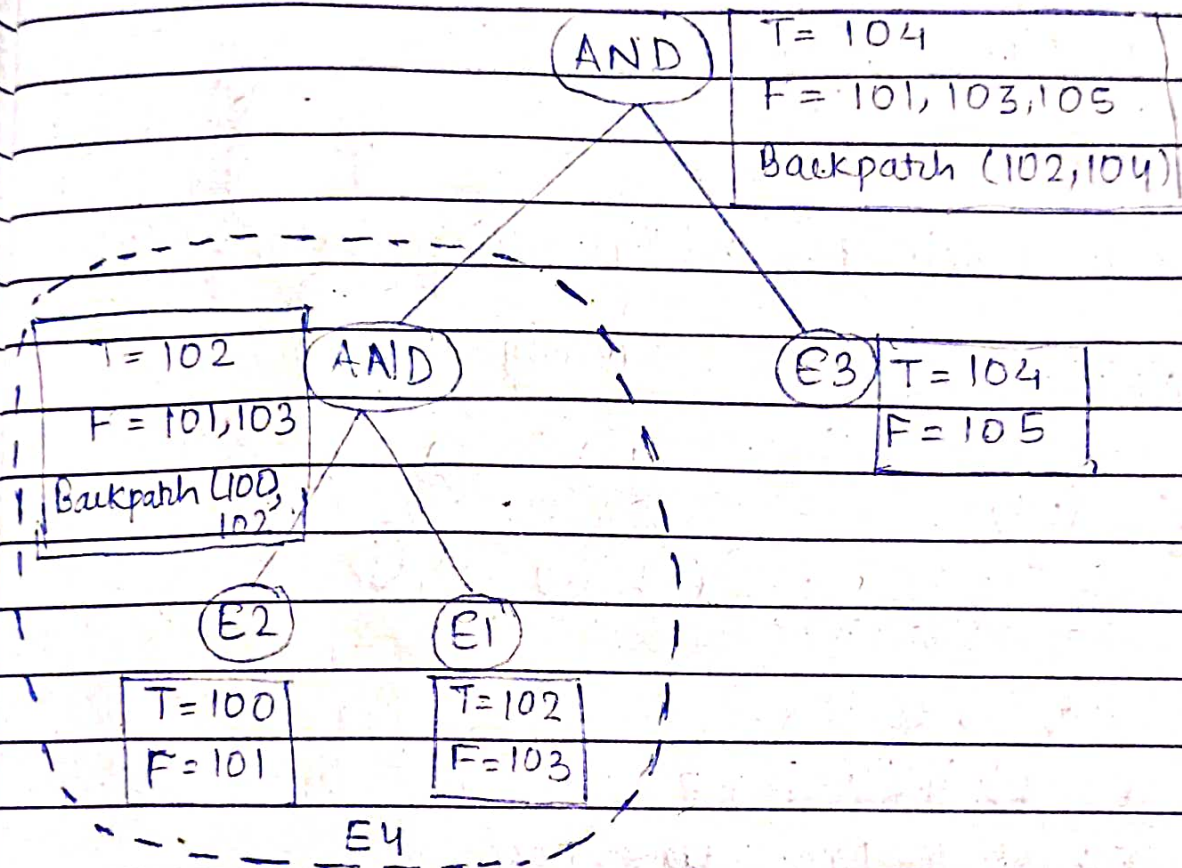
Therefore backpatching is required when $E_4 = 1$ (true). So backpatch statement will be;

Backpatch(102, 104)

Truth list = 102, 104 = 104

False list = ~~100~~ 101, 103, 105.

Therefore, final ~~opt~~ tree will be:



(B) $E1 = i > j;$

$E2 = j \neq 20;$

if $(E2 \&\& E1)$

{ // True section }

else

{ // False section }

Soln: 2 (B) The three address for the boolean expression $E2 \&\& E1$, assuming initial line no. to be 101, can be written as:

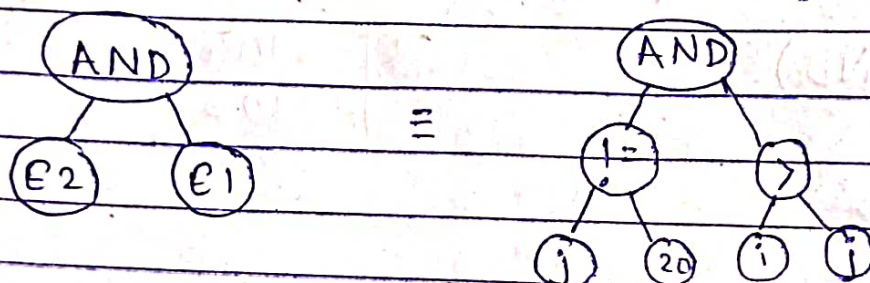
101. if (E2) then goto _____

102. goto _____

103. if (E1) then goto _____

104. goto _____

Binary tree to calculate backpatching can be given as:

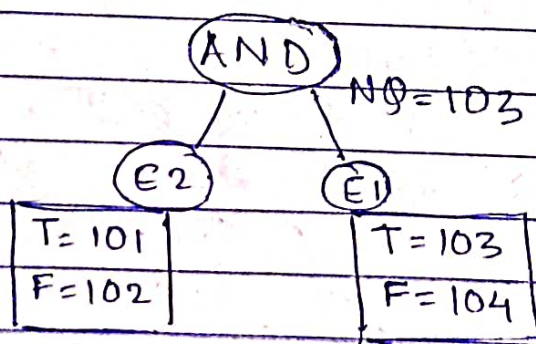


From these address code,

for E2 \Rightarrow T=101, F=102

for E1 \Rightarrow T=103, F=104.

The next Quadruple is 103. Therefore



Truth table for E2 and E1 is:

E2	E1	AND
0	0	0
0	1	0
1	0	0
1	1	1

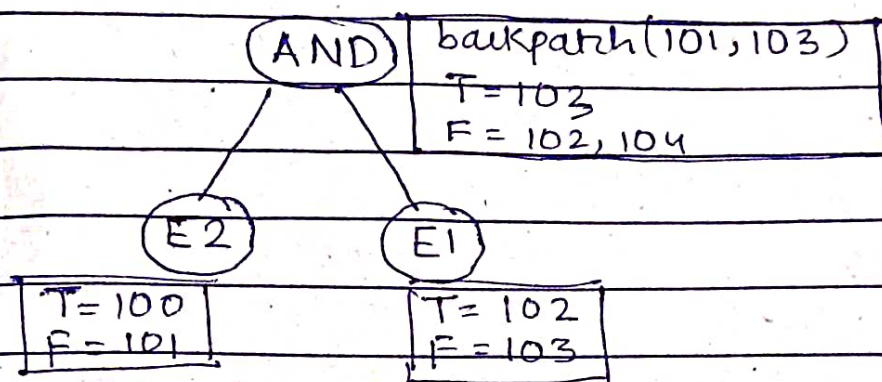
Therefore, backpatching is required when $E2 = 1$ (true)

So, backpatch statement will be: backpatch(101, 103)

Truth list = 101, 103 = 103

False list = 102, 104.

So, final tree will be:



~~(3.) For the given expression:~~

~~$S + r/3 + q - t * 5 +$~~

(3.) For the given expression: $S + r/3 + q - t * 5 + u + v * w / u$
what is the minimum number of temporary variables needed to create a three-address code in static single assignment form? Show the complete solution.

Solⁿ (3): The rules in static assignment form are:

- (i) Each variable must be assigned exactly once.
- (ii) Every variable must be defined before it is used.
- (iii) All of the uses reached by the assignment must be renamed.

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Given expression: $s + r/3 + q - t * 5 + u + v * w / u$

So,

$$t1 = r/3$$

$$t2 = t * 5$$

$$t3 = v * w$$

$$t4 = t3 / u$$

$$t5 = s + t1$$

$$t6 = q + t5$$

$$t7 = t6 - t2$$

$$t8 = t7 + u$$

$$t9 = t8 + t4$$

Total 9 temporary variables are required to create a three-address code in static single assignment form.

(4.) Consider a syntax directed translation

$$E \rightarrow E * T \quad \{ E.val = E.val * T.val \}$$

$$E \rightarrow T \quad \{ E.val = T.val \}$$

$$T \rightarrow F - T \quad \{ T.val = F.val - T.val \}$$

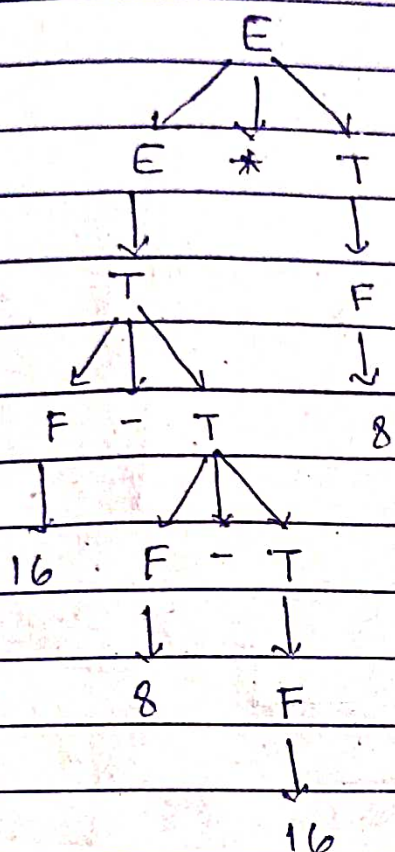
$$T \rightarrow F \quad \{ T.val = F.val \}$$

$$F \rightarrow 8 \quad \{ F.val = 8 \}$$

$$F \rightarrow 16 \quad \{ F.val = 16 \}$$

Evaluate the input string $16 - 8 - 16 * 8$ and find number of reduction. Draw the syntax directed translation tree.

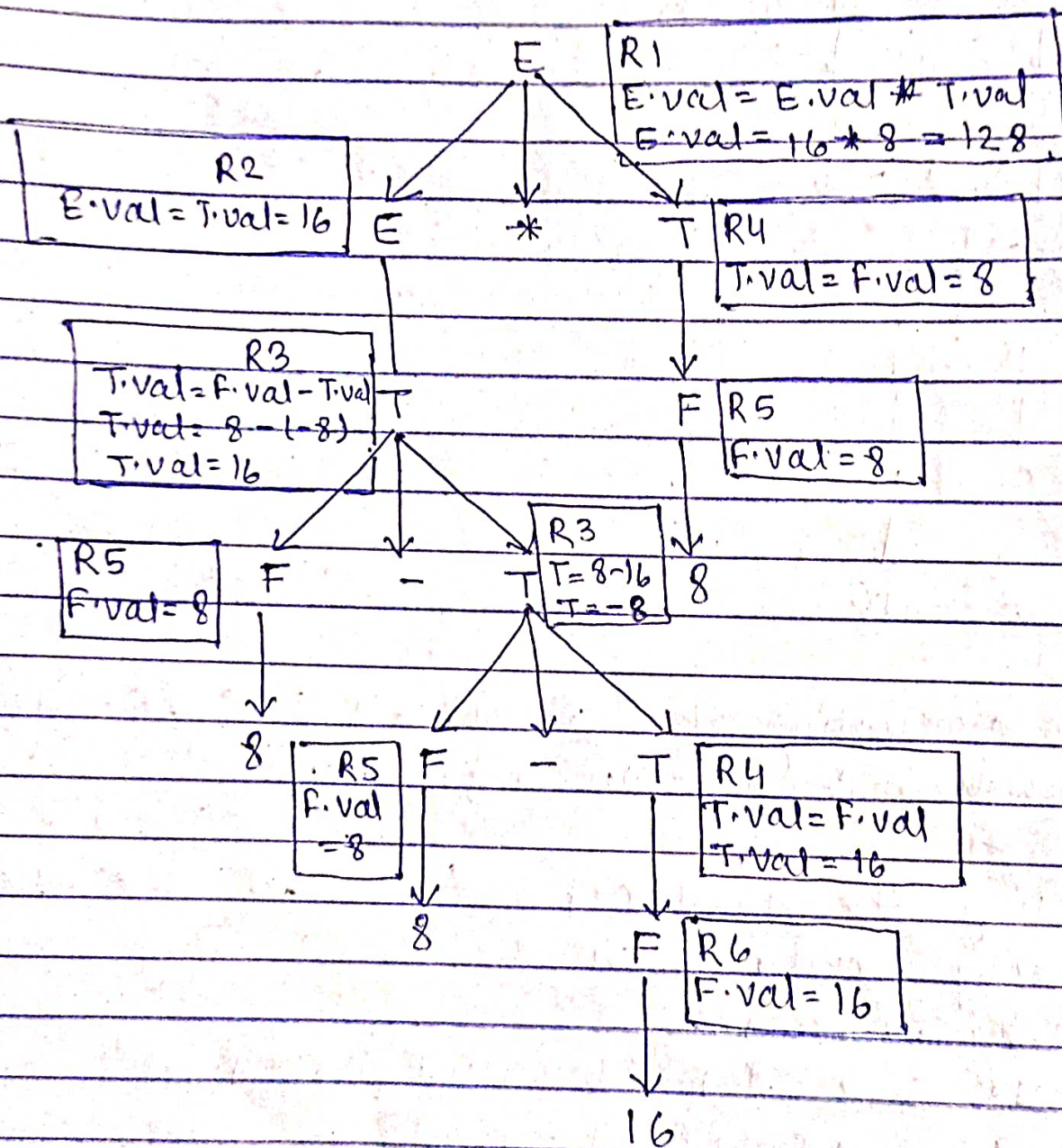
Solⁿ(4): The SDT tree for the given expression will be:



~~For evaluating the given expression, we traverse the tree in an inorder.~~

For evaluating the given expression, we traverse the tree in order, and perform the corresponding reduction while moving upwards.

- Given,
1. $E \rightarrow E * T \quad \{ E.val = E.val * T.val \}$
 2. $E \rightarrow T \quad \{ E.val = T.val \}$
 3. $T \rightarrow F - T \quad \{ T.val = F.val - T.val \}$
 4. $T \rightarrow F \quad \{ T.val = F.val \}$
 5. $F \rightarrow 8 \quad \{ F.val = 8 \}$
 6. $F \rightarrow 16 \quad \{ F.val = 16 \}$



Thus, $16 - 8 - 16 * 8 = 128$

and total number of reductions are 10.

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15) Explain the significance of intermediate code generation in terms of compiler design.

Ans(5-): In the analysis-synthesis model of a compiler, the front end of a compiler translates a source program into an independent intermediate code, then the back end of the compiler uses this intermediate code to generate the target code (which can be understood by the machine). The benefits of using machine independent intermediate code are:

- 1) Because of the machine independent intermediate code, portability will be enhanced.
- 2) Retargeting is facilitated.
- 3) It is easier to apply source code modification to improve the performance of source code by optimising the intermediate code.
- 4) If we generate machine code directly from source code then for n target machines we will have n optimisers and n code generators but if we have a machine independent intermediate code, we will have only one optimiser.

Intermediate code can be either language specific (eg. - Bytecode) or language independent (eg. - three address code).