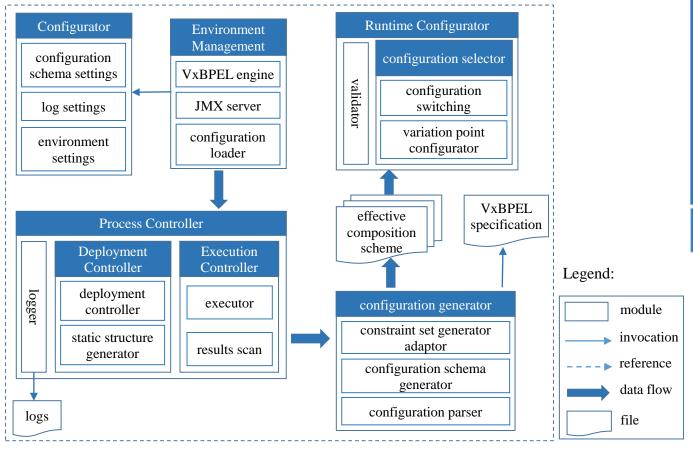
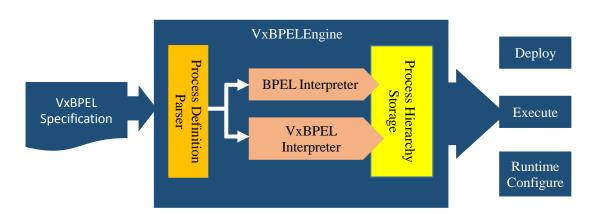


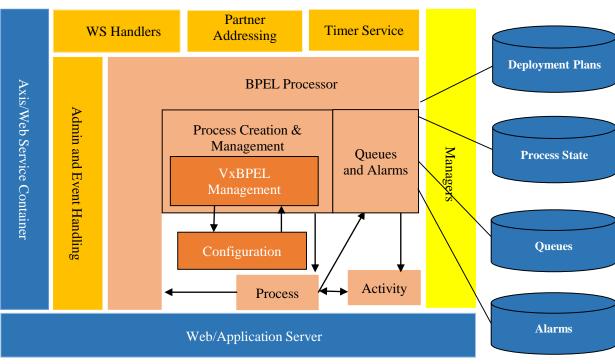
(a) Architecture of VxBPEL Designer



(b) Architecture of MX4B



(c) Overview of VxBPEL Engine



(d) Architecture of VxBPEL Engine