TinyViT: A Small Vision Transformer for Image Classification

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Abstract

Vision Transformers (ViTs) excel in image classification but require large datasets and complex architectures. We introduce TinyViT, a minimalist ViT that retains core components patch embedding and attention while drastically reducing scale. By simplifying token processing and prioritizing parameter efficiency, we test whether a tiny ViT can achieve good accuracy despite limited complexity. Experiments on standard benchmarks show TinyViT delivers acceptable results even with reduced data requirements, challenging the assumption that ViTs inherently demand heavy resources. This work demonstrates that minimalist transformer architectures can learn meaningful representations, offering a pathway to simpler, more accessible models without sacrificing core functionality.

1 Introduction

Transformers, introduced by [Vaswani, 2017] for natural language processing (NLP), have become the dominant architecture for sequence modeling due to their scalability and self-attention mechanisms. Inspired by their

success in NLP, [Alexey, 2020] pioneered the Vision Transformer (ViT), demonstrating that transformers can achieve state-of-the-art results in image recognition by treating images as sequences of patch tokens. By splitting an image into fixed-size patches, linearly embedding them, and processing the sequence with a standard transformer encoder, ViT outperformed convolutional neural networks (CNNs) [He et al., 2016] on large-scale datasets like ImageNet when pretrained on massive datasets (e.g., JFT-300M). However, ViT's strong performance comes at a cost: it requires extensive computational resources and large pretraining datasets, raising practical barriers for adoption in settings where such infrastructure is unavailable.

In this work, we aim to (1) elucidate the foundational mechanics of Vision Transformers and (2) present TinyViT, a minimalist implementation designed to test the viability of ViTs in simplified, resourceefficient settings. Unlike the original ViT, which emphasizes scaling to massive datasets, TinyViT reduces architectural complexity employing fewer transformer layers, smaller embedding dimensions, and streamlined attention mechanisms while retaining the core principles of patch-based processing and self-attention. We evaluate TinyViT on widely adopted benchmarks like CIFAR-10 and CIFAR-100 [Krizhevsky et al., 2009], and STL-10 [Coates et al., 2011].

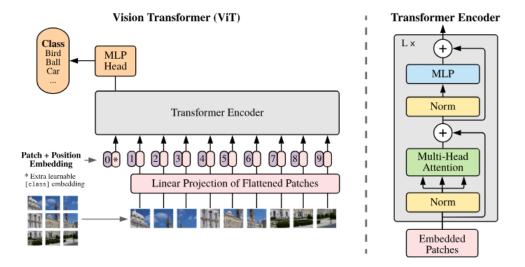


Figure 1: The Vision Transformer (ViT) architecture from [Alexey, 2020]. On the left the model architecture while on the right the Transformer encoder block architecture from [Vaswani, 2017]. $\mathbf{2}$ Architecture

In recent the Transformer years, architecture—originally developed for natural language processing—has been successfully applied to computer vision tasks. The Vision Transformer (ViT) rethinks image classification by treating an image as a sequence of patches, much like tokens in a sentence, and processes them using standard Transformer blocks. In this chapter, we detail the architecture of the Vision Transformer and provide the mathematical foundations underlying its design.

2.1 Overview

The Vision Transformer (ViT) adapts the Transformer architecture—originally developed for natural language processing—to image classification tasks. Instead of processing an image as a whole, ViT divides it into a sequence of patches (analogous to tokens) and processes these patches through a stack of Transformer encoder blocks. The key steps in the ViT pipeline are:

1. Image Patch Embedding: The input image is divided into fixed-size patches that are flattened and projected into a latent space.

- 2. Positional Encoding: Since Transformers are permutation-invariant, positional embeddings are added to the patch embeddings.
- 3. Transformer Encoder: A series of Transformer encoder blocks processes the sequence of embeddings using selfattention and feed-forward networks.
- 4. Classification Head: The encoder output is used by a classification head for the final prediction.

2.2 Image Patch Embedding

Patch Extraction

Given an input image

$$\mathbf{X} \in \mathbb{R}^{H \times W \times C},\tag{1}$$

where H and W denote the height and width of the image, and C denotes the number of channels, the image is divided into N patches of size $P \times P$. Hence, the number of patches is:

$$N = \frac{HW}{P^2}. (2)$$

Flattening and Linear Projection

Each patch, denoted as $\mathbf{x}_p \in \mathbb{R}^{P \times P \times C}$, is flattened into a vector of dimension $P^2 \cdot C$. A learnable linear projection (implemented as a fully connected layer) maps this vector into a D-dimensional embedding space:

$$\mathbf{z}_p = \mathbf{W}_e \mathbf{x}_p + \mathbf{b}_e,$$

where $\mathbf{W}_e \in \mathbb{R}^{D \times (P^2 \cdot C)}$ and $\mathbf{b}_e \in \mathbb{R}^D$. (3)

After processing all patches, we obtain a sequence of embeddings:

$$\mathbf{Z} = [\mathbf{z}_1, \mathbf{z}_2, \dots, \mathbf{z}_N] \in \mathbb{R}^{N \times D}. \tag{4}$$

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