

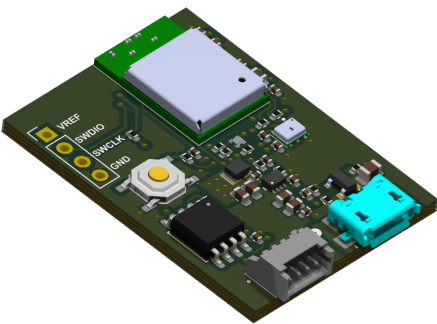
PCB NINA PROJECT

Variant: CHECKED

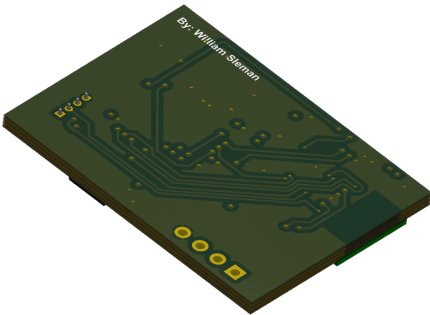
2026-01-06
Rev 1.0

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TOP VIEW



BOTTOM VIEW



NOTES

Not fitted components are marked as **X**

- DRAFT - Very early stage of schematic, ignore details.
- PRELIMINARY - Close to final schematic.
- CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.
- RELEASED - A board with this schematic has been sent to production.

CHECKED 06-JAN-2026

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for
informational design
notes.

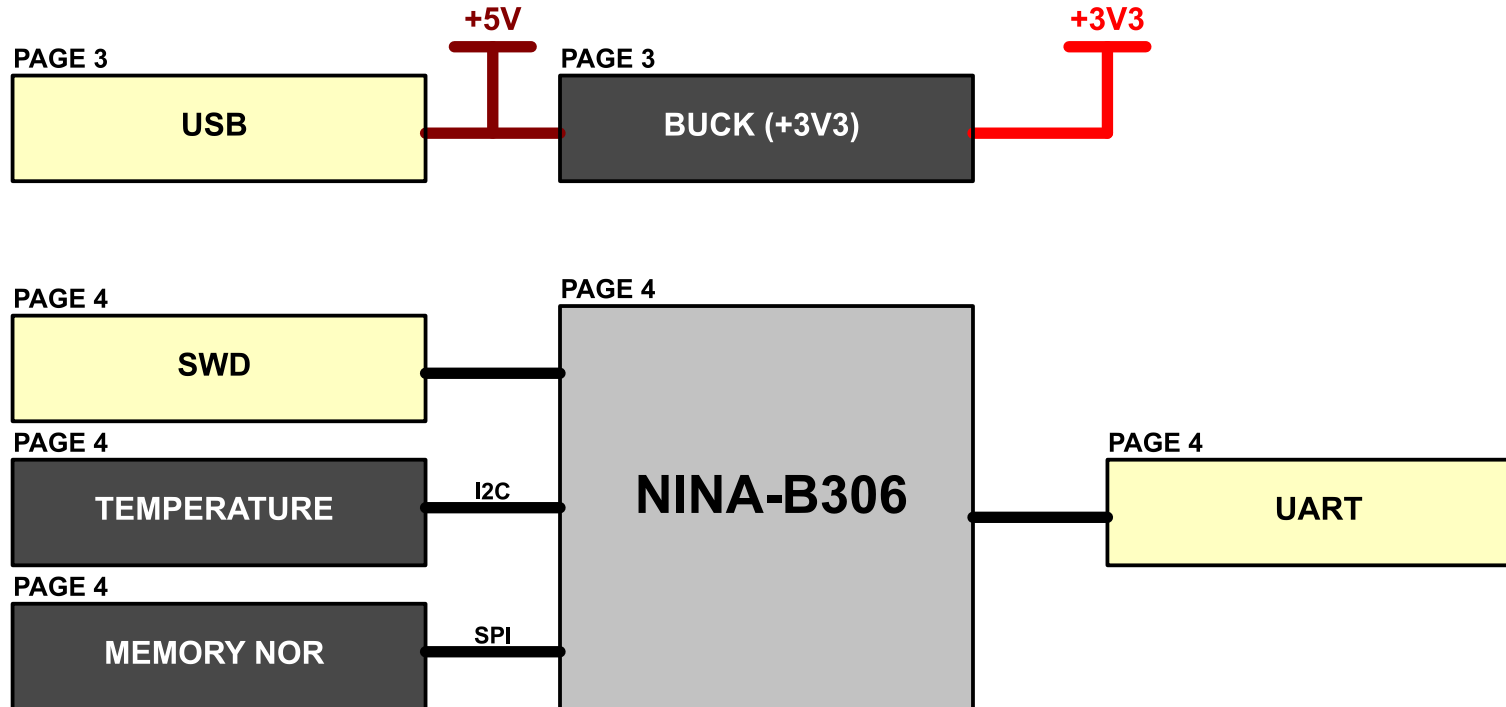
DESIGN NOTE:
Example text for
debug notes.

DESIGN NOTE:
Example text for
cautionary design
notes.

DESIGN NOTE:
Example text for
critical design
notes.

LAYOUT NOTE:
Example text for
critical layout
guidelines.

[02] - Block Diagram



Author: William Sleman

Sheet: /[02] - Block Diagram/

File: sheet2.kicad_sch

Title: PCB NRF52 PROJECT

Size: A4

Date: 2025-05-16

Rev: 1.0

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Id: 2/5

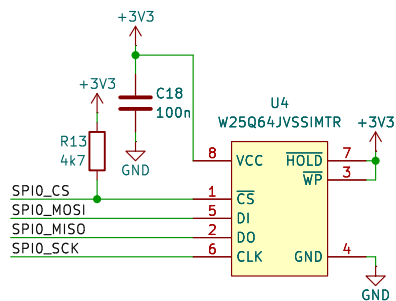
CONNECTOR



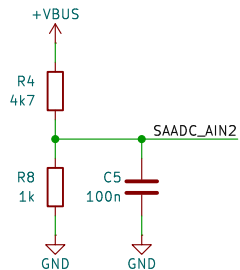
Id: 3/5

[04] - MCU

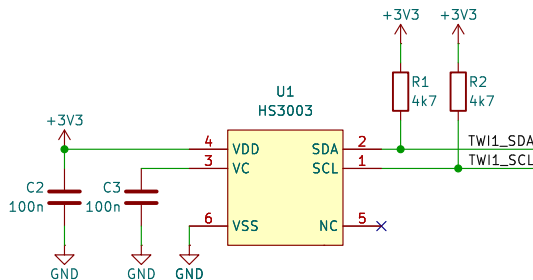
NOR



INPUT READ



TEMPERATURE SENSOR



U3
NINA-B306

55	USB_DM
54	USB_DP
1	GPIO_1/P0.13
2	GPIO_2/P0.14
3	GPIO_3/P0.15
4	GPIO_4/P0.16
5	GPIO_5/P0.24
7	GPIO_7/P0.25
16	GPIO_16/P0.03
17	GPIO_17/P0.28
18	GPIO_18/P0.02
20	GPIO_20/P0.31
21	GPIO_21/P1.12
22	GPIO_22/P1.13
23	GPIO_23/P0.29
24	GPIO_24/P0.30
25	GPIO_25/P0.04
27	GPIO_27/P0.05
34	GPIO_34/P1.08
35	GPIO_35/P1.01
36	GPIO_36/P1.02
37	GPIO_37/P1.03
38	GPIO_38/P1.10
39	GPIO_39/P1.11
40	GPIO_40/P1.15
41	GPIO_41/P1.14
42	GPIO_42/P0.26
43	GPIO_43/P0.06
44	GPIO_44/P0.27

SAADC_AIN2

SPIO_CS

SPIO_MISO

SPIO_SCK

UART_RX

UART_TX

LED_BLUE

SPIO_MOSI

13	ANT
28	GPIO_28/P0.09/NFC1
29	GPIO_29/P0.10/NFC2
19	RESET_N/P0.18
11	SWDCLK
15	SWDIO
45	GPIO_45/P0.07/TRACE_CLK
8	GPIO_8/P1.00/TRACE_D0/SWD0
46	GPIO_46/P0.12/TRACE_D1
32	GPIO_32/P0.11/TRACE_D2
33	GPIO_33/P1.09/TRACE_D3
47	GPIO_47/P0.23/QSPID3
48	GPIO_48/P0.21/QSPID1
49	GPIO_49/P0.22/QSPID2
50	GPIO_50/P0.20/QSPID0
51	GPIO_51/P0.17/QSPIDCS
52	GPIO_52/P0.19/QSPIDCLK

FB1
BLM18EG101TN1D

31 VBUS |

10 VCC |

9 VCC_I0 |

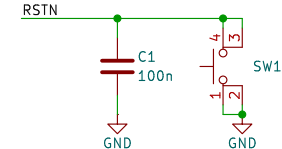
6 GND |

56 EGP |

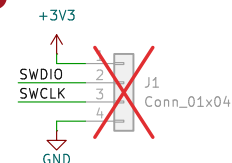
71 EAGP |

6 GND |

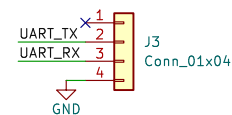
RESET



SWD

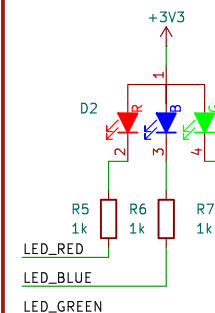


UART



Use a FTDI module to interface.

LED RGB



Author: William Sleman

Sheet: /[04] - MCU/
File: sheet4.kicad_sch

Title: PCB NRF52 PROJECT

Size: A4 Date: 2025-05-16

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Rev: 1.0

Id: 4/5

[05] - Revision History

DD.MM.YYYY - xxx Revision
Variant: xxx

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Variant: xxx

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Author: William Sleman

Sheet: /[05] - Revision History/

File: sheet5.kicad_sch

Title: PCB NRF52 PROJECT

Size: A4

Date: 2025-05-16

Rev: 1.0

KiCad E.D.A. 9.0.2

Id: 5/5