For ADD drift 1Hh sinesoid A= INV

For ADD, input referred amplitude = Inv

d) Naise charge
$$V_X$$
 = them.1 note integraled on C_4 3 C_5
 C_5
 C_5
 C_7
 C_7
 C_8
 C_8

f)
$$V_0^2 = \frac{8^2}{C_1^2} = \frac{1.24e^{-32}C^2}{|pF^2|} = 12.42 \text{ mV}^2 = 111.5 \text{ mVr}$$

All of the charge error in the circuit will be forced to the origin by the feedback action of the amplifier, regardless of CDS action.