

LEFT					RIGHT			
UART0 TX	I2C0 SDA	SPI0 RX	GP0	1	VBUS			
UART0 RX	I2C0 SCL	SPI0 CSN	GP1	2	VSYS			
			GND	3	GND			
	I2C1 SDA	SPI0 SCK	GP2	4	3V3_EN			
	I2C1 SCL	SPI0 TX	GP3	5	3V3 OUT			
UART1 TX	I2C0 SDA	SPI0 RX	GP4	6		ADC_VREF		
UART1 RX	I2C0 SCL	SPI0 CSN	GP5	7	GP28	ADC2		
			GND	8	GND	AGND		
	I2C1 SDA	SPI0 SCK	GP6	9	GP27	ADC1	I2C1 SCL	
	I2C1 SCL	SPI0 TX	GP7	10	GP26	ADC0	I2C1 SDA	
UART1 TX	I2C0 SDA	SPI1 RX	GP8	11	RUN			
UART1 RX	I2C0 SCL	SPI1 CSN	GP9	12	GP22			
			GND	13	GND			
	I2C1 SDA	SPI1 SCK	GP10	14	GP21		I2C0 SCL	
	I2C1 SCL	SPI1 TX	GP11	15	GP20		I2C0 SDA	
UART0 TX	I2C0 SDA	SPI1 RX	GP12	16	GP19	SPI0 TX	I2C1 SCL	
UART0 RX	I2C0 SCL	SPI1 CSN	GP13	17	GP18	SPI0 SCK	I2C1 SDA	
			GND	18	GND			
	I2C1 SDA	SP1 SCK	GP14	19	GP17	SPI0 CSN	I2C0 SCL	UART0 RX
	I2C1 SCL	SP1 TX	GP15	20	GP16	SPI0 RX	I2C0 SDA	UART0 TX