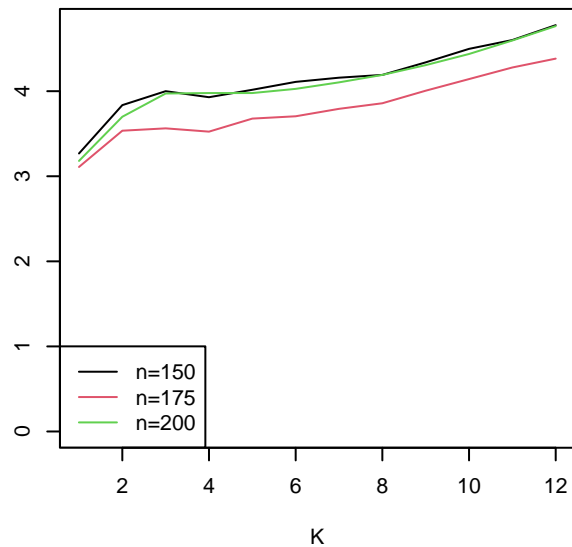
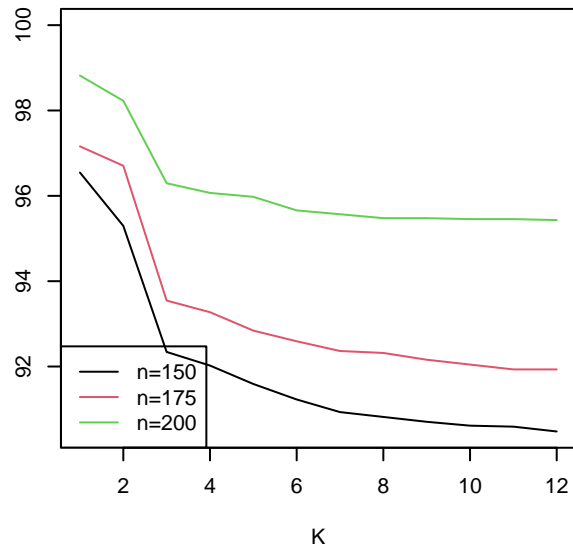


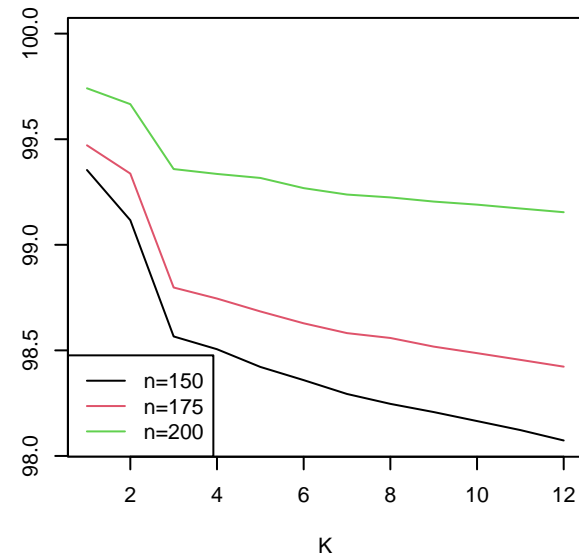
FPR (sim1)



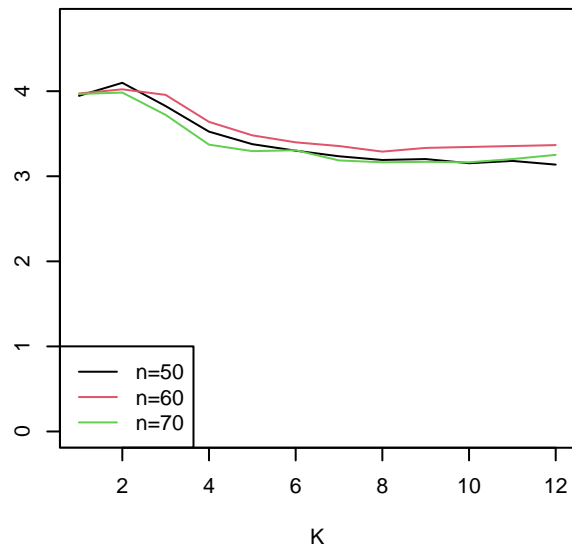
TPR (sim1)



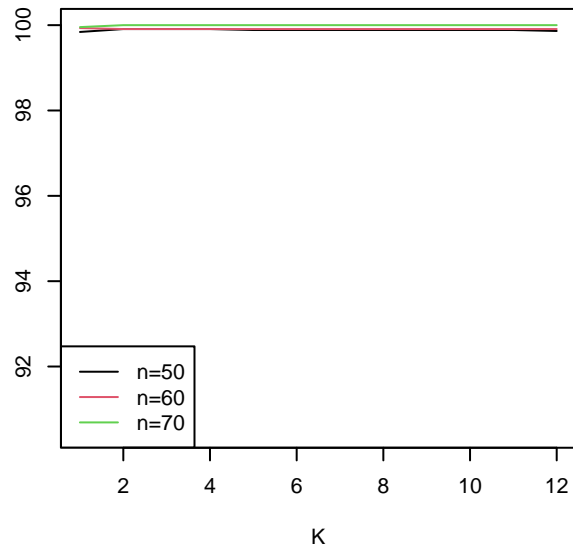
AUC (sim1)



FPR (sim2)



TPR (sim2)



AUC (sim2)

