

### Practice Quiz (2-3)

**Q1. The availability of two stack pointers plays an important role in providing support for OS functionality. Explain how the two stack pointers are commonly used for stack management of the OS kernel and multiple tasks.**

**Q2. What is tail-chaining in the context of interrupts? What are the motivation and advantage of tail-chaining?**

**Q3. How many wires are required for serial wire debug? Name three debugging activities it enables.**

**Q4. Explain how the following assembly code handles a supervisor call**

```
SVC_Handler
TST lr, #4
ITE EQ
MRSNE R0, MSP
MRSEQ R0, PSP
LDR R0, [R0, #24]
LDRB R0, [R0, #-2]
```

**Q5. What are the mechanisms for communication between threads and what is the main purpose/functionality of each mechanism?**