

# POLITECNICO DI BARI

# DIPARTIMENTO DI INGEGNERIA ELETTRICA E DELL'INFORMAZIONE LAUREA MAGISTRALE IN INGEGNERIA DELL'AUTOMAZIONE

Digital Programmable Systems

Prof. Dr. Eng. Martino De Carlo

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Snake game on FPGA

Student: Nicola Saltarelli



#### Abstract

This documentation describes the implementation of the Snake game on the FPGA DE10-Lite platform using the VHDL hardware description language. The software used for the game's implementation is Quartus Prime version 20.1.1.

Snake is a classic game, originating in the 1970s, which gained enormous popularity with the spread of Nokia mobile phones in the late 1990s. The concept of the game is simple: the player controls a snake that moves within a game area. The objective is to eat the food that appears at random points on the screen, causing the snake to grow by one unit each time a piece of food is consumed. The game ends when the snake collides with itself or the edges of the game area and has no more lives.

In the following sections of the documentation, the main components of the project, their organization, and the logic underlying the game's operation will be illustrated.

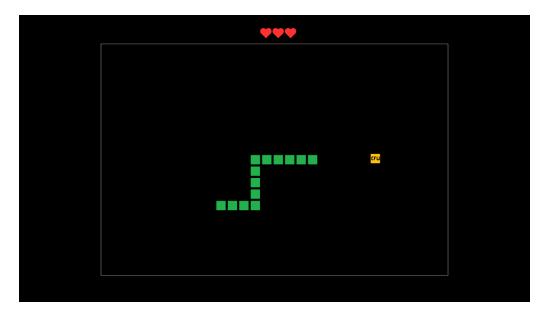


Figure 1: Game graphics.





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## 1 Project Architecture

In this section, the schematic of the project will be presented (Figure 2), which is characterized by three distinct blocks.

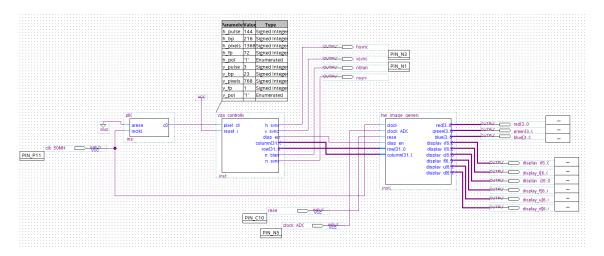


Figure 2: Schematic of the project.

The first block, named "pll0", the PLL0 (Phase-Locked Loop 0) in Quartus Prime is a component dedicated to generating a divided clock signal from an input clock. In this instance, the PLL0 receives as input the 50 MHz clock signal from the FPGA board and a reset signal constantly held at a logic low level.

The consistently low reset signal indicates that the PLL0 should not be initialized or reset at system startup. In other words, the PLL0 begins operation immediately without needing to go through a reset phase.

Essentially, Quartus Prime's PLL0 acts as a clock divider, allowing for the generation of clock signals at desired frequencies from a base input clock. In the project folder, there is a guide dedicated to the generation of PLL0, titled "Using the VGA.pdf", provided by Prof. Dr. Eng. Martino De Carlo.

The vga\_controller block is responsible for managing the synchronization and control signals necessary for correct visual output. This module interfaces directly with the FPGA's clock signal provided by the "pll0" block and with a constant logic level reset input, ensuring uninterrupted operation without the need for initialization procedures upon system startup.

This module provides several outputs, each with a defined purpose in coordinating the display process:

- Horizontal and Vertical Sync Signals (hsync, vsync): These signals are assigned to specific
  pins on the FPGA, as outlined in the board's manual. They facilitate precise timing for
  horizontal and vertical synchronization, ensuring accurate image rendering.
- Display Enable Signal (disp\_ena): This signal determines the activation status of the display. By controlling the display enablement, it allows dynamic control over when images are presented on the screen.
- Column and Row Position Signals (column[31..0], row[31..0]): These signals determine the exact positioning of pixels within the display. They serve as inputs to the subsequent



"hw\_image\_generator," guiding the generation of graphics and game logic with pixel-level accuracy.

• Blanking and Synchronization Signals (nblank, nsync): These signals provide additional information about the display's status. They indicate whether the current pixel is within the active display area or part of the synchronization intervals, though they are not utilized in this project.

It is important to specify that the parameters used in the vga\_controller are specific to the Samsung LT19B300 monitor used in the project. Therefore, these parameters must be modified if the code in the project folder is to be reused with a different monitor.

Finally, the third block that characterizes the schematic is the "hw\_image\_generator," which constitutes the core of the project as it handles the generation of images and the game logic. This block receives several fundamental inputs necessary for the game's operation:

- 50MHz Clock from the board: Used for general timing and game operation.
- 10MHz Clock from the ADC: Used for specific functions related to the ADC (Analog-to-Digital Converter).
- Reset: Used to initialize the game to its default conditions.

Additionally, the "hw image generator" receives three inputs from the "vga controller":

- disp\_enable.
- row[31..0].
- column[31..0].

The outputs of the "hw image generator" include:

- red[3..0], blue[3..0], green[3..0]: These signals represent the red, blue, and green color channels for the display. Each channel consists of 4 bits, allowing the definition of color intensity and their combination to achieve full color representation on the screen.
- Other outputs related to the 7-segment display: These outputs are used to display additional information on the 7-segment display present on the FPGA board.

Further details on the operation of this block will be explained in the following section, where the VHDL code will be discussed in more detail.

# 2 Codes description

In this section of the documentation, the VHDL code implemented will be described. This will provide a better understanding of the design choices made and the underlying logic of the game. The code has been structured following modular principles, with "hw\_image\_generator.vhd" serving as the main component. However, within this code, only the graphical aspects are managed, while the game logic is delegated to separate entities such as "snake\_entity.vhd," "food.vhd," "score.vhd," and "joystick.vhd."

These modules have been carefully designed to handle specific functionalities of the game, ranging from snake movement and food interaction to scorekeeping and joystick input processing.

This structured approach to coding offers several advantages. Firstly, it enhances readability and comprehensibility, as each module is responsible for a well-defined aspect of the game's functionality. This facilitates problem-solving, maintenance, and future modifications. Additionally, the modular approach promotes reusability, allowing individual components to be used in other projects or scenarios requiring similar functionality.



## 2.1 Snake movement and logic

The "snake\_entity.vhd" code is responsible for managing the snake's movement logic, collision detection, and the increase in the snake's length when it eats food. The flowchart in Figure 3 provides a simplified representation of the "snake\_logic" process within the architecture of the "snake\_entity" entity.

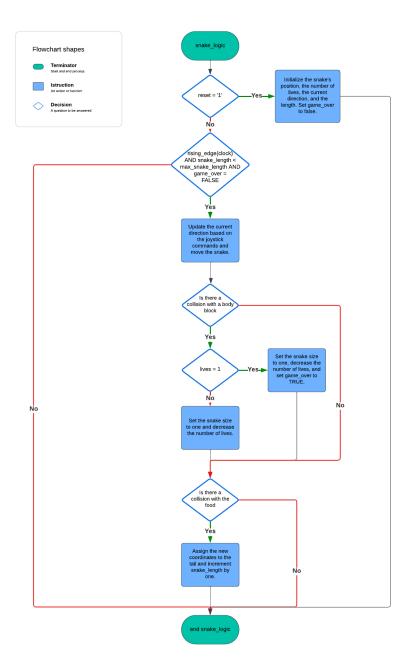


Figure 3: Snake logic flowchart.

We will now examine specific portions of the code, focusing particularly on how snake movement occurs and how collisions with the game boundaries, itself, and food are managed.



The following code (Code 1) is responsible for the snake's movement within the game, employing a mechanism similar to a shift register.

A shift register is a type of register where data is shifted from one flip-flop to the next. In an n-bit shift register, each bit takes the value of the previous bit, creating a propagation effect of values through the register. In the case of the snake, the coordinates of each segment are moved to the position of the previous segment, similar to how data is shifted in a register. The head of the snake receives new coordinates (managed separately in the code), and these coordinates propagate along the snake's body, updating the position of each segment.

#### **Code 1:** Move snake's body

```
1 FOR i IN max_snake_length - 1 DOWNTO 1 LOOP
2     IF i < snake_length THEN
3         internal_snake(i) < internal_snake(i - 1);
4     END IF;
5 END LOOP;</pre>
```

The ultimate effect of this mechanism is that the snake appears to move smoothly across the game grid. When the snake's head changes position (e.g., in response to a direction command), this new position is transmitted along the snake's body, creating the illusion of continuous and coherent movement. Each segment of the body follows the preceding segment, maintaining the shape and integrity of the snake during its motion.

Another code segment worth commenting on is the one concerning the movement of the snake's head based on the current direction stored in the variable "current direction". (Code 2)

## Code 2: Move snake's head

For each possible value of "current\_direction", conditional checks are performed to determine if the snake's head can move in that direction, ensuring that such movement adheres to the game's boundaries. This approach ensures that the snake's movement is controlled and consistent with the rules and boundaries defined by "game\_top", "game\_left", "game\_bottom", and "game\_right" variables, along with the dimensions of the snake's rectangle ("rect\_width" and "rect\_height").

The final code section we will examine in this subsection concerns collisions, which can occur either with the snake's body or with food. Collisions with the game boundary are not explicitly handled because the snake is constrained within the boundaries by first checking the head coordinates (Code 2). Therefore, when the snake reaches the edge of the game field, on the next clock cycle, the coordinates of two snake blocks align, triggering a collision with its own body. This explains why the snake, when it has a length of one, can collide with the boundaries without losing lives.



#### Code 3: Snake collisions

```
-- Check for collisions
1
2
   FOR i IN 1 TO max_snake_length - 1 LOOP
        IF i < snake_length AND internal_snake(0).x = internal_snake(i).x AND ...</pre>
3
            internal_snake(0).y = internal_snake(i).y THEN
            IF lives = 1 THEN
                game_over < TRUE;</pre>
5
                 lives \leq lives - 1;
7
                 snake\_length \leq 1;
                                      -- Reset game
8
                 lives \leq lives - 1;
                 snake_length < 1; -- Reset game</pre>
10
11
            END IF:
        END IF;
12
   END LOOP;
13
14
   -- Check for food
   IF internal_snake(0).x = food_x AND internal_snake(0).y = food_y THEN
15
16
        internal\_snake(snake\_length).x \le internal\_snake(snake\_length - 1).x;
17
        internal\_snake(snake\_length).y \le internal\_snake(snake\_length - 1).y;
        snake_length < snake_length + 1;</pre>
18
   END IF:
19
```

Code 3 manages two critical aspects of the snake game: collision detection and food consumption handling.

The first FOR loop iterates through all segments of the snake (excluding the head) to check if the head ("internal\_snake(0)") collides with any body segment ("internal\_snake(i)"). If the coordinates "x" and "y" of the head match those of a body segment and the snake has more than one segment ("i < snake\_length"), a collision is detected. If the number of lives is one, the game sets "game\_over" to TRUE, decreases the remaining lives by one, and resets the snake's length to one. If lives are greater than one, only the number of lives is decremented and the snake's length is reset to one to restart the game.

After collision checks, the code verifies if the snake's head ("internal\_snake(0)") overlaps with the food position ("food\_x" and "food\_y"). If so, the snake eats the food. The coordinates of the new tail segment ("internal\_snake(snake\_length)") are set equal to the coordinates of the previous tail segment ("internal\_snake(snake\_length - 1)"). The snake's length is incremented by one to add a new segment to the snake's tail.

This mechanism ensures proper functionality of the snake game by managing collisions with the snake's own body and food consumption, dynamically updating the snake's length and available lives.



## 2.2 Food logic

The food logic has been implemented in a dedicated VHDL file named "food.vhd" within the project folder. This file manages the random positioning of food items. The operational logic is illustrated in the flowchart depicted in Figure 4.

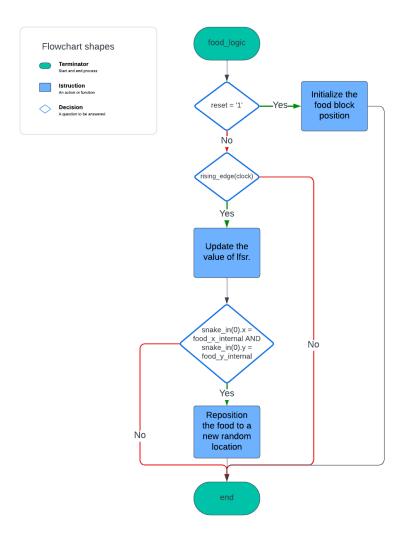


Figure 4: Food logic flowchart.

To facilitate random positioning of food, a dedicated function named "random\_number" has been implemented in the code (Code 4), as VHDL lacks built-in functions for generating random numbers. This function utilizes a Linear Feedback Shift Register (LFSR) to generate random numbers. An LFSR is a type of register where bits are cyclically shifted, and the least significant bit (LSB) is generated based on previous bits using XOR operations. This method ensures a sequence of numbers that appear random but are deterministic, meaning that initializing the register with the same initial state will produce the same sequence of numbers every time. The LFSR implementation within the "random\_number" function enables the generation of random numbers necessary for dynamic food positioning in the game.



#### Code 4: Random number generator

```
FUNCTION random_number(min_val, max_val : INTEGER; lfsr_val : STD_LOGIC_VECTOR(15 ...
       DOWNTO 0)) RETURN INTEGER IS
       VARIABLE lfsr_temp : STD_LOGIC_VECTOR(15 DOWNTO 0);
2
3
       VARIABLE random_val : INTEGER;
4
   BEGIN
         - Copy the initial LSFR value to a temporary variable
5
       lfsr_temp := lfsr_val;
6
7
        -- Perform one iteration of the LSFR
8
       lfsr_temp := lfsr_temp(14 DOWNTO 0) & (lfsr_temp(15) XOR lfsr_temp(13) XOR ...
           lfsr_temp(12) XOR lfsr_temp(10));
10
       -- Convert the LSFR value to an integer
11
       random_val := to_integer(unsigned(lfsr_temp));
12
13
       -- Limit the value within the specified range
14
15
       RETURN (random_val MOD (max_val - min_val + 1)) + min_val;
   END FUNCTION;
16
```

The parameters of the "random\_number" function are "min\_val" and "max\_val", which define the range within which we want to generate the random number, and "lfsr\_val", which is the initial value of the LFSR represented as a 16-bit vector (from 15 to 0). The local variables are "lfsr\_temp", a temporary variable that represents the current state of the LFSR during the function's execution, and "random\_val", the variable where we store the value calculated as the random number. The main instruction is the one in line 9. This instruction represents a feedback loop of the LFSR. We shift all the bits of the LFSR one position to the left (discarding the MSB) and calculate the new bit to insert into the register. The new bit is obtained through a combination of XOR operations between specific bits of the LFSR. In this specific case, bits in positions 15, 13, 12, and 10 are used to calculate the new bit. This step is illustrated more clearly in Figure 5.

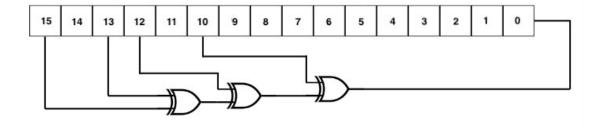


Figure 5: LFSR working principle.

## 2.3 Score module

The VHDL code named "score.vhd" is designed to manage the score display on 7-segment displays. Specifically, it uses all six 7-segment displays provided by the board to show constants represented by the letters C, F, U, and the dash, in addition to two displays dedicated to showing the score, which can range from 0 to 31. This is why two displays are required for the score.

The code features a single process named "display" that activates whenever the value of point changes. If point is between 0 and 32, it calculates the unit digit and the tens digit. It uses the "get\_display\_code" function to obtain the corresponding 7-segment codes. This approach ensures that the display management for each number is handled automatically, minimizing the lines of code and eliminating the need to manage each of the 32 displayed numbers individually.



## 2.4 Joystick module

The VHDL code "joystick.vhd" utilizes the integrated ADC in the FPGA device to acquire analog values from the X and Y axes of a joystick. These analog signals are converted into digital values and interpreted to determine the direction of joystick movement. The "unnamed" component acts as an interface between the main VHDL code and the ADC hardware, enabling monitoring of the joystick position. To accurately replicate the game's functionality, it is crucial to create this component following the detailed instructions provided in the document "ADC with DE-series boards.pdf" by Professor Dr. Eng. Martino De Carlo, available in the project folder.

The VHDL file "joystick.vhd" contains a single process that reads the analog values of the X and Y axes of the joystick ("joystickReadingX" and "joystickReadingY") and converts them into integer values. Using these data, it determines the direction of the joystick and sets the direction signal accordingly. The chosen values for "homeX" and "homeY" were carefully measured using dedicated code (Code 12) to represent the resting values on the X and Y axes. The tolerance constant indicates the sensitivity of the joystick; a lower value corresponds to higher sensitivity. Through testing, it was found that a tolerance of 400 allows smooth and precise execution of commands, as demonstrated in the video provided in the project folder.

Code 12 presents the same operational logic as the codes named "joystick.vhd" and "score.vhd" which can be viewed in the appendix of the documentation. This code is designed to read input from a joystick and display the corresponding values on seven-segment displays. Specifically, four seven-segment displays are used because a 12-bit ADC is employed, and thus the binary value read from the ADC, once converted to a decimal number, has a maximum of four digits.

The code outputs are described as follows:

- display\_r: 7-bit logic vector for displaying the rightmost digit.
- display\_mr: 7-bit logic vector for displaying the second digit from the right.
- display\_ml: 7-bit logic vector for displaying the third digit from the right.
- display\_l: 7-bit logic vector for displaying the leftmost digit.

Unlike the "joystick.vhd" code, in this case, only one channel of the ADC is used. To replicate the code's functionality, in addition to following the guide for creating the "unnamed" component, the correct pin assignments must be made by referring to the FPGA board manual. It is crucial to properly connect the joystick pins on the board, noting that the pin for analog reading is ADC IN0, as indicated on page 32 of the manual.

## 2.5 Display logic

The "hw\_image\_generator.vhd" code assumes a fundamental role in managing the graphical representation of the game. Within this code, in addition to the fundamental logic for image generation, several components are integrated that define the entire gameplay dynamics. These components include "snake\_entity" for snake management, "food" for handling food items, "score" for scoring, and "joystick" for user interaction. This structural design not only facilitates clear and modular management of the game logic but also utilizes the "hw\_image\_generator.vhd" file as a central point for effectively assigning input and output parameters to each component.

The two processes that characterize the architecture are "my\_clockDivider" and "display\_logic". The "my\_clockDivider" process plays a crucial role in the game's timing, determining, for instance, the speed of the snake's movement. The FPGA clock runs at 50 MHz and is divided to produce



a 10 Hz clock. It is important to specify that the effective result is 10 Hz because the period of "clk" out signal" is defined by both a rising and falling edge.

## Code 5: my clockDivider

```
my_clockDivider: PROCESS(clock)
1
2
  BEGIN
       IF rising_edge(clock) THEN
3
           IF count = "1001100010010110011111" THEN
4
               clk_out_signal < not clk_out_signal;
5
               count < (OTHERS => '0');
6
           ELSE
8
               count < count + '1';
           END IF;
9
      END IF;
  END PROCESS my_clockDivider;
```

The "display\_logic" process is responsible for managing the display of various components on the screen. Depending on the current situation, the appropriate components will be shown. If no lives are remaining and thus it is a game over condition, the game over graphic will be displayed. If the maximum score is reached, the victory graphic will be shown. In the absence of these two conditions, the playing field with the snake and the food will be displayed.

All the graphics created were converted using a dedicated Python script, found in the appendix, into matrices of RGB values. These matrices represent each individual pixel of the image and are commonly referred to in VHDL as "Sprites". Sprites related to the snake, food, game over screen, victory screen, and life indicators have been saved within the project package, which contains all the constants and data types used in the code.

To accurately represent the snake and food sprites, the X and Y coordinates indicate the center point of a 31x31 pixel block. Specifically, the playing field of 930x620 pixels has been divided into a grid of 20 rows and 30 columns of 31x31 pixel blocks. This division allows the snake to move across all areas of the field and ensures proper interaction with the food items.

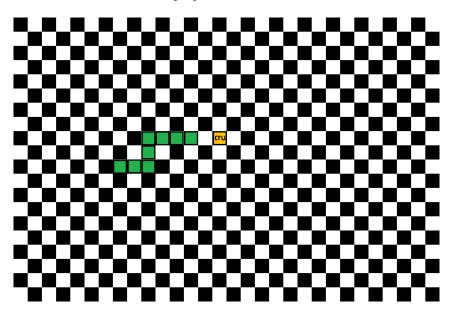


Figure 6: Game grid.



Now we will comment on the code related to the generation of the snake's graphics. The same logic applies to other components displayed on the screen, which are omitted from this documentation for simplicity to avoid redundancy.

#### Code 6: Draw snake

```
FOR i IN 0 TO max_snake_length - 1 LOOP
2
       IF i < snake_length THEN</pre>
          IF row > internal_snake(i).y - ((rect_height-1) / 2) AND row < ...</pre>
3
               internal_snake(i).y + ((rect_height-1) / 2) AND
          column \ge internal\_snake(i).x - ((rect\_width-1) / 2) AND column \le ...
4
               internal_snake(i).x + ((rect_width-1) / 2) THEN
               -- Calculate the relative coordinates within the 31x31 block
               snake_row := row - (internal_snake(i).y - ((rect_height-1) / 2));
6
7
               snake_col := column - (internal_snake(i).x - ((rect_width-1) / 2));
               -- Retrieve the pixel color from the array
9
               10
               green < snake_image(snake_row, snake_col)(7 DOWNTO 4);</pre>
11
               blue < snake_image(snake_row, snake_col)(3 DOWNTO 0);
          END IF:
12
13
      END IF;
  END LOOP;
14
```

As shown in Code 6, to display the snake on the monitor, each segment of the snake is iterated through with a FOR loop. It checks if the current row and column fall within the coordinates of the snake segment, specified by the segment's X and Y coordinates and the rectangle's dimensions ("rect\_width" and "rect\_height"). Then, the relative coordinates within the segment block are calculated. Finally, the values for the red, green, and blue components of the current pixel are set, retrieving them from the "snake image" array that contains the snake's image data.

## A VHDL codes

Code 7: hw\_image\_generator.vhd

```
-- Import IEEE standard libraries
   LIBRARY ieee;
  USE ieee.std_logic_1164.all;
3
  USE ieee.std_logic_unsigned.all;
5
   USE ieee.std_logic_arith.all;
   -- Import custom package for the snake game
8
   USE work.snake_pkg.ALL;
9
   ENTITY hw_image_generator IS
10
       PORT (
11
12
         clock
                     : IN STD_LOGIC;
                                           -- Clock signal
         clock_ADC : IN STD_LOGIC;
                                           -- ADC clock signal
13
                    : IN STD_LOGIC;
                                            -- Reset signal
         reset
14
                     : IN STD_LOGIC;
                                            -- Display enable ('1' = display time, ...
15
         disp_ena
              '0' = blanking time)
                                            -- Pixel row coordinate
16
         row
                     : IN INTEGER;
         column
                     : IN INTEGER;
                                           -- Pixel column coordinate
17
                     : OUT STD_LOGIC_VECTOR(3 DOWNTO 0) := (OTHERS => '0'); -- Red ...
         red
18
             color output
                     : OUT STD_LOGIC_VECTOR(3 DOWNTO 0) := (OTHERS => '0'); -- ...
19
            Green color output
                    : OUT STD_LOGIC_VECTOR(3 DOWNTO 0) := (OTHERS => '0'); -- Blue ...
             color output
```



```
: OUT STD_LOGIC_VECTOR (6 downto 0);
21
          display_r
          display_l
                      : OUT STD_LOGIC_VECTOR (6 downto 0);
22
                      : OUT STD_LOGIC_VECTOR (6 downto 0);
: OUT STD_LOGIC_VECTOR (6 downto 0);
          display_c
23
          display_f
24
          display_u : OUT STD_LOGIC_VECTOR (6 downto 0);
          display_d : OUT STD_LOGIC_VECTOR (6 downto 0)
26
27
       );
  END hw_image_generator;
28
29
   -- Architecture definition for the hw_image_generator entity
30
   ARCHITECTURE behavior OF hw_image_generator IS
31
32
33
         - Internal signals for clock divider process
        SIGNAL clk_out_signal : STD_LOGIC := '0';
34
                                       : STD_LOGIC_VECTOR (21 downto 0) := (OTHERS => '0');
35
        SIGNAL count
36
        -- Internal signals for components
37
        SIGNAL snake_length : INTEGER;
38
        SIGNAL lives
                                       : INTEGER;
39
        SIGNAL internal_snake : snake_array;
40
        SIGNAL food_x
                                     : INTEGER;
                                      : INTEGER;
: BOOLEAN;
        SIGNAL food_y
42
43
       SIGNAL game_over
       SIGNAL direction
                                 : STD_LOGIC_VECTOR(1 DOWNTO 0);
44
45
        -- Snake entity component declaration
46
47
        COMPONENT snake_entity
            PORT (
48
49
                clock
                                 : IN STD_LOGIC;
                                 : IN STD_LOGIC;
                reset
50
                                 : IN INTEGER;
51
                 food_x
52
                 food_y
                                  : IN INTEGER;
                                 : IN STD LOGIC VECTOR(1 DOWNTO 0);
                direction
                direction : IN SID_LOGIC_VE
snake_out : OUT snake_array;
snake_len_out : OUT INTEGER;
gameOver_out : OUT BOOLEAN;
53
54
55
56
                                : OUT INTEGER
57
                 lives_out
            );
58
       END COMPONENT;
59
        -- Food entity component declaration
61
        COMPONENT food
62
           PORT (
63
                                      : IN STD_LOGIC;
                clock
64
                                 : IN STD_LOGIC;
65
                 reset
                                 : OUT INTEGER;
                 food_x
66
                                 : OUT INTEGER;
                food_y
67
                 snake_in
                                  : IN snake_array
68
            );
69
       END COMPONENT;
70
71
        -- Score entity component declaration
72
        COMPONENT score
73
            PORT (
74
                display_r : OUT STD_LOGIC_VECTOR (6 downto 0);
75
                 display_l : OUT STD_LOGIC_VECTOR (6 downto 0);
76
                            : OUT STD_LOGIC_VECTOR (6 downto 0);
: OUT STD_LOGIC_VECTOR (6 downto 0);
                display_c
77
78
                display_f
79
                display_u
                            : OUT STD_LOGIC_VECTOR (6 downto 0);
                display_d : OUT STD_LOGIC_VECTOR (6 downto 0);
80
81
                point
                                  : IN INTEGER
82
       END COMPONENT;
83
84
        -- Joystick entity component declaration
85
```



```
COMPONENT joystick
86
 87
           PORT (
                              : IN STD_LOGIC;
 88
                 clock
                                : IN STD_LOGIC;
                 reset
 89
                 direction : OUT STD_LOGIC_VECTOR(1 DOWNTO 0)
             );
91
        END COMPONENT;
92
 93
 94
95 BEGIN
96
         -- Instantiate snake entity
97
98
         S1 : snake_entity PORT MAP (
                      => clk_out_signal,
=> reset,
=> food_x,
            clock
99
100
            reset
            food_x
101
                             => food_y,
            food v
102
103
            direction
                             => direction,
104
             snake_out
                              => internal_snake,
            snake_len_out => snake_length,
105
             gameOver_out => game_over,
lives_out => lives
107
             lives_out
108
        );
         -- Instantiate food entity
110
        S2 : food PORT MAP (
111
            clock => clk_out_signal,
112
            reset => reset,
food_x => food_x,
113
114
             food_y => food_y,
115
             snake_in => internal_snake
116
117
118
119
         -- Instantiate score entity
         S3 : score PORT MAP (
120
            display_r => display_r,
121
122
            display_l
                         => display_l,
                         => display_c,
=> display_f,
            display_c
123
            display_f
124
            display_u
                         => display_u,
             display_d => display_d,
126
                          => snake_length
127
             point
128
129
         -- Instantiate joystick entity
130
        S4 : joystick PORT MAP (
131
            clock => clock_ADC,
reset => reset,
132
133
            direction => direction
134
135
        );
136
        -- Clock divider process
137
        my_clockDivider: PROCESS(clock)
        BEGIN
139
             IF rising_edge(clock) THEN
140
                 IF count = "1001100010010110011111" THEN
                     clk_out_signal < not clk_out_signal;</pre>
142
                      count < (OTHERS => '0');
143
144
                     count < count + '1';</pre>
145
146
                 END IF;
             END IF;
147
        END PROCESS my_clockDivider;
148
149
        -- Display logic process
150
```



```
display_logic: PROCESS(disp_ena, row, column)
151
                                         : INTEGER;
152
            VARIABLE i
                          : INTE
te_row : INTEGER;
te_col : INTEGER;
153
            VARIABLE snake_row
            VARIABLE snake_col
154
                                    : INTEGER;
            VARIABLE food_row
            VARIABLE food_col
                                     : INTEGER;
156
            VARIABLE gameOver_row : INTEGER;
157
            VARIABLE gameOver_col : INTEGER;
158
                                  : INTEGER;
: INTEGER;
            VARIABLE live_row
159
160
            VARIABLE live_col
                                   : INTEGER;
            VARIABLE victory_row
161
            VARIABLE victory_col
                                    : INTEGER;
162
163
        BEGIN
            IF disp_ena = '1' THEN
                                     -- Display time
164
                 red < (OTHERS => '0');
165
                 green \leq (OTHERS => '0');
166
                blue < (OTHERS => '0');
167
168
169
                 IF game_over = FALSE THEN
170
                     IF snake_length = max_snake_length THEN
171
172
173
                         -- Draw victory screen
                         IF column \geq game_left + (game_width/2) - 156 AND column < ...
174
                             game_left + (game_width/2) + 156 AND
175
                             row > game_top + (game_height/2) - 20 AND row < game_top ...
                                  + (game_height/2) + 20 THEN
                             -- Calculate relative coordinates within the block
176
177
                             victory_row := row - (game_top + (game_height/2) - 20);
                             victory_col := column - (game_left + (game_width/2) - 156);
178
                             -- Get the pixel color from the array
179
180
                             \texttt{green} \, \leq \, \texttt{victory\_image(victory\_row, victory\_col)(7 DOWNTO 4);}
181
182
                             blue < victory_image(victory_row, victory_col)(3 DOWNTO 0);</pre>
                         END IF;
183
184
                     ELSE
186
                         -- Draw snake
187
                         FOR i IN 0 TO max_snake_length - 1 LOOP
188
                             IF i < snake_length THEN</pre>
189
                                 IF row > internal_snake(i).y - ((rect_height-1) / 2) ...
190
                                      AND row < internal_snake(i).y + ((rect_height-1) ...
                                      / 2) AND
191
                                      column \ge internal\_snake(i).x - ((rect\_width-1) / ...
                                          2) AND column < internal_snake(i).x + ...
                                          ((rect_width-1) / 2) THEN
                                       -- Calculate relative coordinates within the ...
192
                                           31x31 block
193
                                      snake_row := row - (internal_snake(i).y - ...
                                          ((rect_height-1) / 2));
                                      snake_col := column - (internal_snake(i).x - ...
194
                                          ((rect_width-1) / 2));
                                      -- Get the pixel color from the array
195
                                      red \le snake\_image(snake\_row, snake\_col)(11 ...
196
                                         DOWNTO 8);
197
                                      green < snake_image(snake_row, snake_col)(7 ...
                                         DOWNTO 4);
198
                                      blue < snake_image(snake_row, snake_col)(3 ...
                                          DOWNTO 0);
                                 END IF;
199
                             END IF;
200
                         END LOOP:
201
202
                         -- Draw food
203
```



```
IF row \geq food_y - ((rect_height-1) / 2) AND row \leq food_y + ...
204
                             ((rect_height-1) / 2) AND
                             column \ge food_x - ((rect_width-1) / 2) AND column \le ...
205
                                 food_x + ((rect_width-1) / 2) THEN
                              -- Calculate relative coordinates within the 31x31 block
206
                             food_row := row - (food_y - ((rect_height-1) / 2));
207
                             food\_col := column - (food\_x - ((rect\_width-1) / 2));
208
                              -- Get the pixel color from the array
209
                             red \le food_image(food_row, food_col)(11 DOWNTO 8);
210
211
                             green < food_image(food_row, food_col)(7 DOWNTO 4);</pre>
212
                             blue < food_image(food_row, food_col)(3 DOWNTO 0);</pre>
                         END IF;
213
214
                         -- Draw gray lines for game field border
215
216
                         IF (row \geq game_top AND row \leq game_bottom AND (column = ...
                              game_left OR column = game_right)) OR
                              (column \geq game_left AND column \leq game_right AND (row = ...
217
                                 game_top OR row = game_bottom)) THEN
                             red < "1000";
218
                             green < "1000";
219
                             blue < "1000";
220
                         END IF;
221
222
                          -- Draw game lives
223
                         TF lives = 1 THEN
224
                             IF column > game_left+(game_width/2)-15 AND column < ...</pre>
225
                                 game_left+(game_width/2)+15 AND
                                 row \ge game\_top-41 \ AND \ row \le game\_top-10 \ THEN
226
227
                                  -- Calculate relative coordinates within the block
                                 live_row := row - (game_top-41);
228
                                 live_col := column - (game_left+(game_width/2)-15);
229
230
                                  -- Get the pixel color from the array
                                 231
232
                                 green < live_image(live_row, live_col)(7 DOWNTO 4);</pre>
                                 blue < live_image(live_row, live_col)(3 DOWNTO 0);</pre>
233
                             END IF:
234
                         ELSIF lives = 2 THEN
                             IF column > game_left+(game_width/2)-30 AND column < ...
236
                                  game_left+(game_width/2)+31 AND
                                  row \ge game\_top-41 AND row \le game\_top-10 THEN
237
                                  -- Calculate relative coordinates within the block
238
239
                                 live_row := row - (game_top-41);
                                 live_col := column - (game_left+(game_width/2)-30);
240
                                  -- Get the pixel color from the array
241
242
                                 red < live_image(live_row, live_col)(11 DOWNTO 8);</pre>
                                 green \leq live\_image(live\_row, live\_col)(7 DOWNTO 4);
243
244
                                 blue < live_image(live_row, live_col)(3 DOWNTO 0);</pre>
245
                         ELSE
246
247
                              IF column \geq game_left+(game_width/2)-46 AND column \leq ...
                                  game_left+(game_width/2)+46 AND
                                  \verb"row" \geq \verb"game_top-rect_width-10" AND "row" \leq \verb"game_top-10" THEN"
248
                                  -- Calculate relative coordinates within the block
249
                                  live_row := row - (game_top-41);
250
                                 live_col := column - (game_left+(game_width/2)-46);
251
                                  -- Get the pixel color from the array
252
                                 253
254
255
                                 blue < live_image(live_row, live_col)(3 DOWNTO 0);</pre>
                             END IF:
256
257
                         END IF:
                     END IF;
258
259
                ELSE
260
261
```



```
-- Draw game over screen
262
                   IF column > game_left + (game_width/2) - 184 AND column < ...
263
                       game_left + (game_width/2) + 184 AND
                       row \geq game_top + (game_height/2) - 20 AND row < game_top + ...
264
                           (game_height/2) + 20 THEN
                        -- Calculate relative coordinates within the block
265
                       gameOver_row := row - (game_top + (game_height/2) - 20);
266
                       gameOver_col := column - (game_left + (game_width/2) - 184);
267
                       -- Get the pixel color from the array
268
269
                       270
                       green < gameOver_image(gameOver_row, gameOver_col)(7 DOWNTO 4);</pre>
                       blue < gameOver_image(gameOver_row, gameOver_col)(3 DOWNTO 0);</pre>
271
272
                   END IF;
273
               END IF:
274
275
           ELSE -- Blanking time
276
               red \leq (OTHERS => '0');
277
278
               green < (OTHERS => '0');
               blue < (OTHERS => '0');
279
           END IF;
280
281
       END PROCESS display_logic;
282
284 END behavior:
```

### Code 8: snake entity.vhd

```
1 LIBRARY ieee;
2 USE ieee.std_logic_1164.all;
3 USE ieee.std_logic_unsigned.all;
   USE ieee.std_logic_arith.all;
4
5 USE work.snake_pkg.ALL;
7 ENTITY snake_entity IS
8
       PORT (
           clock
                           : IN STD_LOGIC;
9
                           : IN STD_LOGIC;
           reset.
10
           food_x
                           : IN INTEGER;
11
           food_y
                           : IN INTEGER;
12
                           : IN STD_LOGIC_VECTOR(1 DOWNTO 0);
13
           direction
14
           snake_out
                           : OUT snake_array;
           snake_len_out : OUT INTEGER;
15
16
           gameOver_out : OUT BOOLEAN;
           lives_out
                           : OUT INTEGER
17
       );
18
   END snake_entity;
20
   ARCHITECTURE behavior OF snake_entity IS
21
22
        - Internal signals
23
24
       SIGNAL internal_snake : snake_array := (
           OTHERS \Rightarrow (x \Rightarrow game_left + (rect_width*10)+((rect_width*1) / 2), y \Rightarrow ...
               game_top + (rect_width*10)+((rect_height+1) / 2))
       ); -- Initial position of the snake's head
26
       SIGNAL snake_length : INTEGER := 1; -- Initial snake length
27
       SIGNAL current_direction : STD_LOGIC_VECTOR(1 DOWNTO 0) := "00"; -- Initial ...
28
           direction: 00 = Up
       SIGNAL game_over : BOOLEAN;
29
       SIGNAL lives : INTEGER := 3; -- Initial number of lives
30
31
32 BEGIN
33
       -- Process for handling snake movement and game logic
34
       snake_logic: PROCESS(clock, reset)
```



```
VARIABLE i : INTEGER:
35
         BEGIN
36
              IF reset = '1' THEN
37
                    internal\_snake(0).x \le game\_left + (rect\_width*10) + ((rect\_width+1) / 2);
38
                    internal\_snake(0).y \le game\_top + (rect\_width*10) + ((rect\_height+1) / 2);
                    snake length < 1;</pre>
40
41
                    current_direction < "00";</pre>
                    lives \leq 3;
42
                   game_over < FALSE;</pre>
43
44
              ELSIF rising_edge(clock) AND snake_length < max_snake_length AND ...</pre>
45
                    game_over = FALSE THEN
46
                    -- Update direction based on joystick commands
47
                    IF direction = "10" THEN
48
                         CASE current_direction IS
49
                              WHEN "00" => current_direction < "10"; -- Up -> Left
50
                              WHEN "10" => current_direction \leq "10"; -- Left -> Left
WHEN "11" => current_direction \leq "10"; -- Down -> Left
WHEN "01" => current_direction \leq "01"; -- Right -> Right
51
52
53
                              WHEN OTHERS => NULL;
                         END CASE;
55
                    ELSIF direction = "01" THEN
56
                         CASE current_direction IS
57
                              WHEN "00" => current_direction \le "01"; -- Up -> Right
WHEN "01" => current_direction \le "01"; -- Right -> Right
WHEN "11" => current_direction \le "01"; -- Down -> Right
58
59
60
                              WHEN "10" => current_direction < "10"; -- Left -> Left
61
                              WHEN OTHERS => NULL;
62
                         END CASE;
63
                    ELSIF direction = "00" THEN
64
65
                         CASE current_direction IS
                              WHEN "00" => current_direction < "00"; -- Up -> Up
66
                              WHEN "00" -> current_direction \( \) "00"; -- Right -> Up

WHEN "11" -> current_direction \( \) "11"; -- Down -> Down

WHEN "10" -> current_direction \( \) "00"; -- Left -> Up
67
68
69
70
                              WHEN OTHERS => NULL;
                         END CASE;
71
                    ELSIF direction = "11" THEN
72
                         CASE current_direction IS
73
                              WHEN "00" => current_direction \leq "00"; -- Up -> Up
WHEN "01" => current_direction \leq "11"; -- Right -> Down
WHEN "11" => current_direction \leq "11"; -- Down -> Down
74
75
76
                              WHEN "10" => current_direction \leq "11"; -- Left -> Down
77
78
                              WHEN OTHERS => NULL;
                         END CASE;
79
80
                    END IF:
81
                    -- Move the snake
82
                    FOR i IN max_snake_length - 1 DOWNTO 1 LOOP
83
84
                         IF i < snake_length THEN</pre>
                              internal_snake(i) < internal_snake(i - 1);</pre>
85
                         END IF;
86
                    END LOOP;
87
88
                    -- Use current_direction to move the snake
                    CASE current direction IS
90
                         WHEN "00" => IF internal_snake(0).y - rect_height > game_top THEN ...
91
                              internal_snake(0).y < internal_snake(0).y - rect_height; END ...</pre>
                              IF: -- Up
                         WHEN "10" => IF internal_snake(0).x - rect_width > game_left THEN ...
92
                              internal_snake(0).x < internal_snake(0).x - rect_width; END ...</pre>
                               TF: -- Left
                         WHEN "11" => IF internal_snake(0).y + rect_height < game_bottom ...</pre>
93
                              THEN internal_snake(0).y < internal_snake(0).y + rect_height; ...
```



```
END IF; -- Down
94
                      WHEN "01" => IF internal_snake(0).x + rect_width < game_right ...</pre>
                           THEN internal_snake(0).x < internal_snake(0).x + rect_width; ...</pre>
                          END IF; -- Right
                      WHEN OTHERS => NULL;
                 END CASE;
96
97
                  -- Check for collisions with the snake itself
98
                 FOR i IN 1 TO max_snake_length - 1 LOOP
99
100
                      IF i < snake_length AND internal_snake(0).x = internal_snake(i).x ...</pre>
                          AND internal_snake(0).y = internal_snake(i).y THEN
                          IF lives = 1 THEN
101
102
                               game_over < TRUE;</pre>
                               lives \leq lives - 1;
103
                               snake\_length \leq 1; -- Reset game
104
                          ELSE
105
                               lives \leq lives - 1;
106
107
                               snake\_length \le 1; -- Reset game
108
                      END IF;
109
                 END LOOP;
111
                  -- Check if the snake has eaten the food
112
                 IF internal_snake(0).x = food_x AND internal_snake(0).y = food_y ...
113
                      THEN
114
                      internal\_snake(snake\_length).x \le internal\_snake(snake\_length - 1).x;
                      internal_snake(snake_length).y < internal_snake(snake_length - 1).y;</pre>
115
                      snake_length < snake_length + 1;</pre>
116
117
                 END IF;
118
119
             END IF;
120
        END PROCESS snake_logic;
121
122
        -- Output assignments
123
        gameOver_out < game_over;</pre>
        lives_out < lives;</pre>
124
125
        snake_len_out < snake_length;</pre>
        snake_out < internal_snake;</pre>
126
127
128 END behavior;
```

## Code 9: food.vhd

```
1 LIBRARY ieee;
  USE ieee.std_logic_1164.all;
3 use ieee.numeric std.all;
4 USE work.snake_pkg.ALL;
5
   ENTITY food IS
6
       PORT (
                          : IN STD_LOGIC;
           clock
8
9
           reset
                           : IN STD LOGIC;
           food_x
                           : OUT INTEGER;
10
                           : OUT INTEGER;
           food_y
11
12
           snake_in
                           : IN snake_array
       );
13
14 END food;
15
   ARCHITECTURE behavior OF food IS
16
17
         - Internal signals
18
       SIGNAL food_x_internal : INTEGER := game_left + (rect_width*10) + ...
19
            ((rect_width+1) / 2);
20
       SIGNAL food_y_internal : INTEGER := game_top + (rect_width*10) + ...
```



```
((rect height+1) / 2);
21
       SIGNAL lfsr : STD_LOGIC_VECTOR(15 DOWNTO 0) := X"ACE1"; -- Initial value of ...
22
           the Linear Feedback Shift Register (LFSR)
         - Function to generate random numbers using the LFSR
24
25
       FUNCTION random_number(min_val, max_val : INTEGER; lfsr_val : ...
            STD_LOGIC_VECTOR(15 DOWNTO 0)) RETURN INTEGER IS
           VARIABLE lfsr_temp : STD_LOGIC_VECTOR(15 DOWNTO 0);
26
27
           VARIABLE random_val : INTEGER;
       BEGIN
28
           lfsr_temp := lfsr_val;
29
30
            -- Perform one iteration of the LFSR
31
           lfsr_temp := lfsr_temp(14 DOWNTO 0) & (lfsr_temp(15) XOR lfsr_temp(13) ...
32
                XOR lfsr_temp(12) XOR lfsr_temp(10));
33
           -- Convert the LFSR value to an integer
34
           random_val := to_integer(unsigned(lfsr_temp));
35
36
           -- Limit the value within the specified range
37
           RETURN (random_val MOD (max_val - min_val + 1)) + min_val;
38
       END FUNCTION;
39
40
   BEGIN
41
42
        -- Game logic process
43
       food_logic: PROCESS(reset, clock)
44
           VARIABLE new_food_col, new_food_row : INTEGER;
45
       BEGIN
46
           IF reset = '1' THEN
47
48
                -- Reset the food position to the initial coordinates
                food_x_internal < game_left + (rect_width*10) + ((rect_width+1) / 2);</pre>
49
50
                food_y_internal < game_top + (rect_width*10) + ((rect_height+1) / 2);</pre>
51
           ELSIF rising_edge(clock) THEN
52
                -- Update the LFSR
                lfsr \leq lfsr(14 DOWNTO 0) & (lfsr(15) XOR lfsr(13) XOR lfsr(12) XOR ...
54
                    lfsr(10));
55
                -- Check if the snake has eaten the food
56
57
                IF snake_in(0).x = food_x_internal AND snake_in(0).y = ...
                    food_y_internal THEN
                    -- Reposition the food to a new random location
58
59
                    new_food_row := random_number(0, 19, 1fsr);
                    new_food_col := random_number(0, 29, 1fsr);
60
61
                    food\_x\_internal \leq game\_left + new\_food\_col * rect\_width + \dots
                         ((rect_width+1) / 2);
                    food_y_internal < game_top + new_food_row * rect_height + ...</pre>
62
                         ((rect_height+1) / 2);
                END IF;
63
           END IF;
64
       END PROCESS food_logic;
66
       -- Output assignments
67
       food_x < food_x_internal;</pre>
       food_y < food_y_internal;</pre>
69
70
71 END behavior;
```

## Code 10: score.vhd

```
1 -- Import IEEE standard libraries
2 LIBRARY ieee;
```



```
3 USE ieee.std_logic_1164.all;
4 USE ieee.std_logic_unsigned.all;
5 USE ieee.std_logic_arith.all;
   -- Entity declaration for the score display
8 ENTITY score IS
9
       PORT (
           display_r
                       : OUT STD_LOGIC_VECTOR (6 downto 0);
10
                       : OUT STD_LOGIC_VECTOR (6 downto 0);
: OUT STD_LOGIC_VECTOR (6 downto 0);
            display_l
11
12
            display_c
                       : OUT STD_LOGIC_VECTOR (6 downto 0);
           display_f
13
                       : OUT STD_LOGIC_VECTOR (6 downto 0);
           display_u
14
                        : OUT STD_LOGIC_VECTOR (6 downto 0);
15
           display_d
                        : IN INTEGER
           point
16
17
       );
  END score;
18
19
  -- Architecture definition for the score entity
20
21 ARCHITECTURE behavior OF score IS
22
       -- Type declaration for the seven-segment display codes
       TYPE segments_type IS ARRAY (0 TO 13) OF STD_LOGIC_VECTOR(6 DOWNTO 0);
24
25
       -- Constant array holding the seven-segment display codes for digits 0-9, \dots
26
            'C', 'F', 'U', and '-
27
       CONSTANT segments : segments_type := (
            "1000000", -- 0
28
            "1111001", -- 1
29
           "0100100", -- 2
30
           "0110000", -- 3
31
            "0011001", -- 4
32
33
            "0010010", -- 5
           "0000010", -- 6
34
           "1111000", -- 7
35
           "0000000", -- 8
36
            "0010000", -- 9
37
           "1000110", -- C
            "0001110", -- F
39
            "1000001", -- U
40
            "0111111" -- dash
41
42
       ):
43
       -- Function to get the display code for a given value
44
       FUNCTION get_display_code(value : INTEGER) RETURN STD_LOGIC_VECTOR IS
45
46
           BEGIN
                IF value \geq 0 AND value \leq 9 THEN
47
                    RETURN segments (value);
48
49
                    RETURN "11111111"; -- Display blank
50
51
                END IF;
      END get_display_code;
52
53
   BEGIN
      -- Process to update the right and left display segments based on the score \dots
55
          (point)
       display : PROCESS(point)
       BEGIN
57
           IF point \geq 0 AND point \leq 32 THEN
58
               display_r < get_display_code((point-1) MOD 10);</pre>
59
            display_1 < get_display_code((point-1) / 10);</pre>
60
61
            ELSE
            display_r < "11111111";
62
            display_l ≤ "11111111"; -- Display blank or error
63
64
         END IF;
     END PROCESS display;
65
```



#### Code 11: joystick.vhd

```
1 library ieee;
use ieee.std_logic_1164.all;
3 use ieee.numeric_std.all;
4
5 ENTITY joystick IS
       PORT (
7
         clock : IN STD_LOGIC;
8
                            : IN STD_LOGIC;
           reset
           direction : OUT STD_LOGIC_VECTOR(1 DOWNTO 0)
10
11
12
13 END joystick;
14
15 ARCHITECTURE behave OF joystick IS
16
        -- Declaration of component 'unnamed'
17
       COMPONENT unnamed IS
18
19
       port (
                                                          := '0'; --
20
           CLOCK : in std_logic
           CHO : out std_logic_vector(11 downto 0); -- readings.CHO
21
           CH1 : out std_logic_vector(11 downto 0);
           CH2 : out std_logic_vector(11 downto 0);
CH3 : out std_logic_vector(11 downto 0);
CH4 : out std_logic_vector(11 downto 0);
                                                                               .CH2
23
                                                                               .СН3
24
                                                                               .CH4
                 : out std_logic_vector(11 downto 0);
: out std_logic_vector(11 downto 0);
           CH5
                                                                               .CH5
26
27
           CH6
                                                                               .CH6
           CH7 : out std_logic_vector(11 downto 0);
                                                                               .CH7
28
                                                          := '0' --
           RESET : in std_logic
                                                                        reset.reset
29
30
       END COMPONENT;
31
32
33
        -- Signals for joystick readings and internal processing
       SIGNAL joystickReadingX, joystickReadingY, c2, c3, c4, c5, c6, c7 : ...
34
            STD_LOGIC_VECTOR(11 downto 0);
        SIGNAL readX : INTEGER;
35
       SIGNAL readY : INTEGER;
36
37
         - Constants defining joystick behavior
38
       CONSTANT tolerance: INTEGER: = 400; -- Tolerance value for joystick movement ...
39
           detection
       CONSTANT homeX : INTEGER := 2008; -- Center X position of joystick
40
       CONSTANT homey: INTEGER := 1978; -- Center Y position of joystick
41
       BEGIN
43
44
            -- Instantiate the unnamed component
45
            A0 : unnamed PORT MAP (
46
47
                 CLOCK => clock,
                CHO => joystickReadingX,
48
                CH1 => joystickReadingY,
49
                CH2 \Rightarrow c2,
50
                CH3 \Rightarrow c3,
51
52
                CH4 \Rightarrow C4
                CH5 \Rightarrow c5,
53
```



```
CH6 => c6.
54
55
                CH7 \Rightarrow c7
                RESET => reset
56
57
            );
            -- Process for joystick direction detection
59
            my_process: PROCESS(clock)
60
61
                IF rising_edge(clock) THEN
62
63
                     -- Convert joystick analog readings to integers
                     readX < to_integer(unsigned(joystickReadingX));</pre>
64
                    readY < to_integer(unsigned(joystickReadingY));</pre>
65
66
                     -- Determine joystick direction based on analog readings relative ...
67
                         to home position
                IF readX > homeX+tolerance THEN -- Right
68
                        direction ≤ "01";
69
                ELSIF readX < homeX-tolerance THEN -- Left</pre>
70
71
                   direction ≤ "10";
                ELSIF readY > homeY+tolerance THEN -- Down
72
                   direction ≤ "11";
73
                ELSIF readY < homeY-tolerance THEN -- Up
74
                   direction < "00";
75
                END IF;
76
77
78
            END IF:
       END PROCESS my_process;
79
       END behave;
80
```

Code 12: Joystick input to seven segment display converter

```
library ieee;
  use ieee.std_logic_1164.all;
2
  use ieee.numeric_std.all;
4
   ENTITY joystick IS
5
       PORT (
6
           clock, reset: IN STD_LOGIC;
7
           display_r : OUT STD_LOGIC_VECTOR(6 downto 0);
8
           display_mr : OUT STD_LOGIC_VECTOR(6 downto 0);
9
           display_ml : OUT STD_LOGIC_VECTOR(6 downto 0);
10
11
           display_1 : OUT STD_LOGIC_VECTOR(6 downto 0)
       );
12
13
  END joystick;
14
   ARCHITECTURE behave OF joystick IS
15
       TYPE segments_type IS ARRAY (0 TO 9) OF STD_LOGIC_VECTOR(6 DOWNTO 0);
16
17
18
       CONSTANT segments : segments_type := (
           "1000000", -- 0
           "1111001", -- 1
20
           "0100100", -- 2
21
           "0110000", -- 3
           "0011001", -- 4
23
           "0010010", -- 5
24
           "0000010", -- 6
25
           "1111000", -- 7
26
           "0000000", -- 8
27
           "0010000" -- 9
28
29
       );
30
       FUNCTION get_display_code(value : INTEGER) RETURN STD_LOGIC_VECTOR IS
31
32
33
           IF value \geq 0 AND value \leq 9 THEN
```



```
RETURN segments (value);
34
35
                  RETURN "11111111";
36
             END IF:
37
        END get_display_code;
39
        COMPONENT unnamed IS
40
41
             PORT (
                  CLOCK : in std_logic := '0';
42
43
                  CH0
                        : out std_logic_vector(11 downto 0);
                         : out std_logic_vector(11 downto 0);
44
                        : out std_logic_vector(11 downto 0);
                  CH2
45
46
                  СНЗ
                         : out std_logic_vector(11 downto 0);
                        : out std_logic_vector(11 downto 0);
                  CH4
47
48
                  CH5
                        : out std_logic_vector(11 downto 0);
                  CH6
                        : out std_logic_vector(11 downto 0);
49
                       : out std_logic_vector(11 downto 0);
                  CH7
50
                  RESET : in std_logic := '0'
51
             );
52
        END COMPONENT;
53
        SIGNAL joystickReading,c1,c2,c3,c4,c5,c6,c7 : STD_LOGIC_VECTOR(11 downto 0);
55
56
        SIGNAL point : INTEGER;
57
   BEGIN
58
59
        A0 : unnamed PORT MAP (
            CLOCK => clock,
60
             CH0 => joystickReading,
61
             CH1 \Rightarrow c1,
62
            CH2 \Rightarrow c2,
63
             CH3 => c3,
64
65
             CH4 \Rightarrow c4,
             CH5 => c5.
66
67
             CH6 \Rightarrow C6,
             CH7 \Rightarrow c7,
68
             RESET => reset
69
70
71
        display : PROCESS(clock)
72
73
             point < to_integer(unsigned(joystickReading));</pre>
74
75
             display_r < get_display_code((point MOD 10));</pre>
             display_mr < get_display_code((point / 10) MOD 10);</pre>
76
             \label{eq:display_ml} {\tt display_ml} \, \leq \, {\tt get\_display\_code} \, (\, {\tt (point} \, / \, 100) \, \, {\tt MOD} \, \, 10) \, ;
77
78
             display_1 < get_display_code((point / 1000) MOD 10);</pre>
        END PROCESS display;
79
80 END behave:
```

# B Python Code

Code 13: Code used for creating matrices of RGB values.

```
1 from PIL import Image # Importing the required module from the Python Imaging ...
    Library (PIL)
2 import numpy as np # Importing NumPy for array manipulation capabilities
3
4 # Load the image
5 img = Image.open('./image.png').convert('RGBA')
6
7 # Resize the image to 31x31 pixels
8 img = img.resize((31, 31))
```



```
9
10 # Extract pixel data
pixel_data = np.array(img)
12
13 # Create the VHDL color array
vhdl_array = []
15 for row in pixel_data:
       vhdl_row = []
16
       for pixel in row:
17
18
           r, g, b, a = pixel
           # Use only fully opaque pixels (a == 255)
19
           if a == 255:
20
21
               vhdl_pixel = (r >> 4, g >> 4, b >> 4) \# Convert to 4-bit per color ...
                  channel
           else:
22
23
               vhdl_pixel = (0, 0, 0) # Black color for transparent pixels
           vhdl_row.append(vhdl_pixel)
24
      vhdl_array.append(vhdl_row)
25
26
27 # Generate VHDL code for the array
vhdl_code = "CONSTANT image : image_type := (\n"
29 for row in vhdl_array:
       vhdl_code += " ("
vhdl_code += ", ".join(f"X\"{r:01X}{g:01X}{b:01X}\"" for r, g, b in row)
30
31
      vhdl_code += "), \n'
32
vhdl_code = vhdl_code.rstrip(",\n") + "\n);"
34
35 # Save the VHDL code to a file
36 with open("./image.vhdl", "w") as f:
      f.write(vhdl_code)
37
```